



High-performance HDMI[™] Signal Switch w/ Integrated Side-band Signal Support

Features

- → 4-Differential Channel 2:1 Mux/DeMux + 2-Channel 2:1 Mux/DeMux
- → Deep ColorTM Support
- → Data Rate: 4.0Gbps for high data channels
- → Clock rate max support @ 340 MHz
- → Supports both AC coupled and DC coupled signals
- → Switching speed: 4ns
- → Isolation: -40dB@1GHz for high-speed channels
- → Integrated ESD protection; HBM ESD: 2KV per JEDEC Standard
- → High-speed data bits; Contact Discharge ESD: 2KV per IEC61000-4-2 standard
- → Side band signal bits: 2kV HBM
- Low bit-to-bit skew →
- Enable/Disable time: 9ns →
- → Bidirectional
- → Packaging (Pb-free & Green): - 48-pin BQSOP (B)

Block Diagram



Description

Diodes' PI3HDMI[™] series of switch circuits are targeted for high-resolution video networks that are based on DVI/HDMITM standards, and TMDS signal processing.

The PI3HDMI1210-A is a 2-to-1 HDMITM Mux/DeMux Switch. The device multiplexes differential signals to one of two corresponding HDMITM inputs. The switch is bidirectional and offers little or no attenuation of the high-speed signals at the outputs. It is designed for low bit-to-bit skew and high channel-to-channel noise isolation.

The maximum DVI/HDMI[™] data rate of 4.0Gbps provides the resolution required by the next generation HDTV and PC graphics. Three differential channels are used for data (video signals for DVI or audio/video signals for HDMITM), and one differential channel is used for Clock for decoding the TMDS signals at the outputs.

PI3HDMI1210-A was designed specifically to meet ATC-'sink' requirements. Therefore, Diodes Incorporated recommends locating this switch at the sink to switch between multiple sources.

V_{DD}33 [D0+A 48 CPE D0-A 2 47 D0+ 3 46 D1+A 45 D0- [4 D1-A D1+Γ 5 44 D2+A D1- [6 D2-A 43 V_{DD}33 [CLK+A 42 SEL 8 41 CLK-A D2+[9 40 $V_{DD}33$ D2- Г 10 39 D0+B CLK+ 11 D0-B 38 CLK- [12 D1+B 37 13 GND [D1-B 36 DD 33 C 14 D2+B 35 15 D2-B 34 16 CLK+B CPE 33 17 CLK-B GND [32 18 A0 [31 $V_{DD}33$ 19 GND E 30] GND 20 A1 [29 0B1 IN [21 28 0B2 V_{DD}50 <u></u> 22 27 **7** 1B1 EN C 23 26 1B2 CP 24 25 GND

Pin Description

1





Pin Description

Pin Name	Description	
V _{DD} 33	3.3V Power supply	
СР	Tie this pin to GND via 0.1µF capacitor for optimal operation	
V _{DD} 50	5.0V Power supply	
Dx	High Speed TMDS signal I/O	
DxA, DxB	High Speed TMDS signal I/O	
SEL	High Speed Signal Control	
IN	Side Band Signal Control	
Ax, xB1, xB2	Side Band Signal I/O	
GND	GND pin	
EN\	Global Enable for Side Band Signals	
CLK, CLKA/B	TMDS CLK signal I/O	

Truth Table

SEL(pin8)	Function ⁽¹⁾
L	$CLK \rightarrow CLKA$
L	$D_x \rightarrow D_x A$
Н	$D_x \rightarrow D_x B$
Н	$CLK \rightarrow CLKB$

EN(pin23)	IN(pin21)	Function ⁽²⁾
L	L	$A_n \rightarrow {}_nB_1$
L	Н	$A_n \rightarrow {}_nB_2$
Н	Х	$A_n = B_1 = B_2 = H_1 - Z$

Note: 1) x=0,1,2,3 2) n=0,1





Maximum Ratings

(Above which useful life may be impaired. For user guidelines, not tested.)

65°C to +150°C
0.5V to 5.5V
20mA
0.5W
-0.3V to 4.0V
0.35V to 6.0V

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Power Supply Characteristics

Paramenter	Description	Min.	Max.	Units
VDD ³³	3.3V Power Supply	3.0	3.6	V
V DD ⁵⁰	5.0 Power Supply	4.2	5.5	V

DC Electrical Characteristics for Switching over Operating Range

 $(T_A = -40^{\circ}C \text{ to } +85^{\circ}C \text{ for high speed signals only})$ Paramenter Description **Test Conditions** Min. Typ⁽²⁾ Max. Units VIHSEI⁽³⁾ Guaranteed HIGH level 2.8 Input HIGH Voltage $V_{IL SEL}^{(3)}$ Input LOW Voltage Guaranteed LOW level 1.3 2.0V Clamp Diode Voltage 0.9 VIKHS V_{DD}=Max., I_{IN}=-18mA

Notes:

1 For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.

2. Typical values are at $T_A = 25^{\circ}C$ ambient and maximum loading.

3. VIHSEL + VILSEL are for SEL input only (pin 8)

DC Electrical Characteristics for Switching over Operating Range

 $(T_A = -40^{\circ}C \text{ to } +85^{\circ}C)$ for side band signals only

Paramenter	Description	Test Conditions	Min.	Typ ⁽²⁾	Max.	Units
V _{IHIN} ⁽³⁾	Input HIGH Voltage	Guaranteed HIGH level	2.1			
V _{ILIN} ⁽³⁾	Input LOW Voltage	Guaranteed LOW level			0.8	V
V _{IKSB} ⁽⁴⁾	Clamp Diode Voltage	V _{DD} =Max., I _{IN} =-18mA			-0.5	
IIH	Input HIGH Current	V _{DD} =Max., V _{IN} =V _{DD}			±5	
IIL	Input LOW Current	V _{DD} =Max., V _{IN} =GND			±5	μA

Notes:

For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type. 1.

Typical values are at $T_A = 25^{\circ}C$ ambient and maximum loading. 2.

 $V_{IHIN} + V_{ILIN}$ are for IN input only. 3.





Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
I _{CC}	Quiescent Power Supply Current	$V_{DD} = Max., V_{IN} = V_{DD}$ or GND		11	15	mA

Notes:

For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type. 1.

2. Typical values are at $T_A = 25^{\circ}C$ ambient and maximum loading.

Dynamic Electrical Characteristics Over the Operating Range (T_A = -40° to +85°C)

Parameter	Description	Test Conditions	Min.	Typ. ⁽³⁾	Max.	Units
X _{TALKHS} ⁽¹⁾	Crosstalk	See Fig. 1 for Measurement Setup, f = 1.65Gbps		-30		
X _{TALKSB} ⁽²⁾		f = 10MHz		-62		dB
O _{IRR_{HS}⁽¹⁾}	OFF Isolation	See Fig. 2 for Measurement Setup, f = 1.65Gbps		-40		
O _{IRRSB} ⁽²⁾		10MHz		-67		
BW _{HS} ⁽¹⁾ DATA	-3dB Bandwith for HS data			2.3		GHz
		@1.65 Gbps (8-bit deep color)		87		
		@2.0625 Gbps (10-bit deep color)		-0.96		
IL _{HS} ⁽¹⁾	IL _{HS} ⁽¹⁾ Insertion Loss	@2.475 Gbps (12-bit deep color)		-1.18		dB
		@3.3 Gbps (16-bit deep color)		-1.84		
BW _{CLOCK} ⁽⁴⁾	-3dB Bandwidth for CLK			1.7		GHz

Notes:

High-speed signal path only 1.

Side-band signal path only 2.

Typical values are @ $T_A = 25^{\circ}C$ ambient 3.

BW measured on CLK pins only (11, 12, 32, 33, 41 & 42) 4.

Switching Characteristics

 $(T_A = -40^\circ \text{ to } +85^\circ \text{C}, V_{DD} 33 = 3.3 \text{V} \pm 10\%, V_{DD} 50 = 5.0 \text{V} \pm 10\%$

Paramenter	Description	Min.	Typ. ⁽²⁾	Max.	Units
tpZH, tpZL	Line Enable Time - SEL to A_N , B_N	0.5		8.0	nc
tp _{HZ} , tp _{LZ}	Line Disable Time - SEL to A _N , B _N	0.5		4.0	ns

Notes:

For measurement setup, please see "Test Circuit For Electrical Characteristic" on page 6, and "Switching waveforms" on page 7 1.







Fig 1. Crosstalk Setup



Fig 2. Off-isolation setup







Fig 3. Crosstalk for High-speed data signals only



Fig 4. Off Isolation for high-speed data signals only







Fig 5. Insertion loss for high-speed data signals only





Test Circuit for Electrical Characteristics⁽¹⁾



Notes:

- 1.
- C_L = Load capacitance: includes jig and probe capacitance. R_T = Termination resistance: should be equal to Z_{OUT} of the Pulse Generator 2.
- Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. 3. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- 4. All input impulses are supplied by generators having the following characteristics: PRR \leq MHz, $Z_0 = 50\Omega$, $t_R \leq 2.5$ ns, $t_F \leq 2.5$ ns.
- 5. The outputs are measured one at a time with one transition per measurement.

Switch Positions

Test	Switch
t_{PLZ}, t_{PZL} (output on B-side)	2 x (V _{DD} 33)
t_{PHZ} , t_{PZH} (output on B-side)	GND
Prop Delay	Open

Test Circuit for Dynamic Electrical Characteristics







Switching Waveforms



Voltage Waveforms Enable and Disable Times



Example of a circuit that needs to be connected to the SEL pin (8) of the PI3HDMI1210-A of our device



A product Line of Diodes Incorporated

PI3HDMI1210-A

Application Test Results



Figure 6. TMDS TP2 Tx eye compliance test setup



Figure 7. TMDS TP3 Rx "switch at receiver" eye compliance test setup







Figure 8. TP2 (Tx) eye-measurements (left is with switch, right is without switch), refer to figure 1 for the test set up



Figure 9. TP3 (Rx) - "Switch at receiver side" with 2-meter cable eye-measurements (left is with switch, right is without switch), refer to figure 2 for the test set up.





Packaging Mechanical: 48-pin BQSOP (B)



For latest package info.

please check: http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/

Ordering Information

Ordering Code	Package Code	Package Description
PI3HDMI1210-ABEX	В	48-pin, 154-Mil Wide (BQSOP)

Notes:

Thermal characteristics can be found on the company web site at www.diodes.com/design/support/packaging/

• E = Pb-free and Green

• X suffix = Tape/Reel





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