

N-Channel Power MOSFET

600V, 9.5A, 0.38Ω

FEATURES

- Super-Junction technology
- High performance due to small figure-of-merit
- High ruggedness performance
- High commutation performance
- Pb-free plating
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21

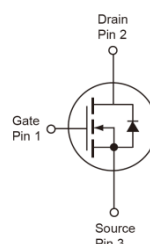
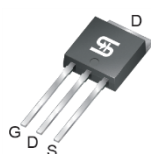
APPLICATIONS

- Power Supply
- Lighting

KEY PERFORMANCE PARAMETERS		
PARAMETER	VALUE	UNIT
V_{DS}	600	V
$R_{DS(on)}$ (max)	0.38	Ω
Q_g	19.4	nC



TO-251 (IPAK)



ABSOLUTE MAXIMUM RATINGS (T _A = 25°C unless otherwise noted)				
PARAMETER		SYMBOL	IPAK/DPAK	UNIT
Drain-Source Voltage		V _{DS}	600	V
Gate-Source Voltage		V _{GS}	±30	V
Continuous Drain Current ^(Note 1)	T _C = 25°C	I _D	9.5	A
	T _C = 100°C		6	A
Pulsed Drain Current ^(Note 2)		I _{DM}	28.5	A
Total Power Dissipation @ T _C = 25°C		P _{DTOT}	83	W
Single Pulsed Avalanche Energy ^(Note 3)		E _{AS}	64	mJ
Single Pulsed Avalanche Current ^(Note 3)		I _{AS}	1.6	A
Operating Junction and Storage Temperature Range		T _J , T _{STG}	- 55 to +150	°C

THERMAL PERFORMANCE			
PARAMETER	SYMBOL	IPAK/DPAK	UNIT
Junction to Case Thermal Resistance	$R_{\theta JC}$	1.5	°C/W
Junction to Ambient Thermal Resistance	$R_{\theta JA}$	62	°C/W

Thermal Performance Note: $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistances. The case-thermal reference is defined at the solder mounting surface of the drain pins. $R_{\theta JA}$ is guaranteed by design while $R_{\theta CA}$ is determined by the user's board design. $R_{\theta JA}$ shown below for single device operation on FR-4 PCB in still air.

ELECTRICAL SPECIFICATIONS (T _A = 25°C unless otherwise noted)						
PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
Static						
Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	BV _{DSS}	600	--	--	V
Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	V _{GS(TH)}	2	3	4	V
Gate Body Leakage	V _{GS} = ±30V, V _{DS} = 0V	I _{GSS}	--	--	±100	nA
Zero Gate Voltage Drain Current	V _{DS} = 600V, V _{GS} = 0V	I _{DSS}	--	--	1	μA
Drain-Source On-State Resistance (Note 4)	V _{GS} = 10V, I _D = 2.85A	R _{DS(on)}	--	0.26	0.38	Ω
Dynamic (Note 5)						
Total Gate Charge	V _{DS} = 380V, I _D = 9.5A, V _{GS} = 10V	Q _g	--	19.4	--	nC
Gate-Source Charge		Q _{gs}	--	3.5	--	
Gate-Drain Charge		Q _{gd}	--	8.9	--	
Input Capacitance	V _{DS} = 100V, V _{GS} = 0V, f = 1.0MHz	C _{iss}	--	795	--	pF
Output Capacitance		C _{oss}	--	67	--	
Gate Resistance	F = 1MHz, open drain	R _g	--	3.1	--	Ω
Switching (Note 6)						
Turn-On Delay Time	V _{DD} = 380V, R _{GEN} = 25Ω, I _D = 9.5A, V _{GS} = 10V,	t _{d(on)}	--	23.6	--	ns
Turn-On Rise Time		t _r	--	11.6	--	
Turn-Off Delay Time		t _{d(off)}	--	66	--	
Turn-Off Fall Time		t _f	--	9.6	--	
Source-Drain Diode						
Forward Voltage (Note 4)	I _S = 9.5A, V _{GS} = 0V	V _{SD}	--	--	1.4	V
Reverse Recovery Time	V _R = 100V, I _S = 9.5A dI _F /dt = 100A/μs	t _{rr}	--	272	--	ns
Reverse Recovery Charge		Q _{rr}	--	2.9	--	μC

Notes:

- Current limited by package.
- Pulse width limited by the maximum junction temperature.
- $L = 50mH, I_{AS} = 1.6A, V_{DD} = 50V, R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$
- Pulse test: $PW \leq 300\mu s$, duty cycle $\leq 2\%$.
- For DESIGN AID ONLY, not subject to production testing.
- Switching time is essentially independent of operating temperature.

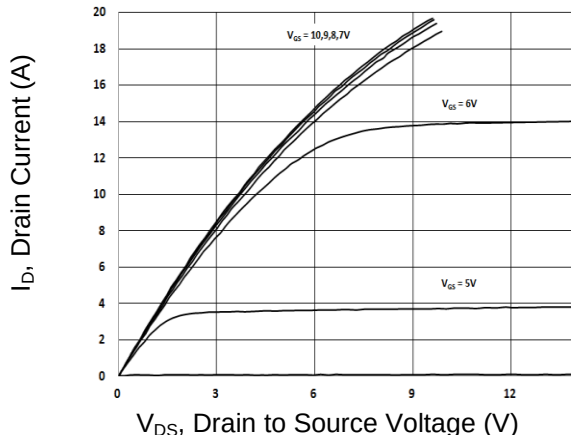
ORDERING INFORMATION

PART NO.	PACKAGE	PACKING
TSM60NB380CH C5G	TO-251 (IPAK)	75pcs / Tube

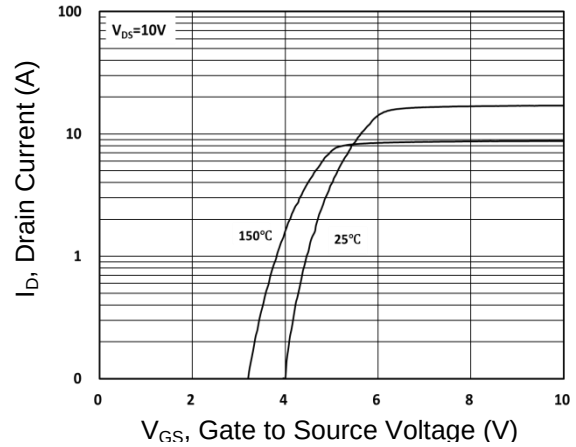
CHARACTERISTICS CURVES

($T_c = 25^\circ\text{C}$ unless otherwise noted)

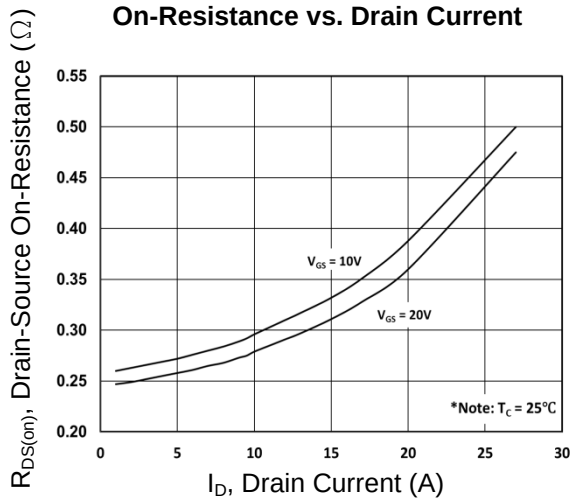
Output Characteristics



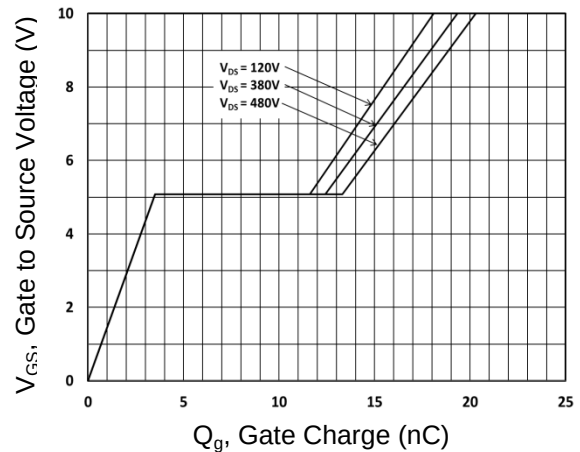
Transfer Characteristics



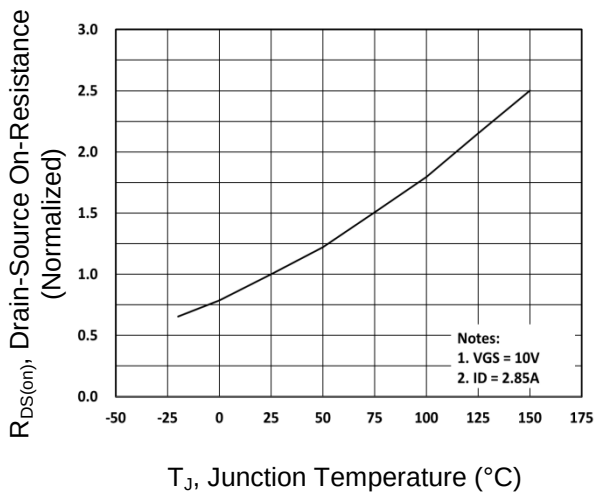
On-Resistance vs. Drain Current



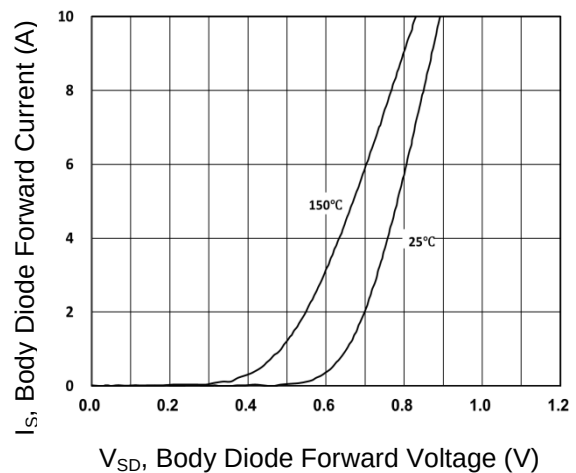
Gate-Source Voltage vs. Gate Charge



On-Resistance vs. Junction Temperature



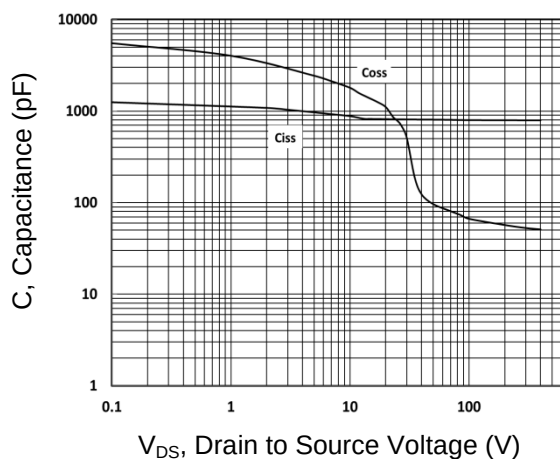
Source-Drain Diode Forward Current vs. Voltage



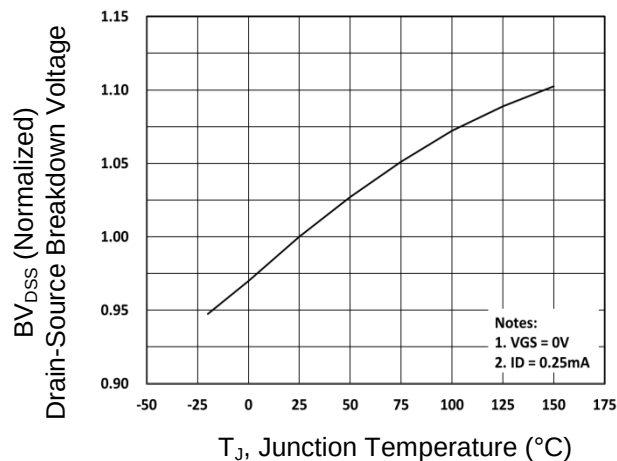
CHARACTERISTICS CURVES

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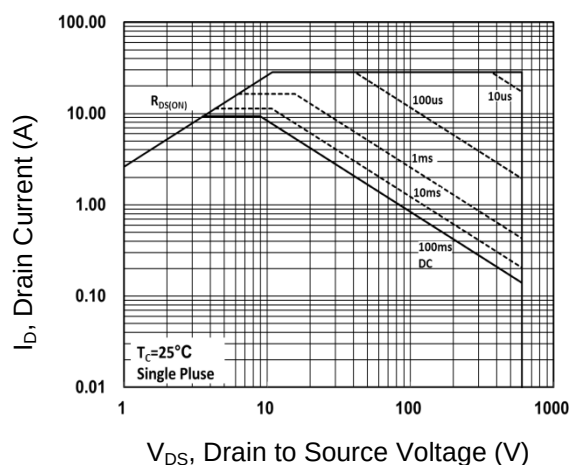
Capacitance vs. Drain-Source Voltage



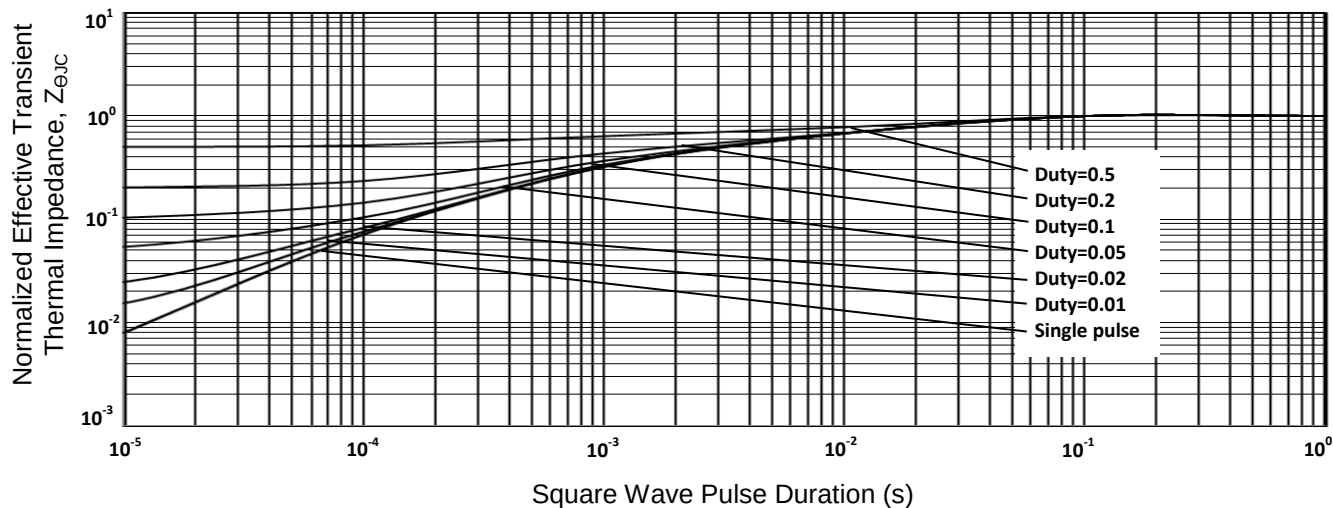
BV_{DSS} vs. Junction Temperature



Maximum Safe Operating Area

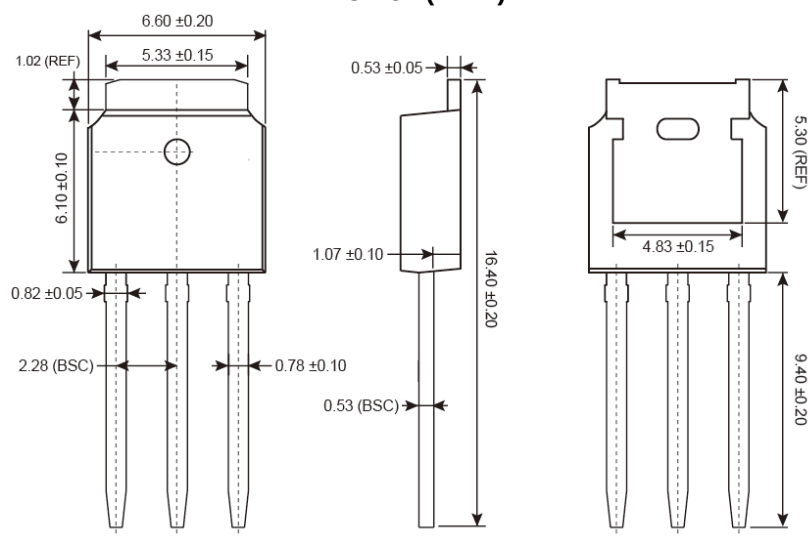


Normalized Thermal Transient Impedance, Junction-to-Case

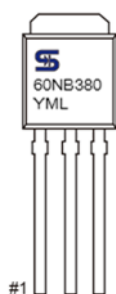


PACKAGE OUTLINE DIMENSIONS (Unit: Millimeters)

TO-251 (IPAK)



MARKING DIAGRAM



- Y** = Year Code
M = Month Code for Halogen Free Product
O =Jan **P** =Feb **Q** =Mar **R** =Apr
S =May **T** =Jun **U** =Jul **V** =Aug
W =Sep **X** =Oct **Y** =Nov **Z** =Dec
L = Lot Code (1~9, A~Z)

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