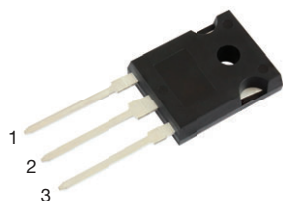
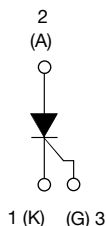


Thyristor High Voltage, Phase Control SCR, 40 A



TO-247AD 3L



FEATURES

- Designed and qualified according to JEDEC® - JESD 47
- Flexible solution for reliable AC power rectification
- Easy control peak current at charger power up to reduce passive / electromechanical components
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE

APPLICATIONS

- Typical usage is in input rectification crowbar (soft start) and AC switch in motor control, UPS, welding and battery charge

DESCRIPTION

The VS-40TPS16L-M3 high voltage series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. AEC-Q101 qualified P/N available (40TPS16LHM3).

PRIMARY CHARACTERISTICS

$I_{T(AV)}$	35 A
V_{DRM}/V_{RRM}	1600 V
V_{TM}	1.45 V
I_{GT}	150 mA
T_J	-40 °C to +125 °C
Package	TO-247AD 3L
Circuit configuration	Single SCR

MAJOR RATINGS AND CHARACTERISTICS

PARAMETER	TEST CONDITIONS	VALUES	UNITS
$I_{T(AV)}$	Sinusoidal waveform	35	A
I_{RMS}		55	
V_{RRM}/V_{DRM}		1600	V
I_{TSM}		500	A
V_T	40 A, $T_J = 25\text{ °C}$	1.45	V
dv/dt		1000	V/ μ s
di/dt		100	A/ μ s
T_J		-40 to +125	°C

VOLTAGE RATINGS

PART NUMBER	V_{RRM}/V_{DRM} , MAXIMUM REPETITIVE PEAK AND OFF-STATE VOLTAGE V	V_{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I_{RRM}/I_{DRM} AT 125 °C mA
VS-40TPS16L-M3	1600	1700	10



ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average on-state current	I _{T(AV)}	T _C = 79 °C, 180° conduction half sine wave		35	A
Maximum continuous RMS on-state current as AC switch	I _{T(RMS)}			55	
Maximum peak, one-cycle non-repetitive surge current	I _{TSM}	10 ms sine pulse, rated V _{RRM} applied	Initial T _J = T _J max.	420	
		10 ms sine pulse, no voltage reapplied		500	
Maximum I ² t for fusing	I ² t	10 ms sine pulse, rated V _{RRM} applied		880	
		10 ms sine pulse, no voltage reapplied		1250	
Maximum I ² √t for fusing	I ² √t	t = 0.1 ms to 10 ms, no voltage reapplied		12 500	A ² √s
Low level value of threshold voltage	V _{T(TO)1}	T _J = 125 °C		1.02	V
High level value of threshold voltage	V _{T(TO)2}			1.23	
Low level value of on-state slope resistance	r _{t1}			9.74	mΩ
High level value of on-state slope resistance	r _{t2}			7.50	
Maximum peak on-state voltage	V _{TM}	110 A, T _J = 25 °C		1.92	V
		90 A, T _J = 25 °C		1.82	
Maximum rate of rise of turned-on current	di/dt	T _J = 25 °C		100	A/μs
Maximum holding current	I _H	Anode supply = 6 V, resistive load, initial T _J = 1 A, I _T = 25 °C		300	mA
Maximum latching current	I _L	Anode supply = 6 V, resistive load, T _J = 25 °C		350	
Maximum reverse and direct leakage current	I _{RRM} /I _{DRM}	T _J = 25 °C	V _R = rated V _{RRM} /V _{DRM}	0.5	
		T _J = 125 °C		10	
Maximum rate of rise of off-state voltage	dv/dt	T _J = T _J maximum, linear to 80 % V _{DRM} , R _g - k = open		1000	V/μs

TRIGGERING					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum peak gate power	P _{GM}			10	W
Maximum average gate power	P _{G(AV)}			2.5	
Maximum peak gate current	I _{GM}			2.5	A
Maximum peak negative gate voltage	-V _{GM}			10	V
Maximum required DC gate voltage to trigger	V _{GT}	T _J = -40 °C	Anode supply = 6 V resistive load	4.0	V
		T _J = 25 °C		2.5	
		T _J = 125 °C		1.7	
Maximum required DC gate current to trigger	I _{GT}	T _J = -40 °C	Anode supply = 6 V resistive load	270	mA
		T _J = 25 °C		150	
		T _J = 125 °C		80	
Maximum DC gate voltage not to trigger	V _{GD}	T _J = 125 °C, V _{DRM} = rated value		0.25	V
Maximum DC gate current not to trigger	I _{GD}			6	mA



THERMAL AND MECHANICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T_J, T_{Stg}		-40 to +125	°C
Maximum thermal resistance, junction to case	R_{thJC}	DC operation	0.6	°C/W
Maximum thermal resistance, junction to ambient	R_{thJA}		40	
Maximum thermal resistance, case to heat sink	R_{thCS}	Mounting surface, smooth, and greased	0.25	
Approximate weight			6	g
			0.21	oz.
Mounting torque	minimum		6 (5)	kgf · cm
	maximum		12 (10)	(lbf · in)
Marking device		Case style TO-247AD 3L	40TPS16L	

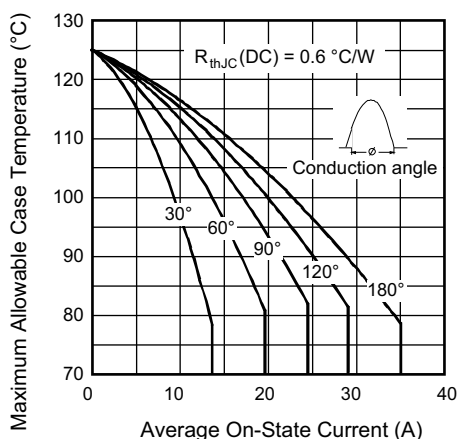


Fig. 1 - Current Rating Characteristics

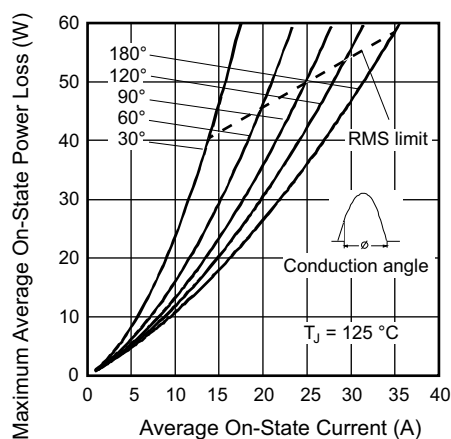


Fig. 3 - On-State Power Loss Characteristics

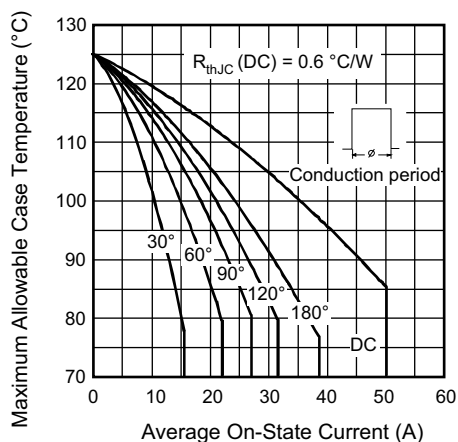


Fig. 2 - Current Rating Characteristics

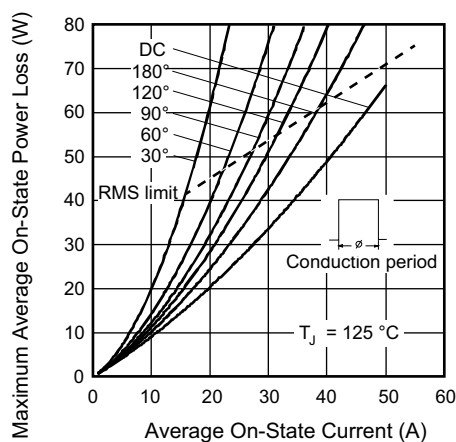


Fig. 4 - On-State Power Loss Characteristics

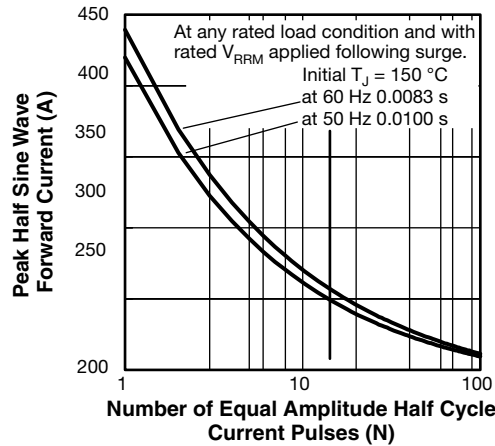


Fig. 5 - Maximum Non-Repetitive Surge Current

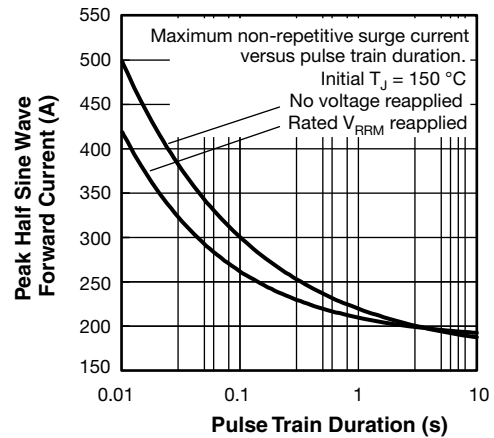


Fig. 6 - Maximum Non-Repetitive Surge Current

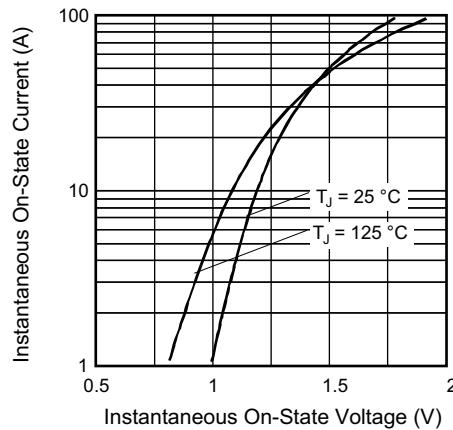


Fig. 7 - On-State Voltage Drop Characteristics

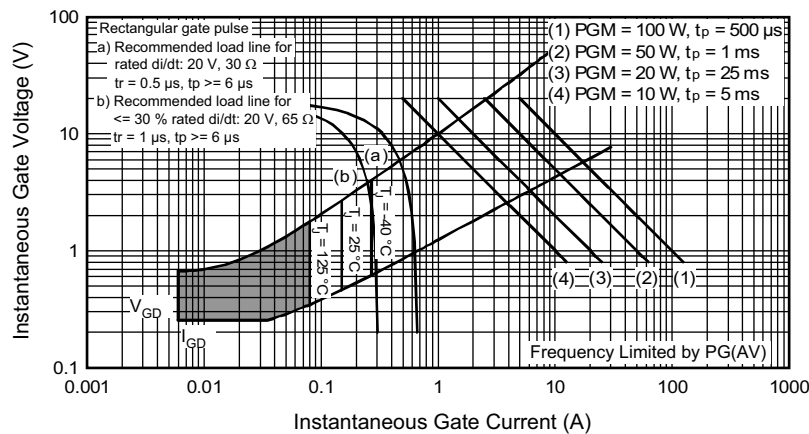


Fig. 8 - Gate Characteristics

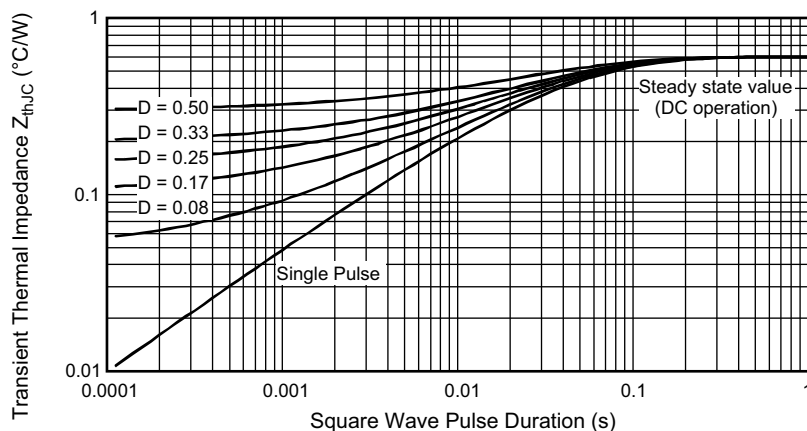


Fig. 9 - Gate Characteristics

ORDERING INFORMATION TABLE

Device code

VS-	40	T	P	S	16	L	-M3
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Diagram illustrating the device code structure (VS-40T-P-S-16-L-M3) with corresponding digit positions (1-8) and their meanings:

- 1** - Vishay Semiconductors product
- 2** - Current rating (40 = 40 A)
- 3** - Circuit configuration:
T = thyristor
- 4** - Package:
P = TO-247
- 5** - Type of silicon:
S = standard recovery rectifier
- 6** - Voltage ratings ————— **16 = 1600 V**
- 7** - L = long leads
- 8** - Environmental digit:
M3 = halogen-free, RoHS-compliant, and terminations lead

ORDERING INFORMATION (Example)			
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-40TPS16L-M3	25	500	Antistatic plastic tubes

LINKS TO RELATED DOCUMENTS		
Dimensions	TO-247AD 3L	www.vishay.com/doc?95626
Part marking information	TO-247AD 3L	www.vishay.com/doc?95007



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