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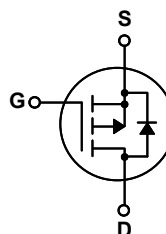
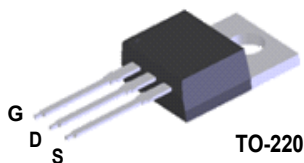
FQP17P06 **P-Channel QFET® MOSFET** - 60 V, - 17 A, 120 mΩ

Description

This P-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor®'s proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.

Features

- - 17 A, - 60 V, $R_{DS(on)} = 120 \text{ m}\Omega$ (Max.) @ $V_{GS} = -10 \text{ V}$, $I_D = -8.5 \text{ A}$
- Low Gate Charge (Typ. 21 nC)
- Low C_{rss} (Typ. 80 pF)
- 100% Avalanche Tested
- 175°C Maximum Junction Temperature Rating



Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	FQP17P06	Unit
V_{DSS}	Drain-Source Voltage	-60	V
I_D	Drain Current	- Continuous ($T_C = 25^\circ\text{C}$)	A
		- Continuous ($T_C = 100^\circ\text{C}$)	A
I_{DM}	Drain Current	- Pulsed (Note 1)	A
V_{GSS}	Gate-Source Voltage	± 25	V
E_{AS}	Single Pulsed Avalanche Energy (Note 2)	300	mJ
I_{AR}	Avalanche Current (Note 1)	-17	A
E_{AR}	Repetitive Avalanche Energy (Note 1)	7.9	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	-7.0	V/ns
P_D	Power Dissipation ($T_C = 25^\circ\text{C}$)	79	W
	- Derate above 25°C	0.53	W/ $^\circ\text{C}$
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +175	$^\circ\text{C}$
T_L	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	300	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	FQP17P06	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	1.9	$^\circ\text{C/W}$
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink, Typ.	0.5	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	$^\circ\text{C/W}$

Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = -250 μA	-60	--	--	V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = -250 μA, Referenced to 25°C	--	-0.06	--	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = -60 V, V _{GS} = 0 V	--	--	-1	μA
		V _{DS} = -48 V, T _C = 150°C	--	--	-10	μA
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = -25 V, V _{DS} = 0 V	--	--	-100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = 25 V, V _{DS} = 0 V	--	--	100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = -250 μA	-2.0	--	-4.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = -10 V, I _D = -8.5 A	--	0.094	0.12	Ω
g _{FS}	Forward Transconductance	V _{DS} = -30 V, I _D = -8.5 A	--	9.3	--	S
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = -25 V, V _{GS} = 0 V, f = 1.0 MHz	--	690	900	pF
C _{oss}	Output Capacitance		--	325	420	pF
C _{rss}	Reverse Transfer Capacitance		--	80	105	pF
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DD} = -30 V, I _D = -8.5 A, R _G = 25 Ω (Note 4)	--	13	35	ns
t _r	Turn-On Rise Time		--	100	210	ns
t _{d(off)}	Turn-Off Delay Time		--	22	55	ns
t _f	Turn-Off Fall Time		--	60	130	ns
Q _g	Total Gate Charge	V _{DS} = -48 V, I _D = -17 A, V _{GS} = -10 V (Note 4)	--	21	27	nC
Q _{gs}	Gate-Source Charge		--	4.2	--	nC
Q _{gd}	Gate-Drain Charge		--	10	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain-Source Diode Forward Current		--	--	-17	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	-68	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = -17 A	--	--	-4.0	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = -17 A, dI _F / dt = 100 A/μs	--	92	--	ns
Q _{rr}	Reverse Recovery Charge		--	0.32	--	μC

Notes:

1. Repetitive Rating ; Pulse width limited by maximum junction temperature
2. $L = 1.2\text{ mH}$, $I_{AS} = -17\text{ A}$, $V_{DD} = -25\text{ V}$, $R_G = 25\text{ }\Omega$, Starting $T_J = 25^\circ\text{C}$
3. $I_{SD} \leq -17\text{ A}$, $dI/dt \leq 300\text{ A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$
4. Essentially independent of operating temperature

Typical Characteristics

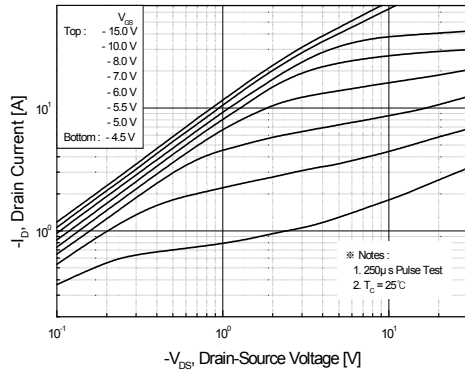


Figure 1. On-Region Characteristics

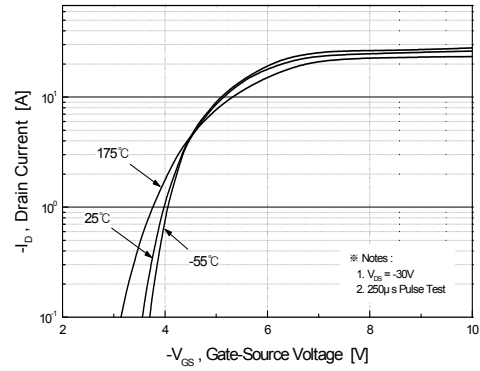


Figure 2. Transfer Characteristics

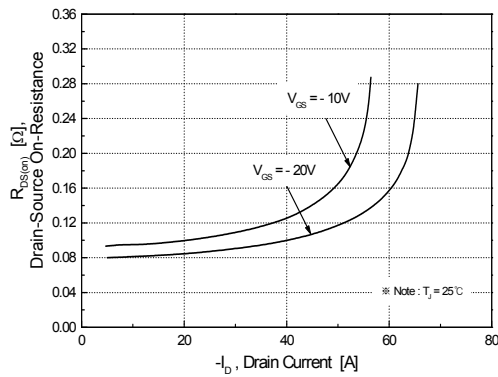


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

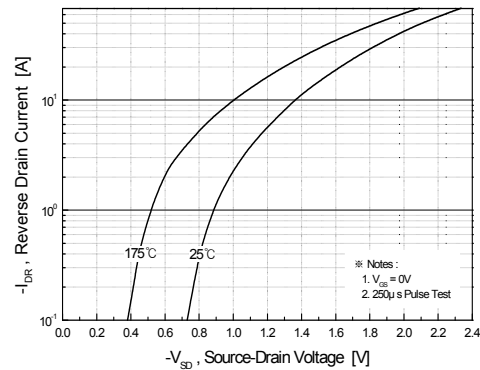


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

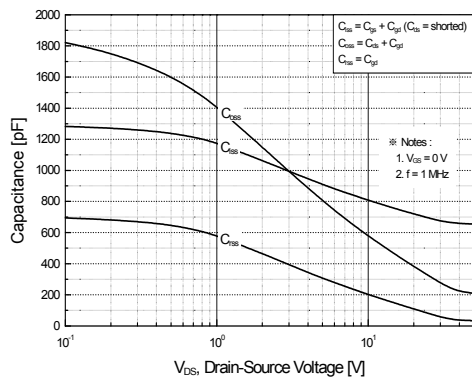


Figure 5. Capacitance Characteristics

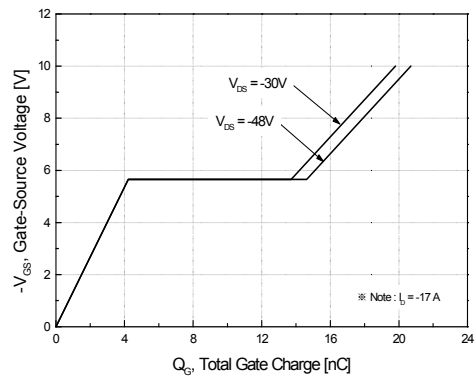


Figure 6. Gate Charge Characteristics

Typical Characteristics (Continued)

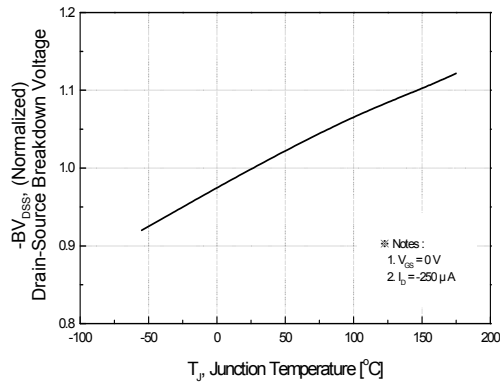


Figure 7. Breakdown Voltage Variation vs. Temperature

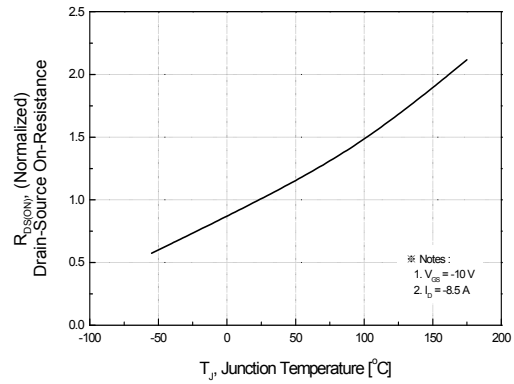


Figure 8. On-Resistance Variation vs. Temperature

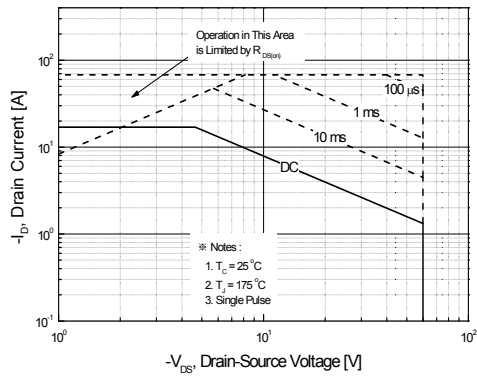


Figure 9. Maximum Safe Operating Area

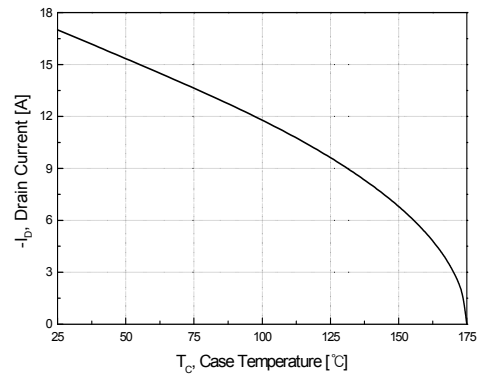


Figure 10. Maximum Drain Current vs. Case Temperature

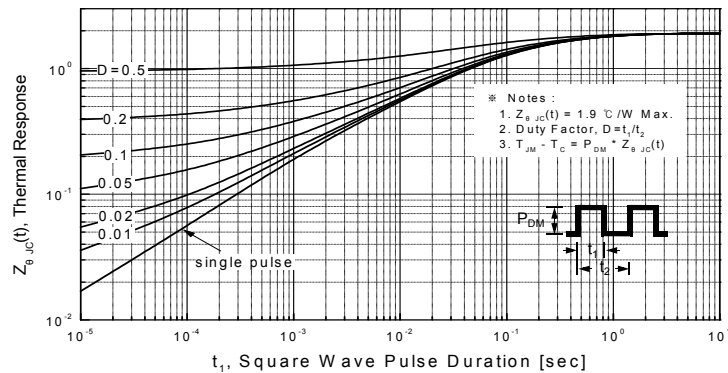
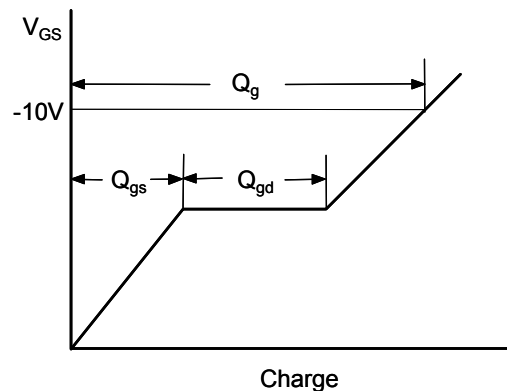
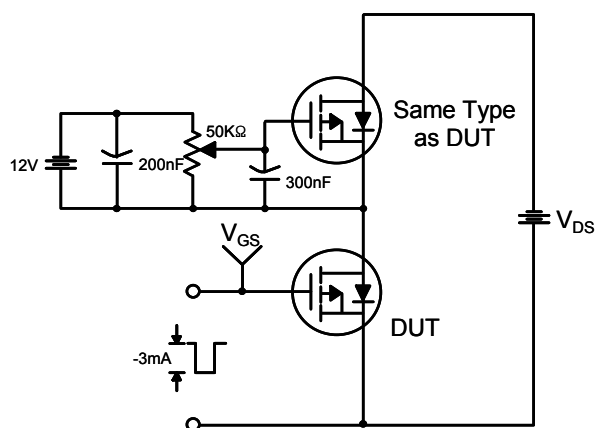
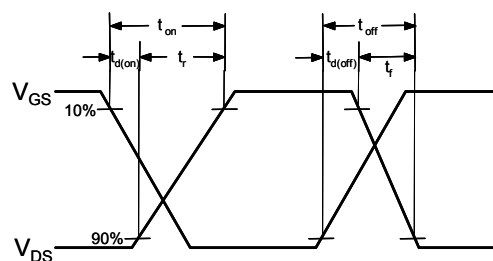
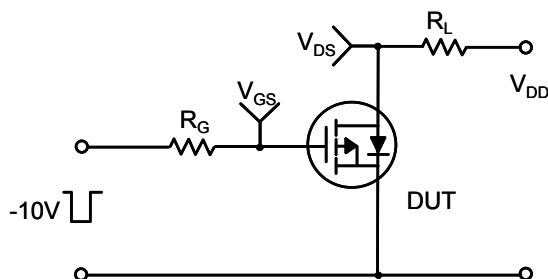


Figure 11. Transient Thermal Response Curve

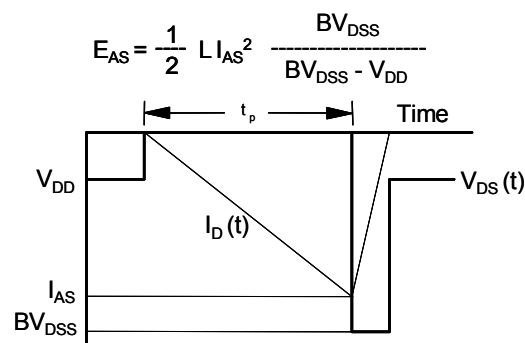
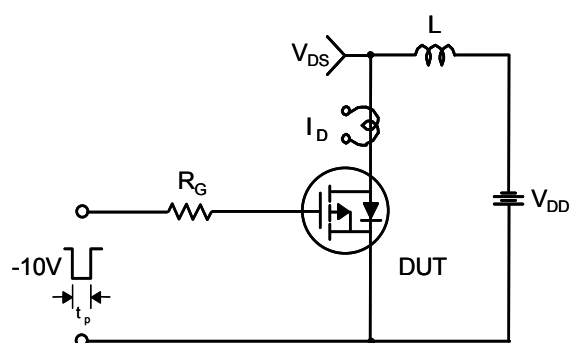
Gate Charge Test Circuit & Waveform



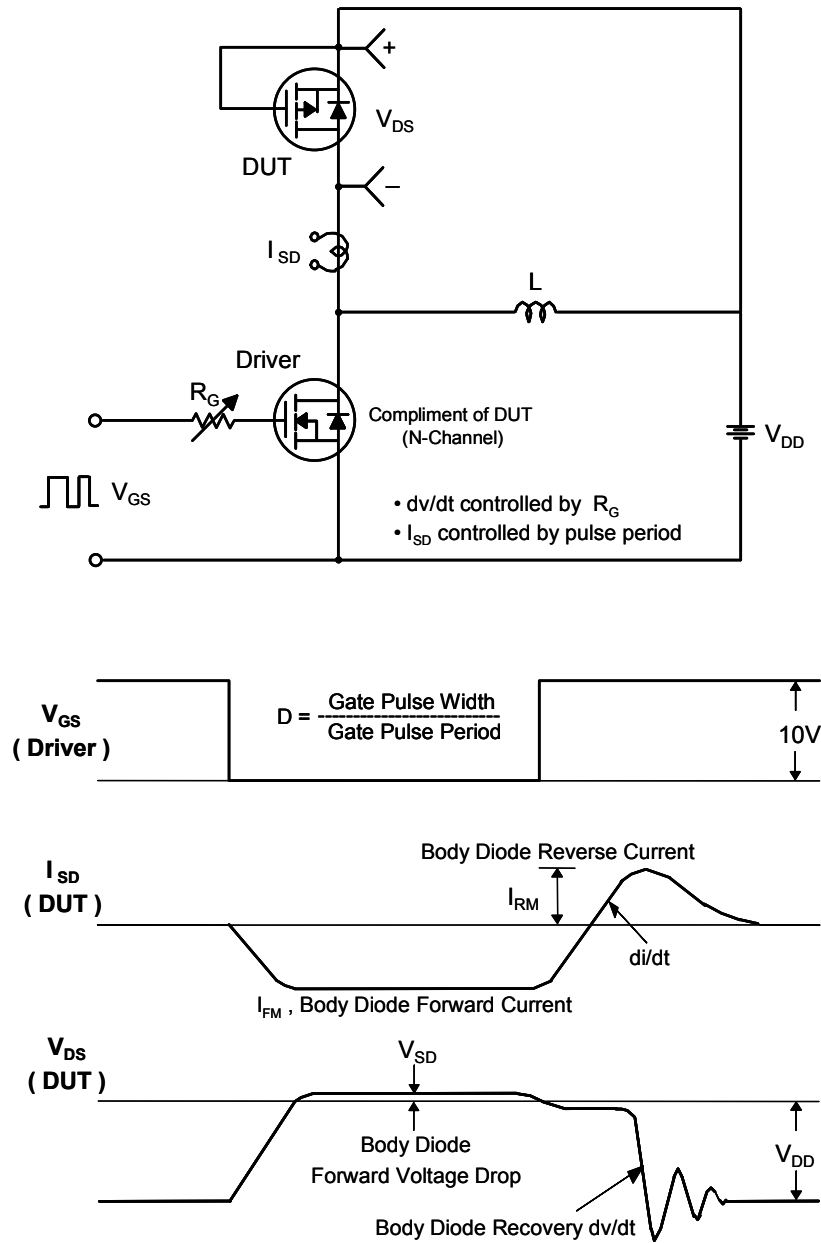
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

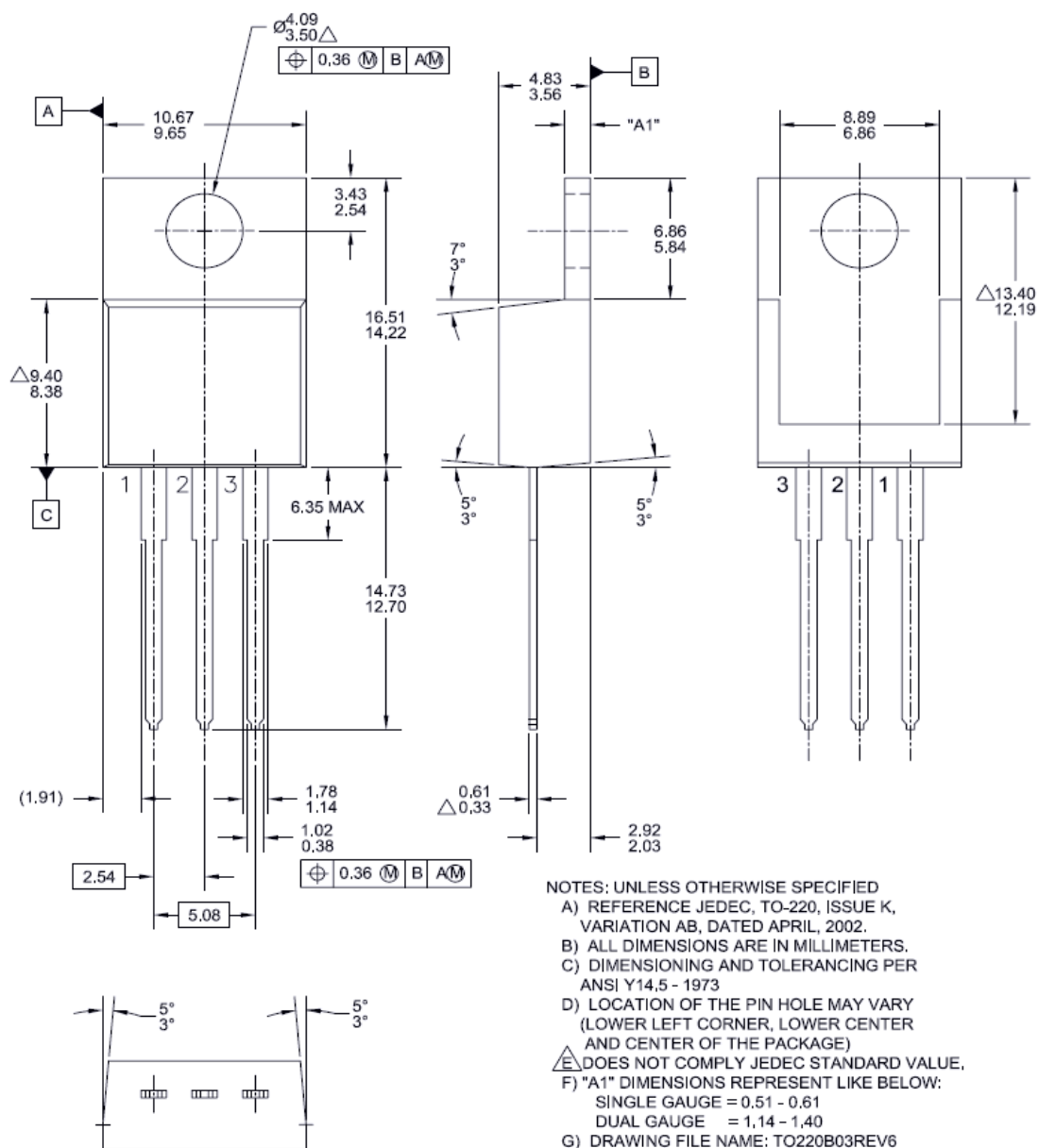


Peak Diode Recovery dv/dt Test Circuit & Waveforms



Package Dimensions

TO-220





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Офис по работе с юридическими лицами:

105318, г.Москва, ул.Щербаковская д.3, офис 1107, 1118, ДЦ «Щербаковский»

Телефон: +7 495 668-12-70 (многоканальный)

Факс: +7 495 668-12-70 (доб.304)

E-mail: info@moschip.ru

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