MMIC wideband amplifier

Rev. 4 — 13 July 2015

Product data sheet

1. Product profile

1.1 General description

Silicon Monolithic Microwave Integrated Circuit (MMIC) wideband amplifier with internal matching circuit in a 6-pin SOT363 plastic SMD package.

1.2 Features and benefits

- Input internally matched to 50 Ω
- A gain of 32.2 dB at 950 MHz
- Output power at 1 dB gain compression = 8 dBm
- Supply current = 29.9 mA at a supply voltage of 5 V
- Reverse isolation > 34 dB up to 2 GHz
- Good linearity with low second order and third order products
- Noise figure = 3.9 dB at 950 MHz
- Unconditionally stable (K > 1)
- No output inductor required

1.3 Applications

- LNB IF amplifiers
- General purpose low noise wideband amplifier for frequencies between DC and 2.2 GHz

2. Pinning information

Pin	Description	Simplified outline	Graphic symbol
1	V _{CC}		
2, 5	GND2		
3	RF_OUT		6-
4	GND1	0	
6	RF_IN		4 2, 5 777 777 sym052



3. Ordering information

Table 2. Order	ing informa	ition	
Type number	Package		
	Name	Description	Version
BGA2865	-	plastic surface-mounted package; 6 leads	SOT363

4. Marking

Table 3. Marking	3	
Type number	Marking code	Description
BGA2865	*EC	* = - : made in Hong Kong
		* = p : made in Hong Kong
		* = W : made in China
		* = t : made in Malaysia

5. Limiting values

Table 4.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage	RF input AC coupled	-0.5	+7.0	V
I _{CC}	supply current		-	36	mA
P _{tot}	total power dissipation	T _{sp} = 90 °C	-	200	mW
T _{stg}	storage temperature		-40	+125	°C
Tj	junction temperature		-	125	°C
P _{drive}	drive power		-	+10	dBm

6. Thermal characteristics

Table 5.	Thermal characteristics			
Symbol	Parameter	Conditions	Тур	Unit
R _{th(j-sp)}	thermal resistance from junction to solder point	P_{tot} = 200 mW; T_{sp} = 90 °C	300	K/W

7. Characteristics

Table 6.Characteristics

 $V_{CC} = 5.0 \text{ V}; Z_S = Z_L = 50 \Omega; P_i = -35 \text{ dBm}; T_{amb} = 25 \text{ °C}; \text{ measured on demo board; unless otherwise specified.}$

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CC}	supply voltage		4.5	5.0	5.5	V
I _{CC}	supply current		23.0	26.4	29.7	mA

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
G _p	power gain	f = 250 MHz	30.3	30.9	31.5	dB
		f = 950 MHz	31.5	32.2	32.9	dB
		f = 2150 MHz	28.1	29.6	31.1	dB
RL _{in}	input return loss	f = 250 MHz	14	16	18	dB
		f = 950 MHz	17	18	20	dB
		f = 2150 MHz	10	11	18	dB
RL _{out}	output return loss	f = 250 MHz	14	18	23	dB
		f = 950 MHz	9	10	11	dB
		f = 2150 MHz	7	9	12	dB
ISL	isolation	f = 250 MHz	41	61	82	dB
		f = 950 MHz	41	43	44	dB
		f = 2150 MHz	34	37	39	dB
NF	noise figure	f = 250 MHz	3.4	3.8	4.3	dB
		f = 950 MHz	3.4	3.9	4.3	dB
		f = 2150 MHz	3.6	4.0	4.5	dB
B _{-3dB}	-3 dB bandwidth	3 dB below gain at 1 GHz	2.1	2.2	2.4	GHz
K	Rollett stability factor	f = 250 MHz	14	16	18	
		f = 950 MHz	1.2	1.5	1.8	
		f = 2150 MHz	0.9	1.2	1.4	
P _{L(sat)}	saturated output power	f = 250 MHz	10	11	12	dBm
		f = 950 MHz	8	9	10	dBm
		f = 2150 MHz	2	3	5	dBm
P _{L(1dB)}	output power at 1 dB gain compression	f = 250 MHz	8	9	10	dBm
		f = 950 MHz	6	8	9	dBm
		f = 2150 MHz	1	2	3	dBm
IP3 _I	input third-order intercept point	$P_{drive} = -38 \text{ dBm}$ (for each tone)				
		f ₁ = 250 MHz; f ₂ = 251 MHz	-13	-11	-8	dBm
		f ₁ = 950 MHz; f ₂ = 951 MHz	-16	-14	-11	dBm
		f ₁ = 2150 MHz; f ₂ = 2151 MHz	-23	-20	-17	dBm
IP3 ₀	output third-order intercept point	$P_{drive} = -38 \text{ dBm}$ (for each tone)				
		f ₁ = 250 MHz; f ₂ = 251 MHz	18	20	22	dBm
		f ₁ = 950 MHz; f ₂ = 951 MHz	16	19	21	dBm
		f ₁ = 2150 MHz; f ₂ = 2151 MHz	7	10	13	dBm
P _{L(2H)}	second harmonic output power	P _{drive} = -35 dBm				+
. /		f _{1H} = 250 MHz; f _{2H} = 500 MHz	-52	-50	-48	dBm
		f _{1H} = 950 MHz; f _{2H} = 1900 MHz	-42	-40	-39	dBm
∆IM2	second-order intermodulation distance	$P_{drive} = -38 \text{ dBm}$ (for each tone)				+
		f ₁ = 250 MHz; f ₂ = 251 MHz	36	47	58	dBc
		$f_1 = 950 \text{ MHz}; f_2 = 951 \text{ MHz}$	25	37	48	dBc

Table 6. Characteristics ... continued $V_{co} = 5.0 V$; $Z_{c} = Z_{i} = 50 \Omega$; $P_{i} = -35 dBm$; T_{ar}

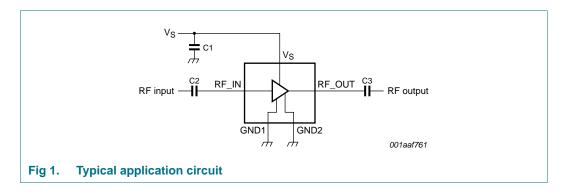
25 °C : measured on demo board: unless otherwise specified

8. Application information

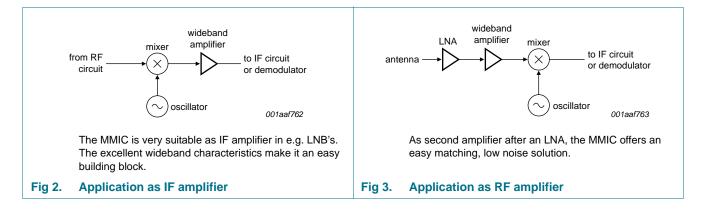
<u>Figure 1</u> shows a typical application circuit for the BGA2865 MMIC. The device is internally matched to 50 Ω and therefore does not need any external matching. The value of the input and output DC blocking capacitors C2 and C3 should not be more than 100 pF for applications above 100 MHz. However, when the device is operated below 100 MHz, the capacitor value should be increased.

The 22 nF supply decoupling capacitor C1 should be located as close as possible to the MMIC.

The PCB top ground plane, connected to pins 2, 4 and 5 must be as close as possible to the MMIC, preferably also below the MMIC. When using via holes, use multiple via holes as close as possible to the MMIC.



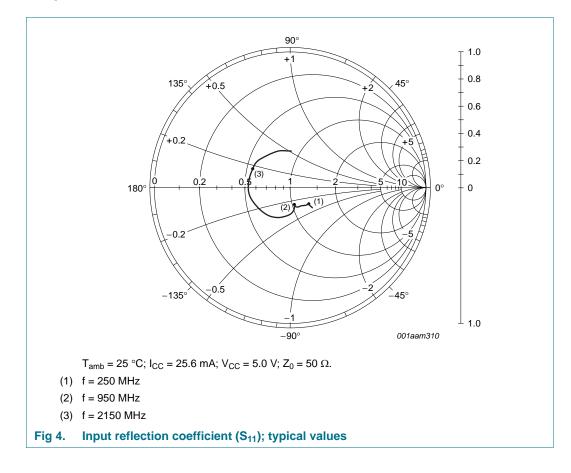
8.1 Application examples



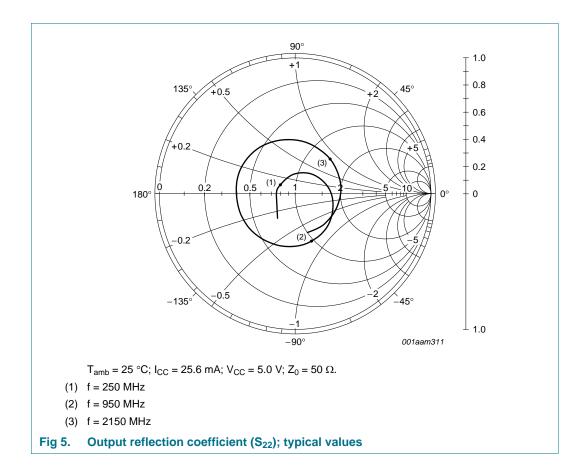
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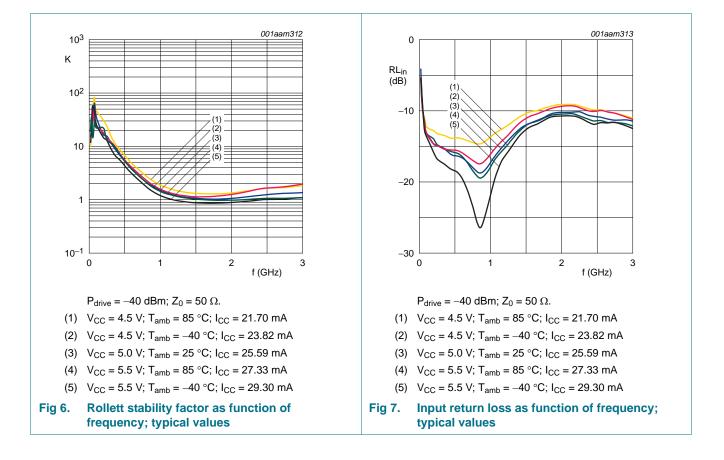
8.2 Graphs



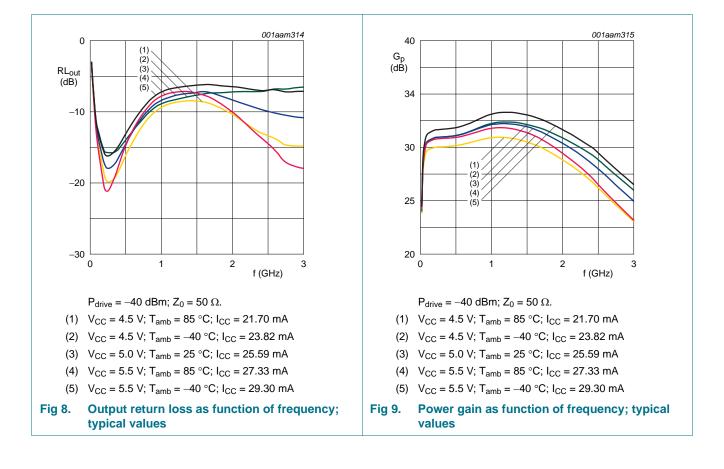
MMIC wideband amplifier



MMIC wideband amplifier



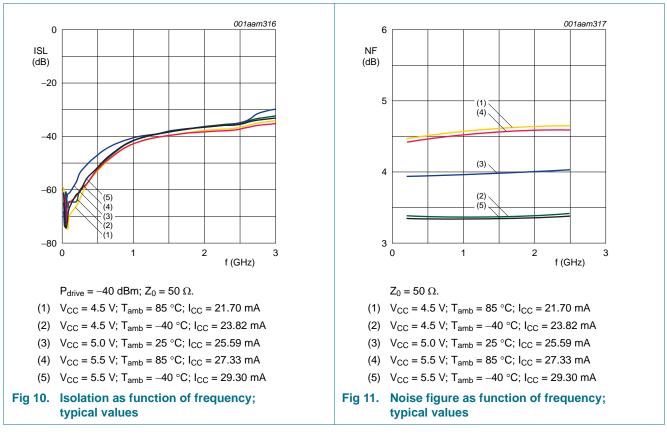
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BGA2865

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8.3 Tables

Table 7.Supply current over temperature and supply voltagesTypical values.

Symbol	Parameter	Conditions	T _{amb} (°0	T _{amb} (°C)			
			-40	+25	+85		
I _{CC}	supply current	$V_{CC} = 4.5 V$	23.82	23.00	21.70	mA	
		$V_{CC} = 5.0 V$	26.65	25.59	24.42	mA	
		$V_{CC} = 5.5 V$	29.30	27.94	27.33	mA	

Table 8.Second harmonic output power over temperature and supply voltagesTypical values.

Symbol	Parameter	Conditions	Tamb	(°C)		Unit
			-40	+25	+85	
P _{L(2H)}	second harmonic output power	f = 250 MHz; $P_{drive} = -37 \text{ dBm}$				
		$V_{CC} = 4.5 V$	-49	-51	-52	dBm
		V _{CC} = 5.0 V	-49	-50	-51	dBm
		V _{CC} = 5.5 V	-49	-50	-50	dBm
		f = 950 MHz; P_{drive} = -37 dBm				
		V _{CC} = 4.5 V	-40	-42	-44	dBm
		$V_{CC} = 5.0 V$	-40	-42	-43	dBm
		V _{CC} = 5.5 V	-40	-42	-43	dBm

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Symbol	Parameter	Conditions	T _{amb} (°C)		Unit
			-40	+25	+85	
P _{i(1dB)}	input power at 1 dB gain compression	f = 250 MHz				
		$V_{CC} = 4.5 V$	-21.6	-21.6	-21.6	dBm
		$V_{CC} = 5.0 V$	-21.0	-21.1	-21.1	dBm
		$V_{CC} = 5.5 V$	-20.6	-20.7	-20.7	dBm
		f = 950 MHz				
		$V_{CC} = 4.5 V$	-22.7	-22.9	-23.1	dBm
		$V_{CC} = 5.0 V$	-22.4	-22.6	-23.0	dBm
		V _{CC} = 5.5 V	-22.1	-22.4	-22.9	dBm
		f = 2150 MHz				
		$V_{CC} = 4.5 V$	-25.0	-25.6	-26.4	dBm
		$V_{CC} = 5.0 V$	-25.1	-25.9	-26.7	dBm
		$V_{CC} = 5.5 V$	-25.2	-26.2	-27.0	dBm

Table 9. Input power at 1 dB gain compression over temperature and supply voltages *Typical values.*

Table 10. Output power at 1 dB gain compression over temperature and supply voltages *Typical values.*

Symbol	Parameter	Conditions	T _{amb}	(°C)		Unit
			-40	+25	+85	
P _{L(1dB)}	output power at 1 dB gain compression	f = 250 MHz				
		$V_{CC} = 4.5 V$	8.3	7.8	7.3	dBm
		$V_{CC} = 5.0 V$	9.3	8.8	8.3	dBm
		$V_{CC} = 5.5 V$	10.1	9.6	9.0	dBm
		f = 950 MHz				
		$V_{CC} = 4.5 V$	8.0	7.3	6.3	dBm
		$V_{CC} = 5.0 V$	8.9	8.0	6.9	dBm
		$V_{CC} = 5.5 V$	9.6	8.6	7.4	dBm
		f = 2150 MHz				
		$V_{CC} = 4.5 V$	4.1	2.4	0.3	dBm
		V _{CC} = 5.0 V	4.5	2.6	0.2	dBm
		V _{CC} = 5.5 V	4.7	2.4	0.1	dBm

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Symbol	Parameter	Conditions	T _{amb} ((°C)		Unit
			-40	+25	+85	
P _{L(sat)}	saturated output power	f = 250 MHz				
		$V_{CC} = 4.5 V$	10.5	10.1	9.6	dBm
		$V_{CC} = 5.0 V$	11.8	11.3	10.8	dBm
		$V_{CC} = 5.5 V$	12.3	11.8	11.3	dBm
		f = 950 MHz				
		$V_{CC} = 4.5 V$	9.6	8.8	7.7	dBm
		$V_{CC} = 5.0 V$	10.4	9.3	8.2	dBm
		$V_{CC} = 5.5 V$	10.9	9.8	8.5	dBm
		f = 2150 MHz				
		$V_{CC} = 4.5 V$	5.3	3.7	2.0	dBm
		$V_{CC} = 5.0 V$	5.7	4.0	2.0	dBm
		V _{CC} = 5.5 V	5.9	4.0	2.0	dBm

 Table 11.
 Saturated output power over temperature and supply voltages

 Typical values.
 Values.

Table 12.	Second-order intermodulation distance over temperature and supply voltages
Typical valu	Jes.

Symbol	Parameter	Conditions	T _{amb} (°C)			Unit
			-40	+25	+85	
∆IM2	second-order intermodulation distance	$ f_1 = 250 \text{ MHz}; \\ f_2 = 251 \text{ MHz}; \\ P_{drive} = -40 \text{ dBm} $				
		$V_{CC} = 4.5 V$	50	48	45	dBc
		V _{CC} = 5.0 V	51	48	46	dBc
		$V_{CC} = 5.5 V$	51	48	46	dBc
		V _{CC} = 4.5 V	41	40	37	dBc
		V _{CC} = 5.0 V	42	39	36	dBc
		V _{CC} = 5.5 V	41	38	35	dBc

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Symbol	Parameter	Conditions	T _{amb}	T _{amb} (°C)		
			-40	+25	+85	
IP3 ₀	output third-order intercept point	$f_1 = 250 \text{ MHz};$ $f_2 = 251 \text{ MHz};$ $P_{drive} = -40 \text{ dBm}$				
		$V_{CC} = 4.5 V$	20.8	19.2	18.1	dBm
		$V_{CC} = 5.0 V$	21.6	21.2	20.1	dBm
		V _{CC} = 5.5 V	23.3	21.0	20.5	dBm
		$f_1 = 950 \text{ MHz};$ $f_2 = 951 \text{ MHz};$ $P_{drive} = -40 \text{ dBm}$				
		$V_{CC} = 4.5 V$	20.7	18.9	16.9	dBm
		$V_{CC} = 5.0 V$	21.3	19.4	17.4	dBm
		V _{CC} = 5.5 V	21.7	19.9	17.9	dBm
		$f_1 = 2150 \text{ MHz};$ $f_2 = 2151 \text{ MHz};$ $P_{drive} = -40 \text{ dBm}$				
		V _{CC} = 4.5 V	12.6	10.5	8.2	dBm
		V _{CC} = 5.0 V	12.9	10.8	8.4	dBm
		V _{CC} = 5.5 V	13.3	10.9	8.5	dBm

Table 13.	Output third-order intercept point over temperature and supply voltages
Typical val	ues.

Table 14. -3 dB bandwidth over temperature and supply voltages Typical values.

Symbol	Parameter	Conditions	T _{amb} (°0	T _{amb} (°C)		
			-40	+25	+85	
B _{-3dB}	-3 dB bandwidth	$V_{CC} = 4.5 V$	2.419	2.312	2.200	GHz
		$V_{CC} = 5.0 V$	2.403	2.284	2.164	GHz
		$V_{CC} = 5.5 V$	2.378	2.253	2.127	GHz

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Test information 9.

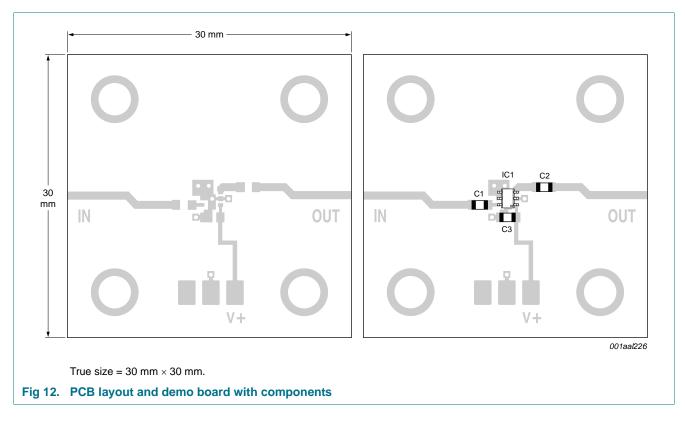


Table 15. List of components used for the typical application

Component	Description	Value	Dimensions
C1, C2	multilayer ceramic chip capacitor	100 pF	0603
C3	multilayer ceramic chip capacitor	22 nF	0603
IC1	BGA2865 MMIC	-	SOT363

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10. Package outline

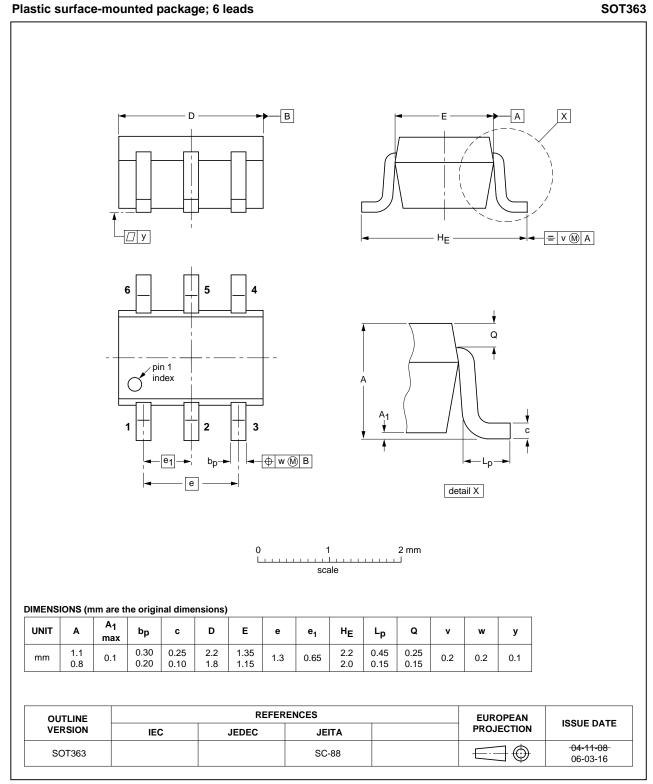


Fig 13. Package outline SOT363

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11. Abbreviations

Table 16. Abbreviations				
Acronym	Description			
IF	Intermediate Frequency			
LNA	Low-Noise Amplifier			
LNB	Low-Noise Block converter			
PCB	Printed-Circuit Board			

12. Revision history

Table 17.Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BGA2865 v.4	20150713	Product data sheet	-	BGA2865 v.3
Modifications:	 The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors. Legal texts have been adapted to the new company name where appropriate. 			
BGA2865 v.3	20130826	Product data sheet	-	BGA2865 v.2
BGA2865 v.2	20101101	Product data sheet	-	BGA2865 v.1
BGA2865 v.1	20100817	Product data sheet	-	-

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Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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На всех этапах разработки и производства наши партнеры могут получить квалифицированную поддержку опытных инженеров.

Система менеджмента качества компании отвечает требованиям в соответствии с ГОСТ Р ИСО 9001, ГОСТ РВ 0015-002 и ЭС РД 009

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