



SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

P-Channel Silicon MOSFET

CPH6347 — General-Purpose Switching Device Applications

Features

- 1.8V drive
- Halogen free compliance
- Protection diode in

Specifications

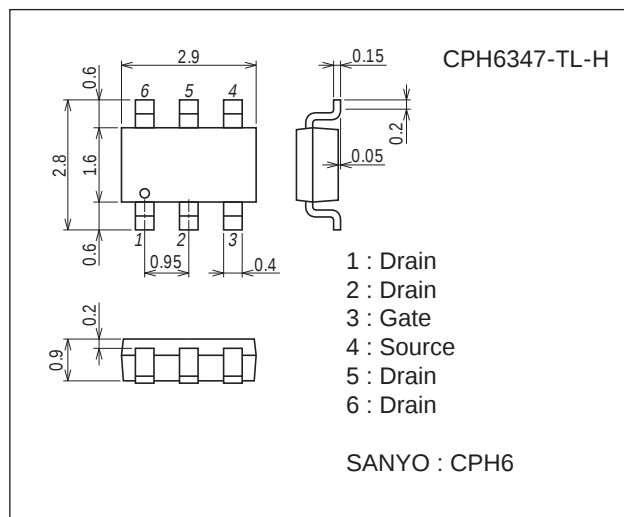
Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		-20	V
Gate-to-Source Voltage	V_{GSS}		±12	V
Drain Current (DC)	I_D		-6	A
Drain Current (Pulse)	I_{DP}	$PW \leq 10\mu s$, duty cycle $\leq 1\%$	-24	A
Allowable Power Dissipation	P_D	When mounted on ceramic substrate (900mm ² ×0.8mm)	1.6	W
Channel Temperature	T_{ch}		150	°C
Storage Temperature	T_{stg}		-55 to +150	°C

Package Dimensions

unit : mm (typ)

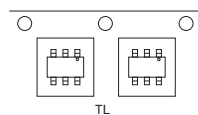
7018A-003



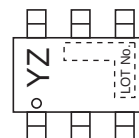
Product & Package Information

- Package : CPH6
- JEITA, JEDEC : SC-74, SOT-26, SOT-457
- Minimum Packing Quantity : 3,000 pcs./reel

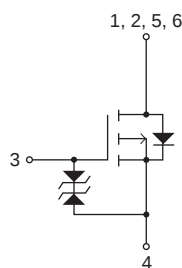
Packing Type: TL



Marking



Electrical Connection



SANYO Semiconductor Co., Ltd.

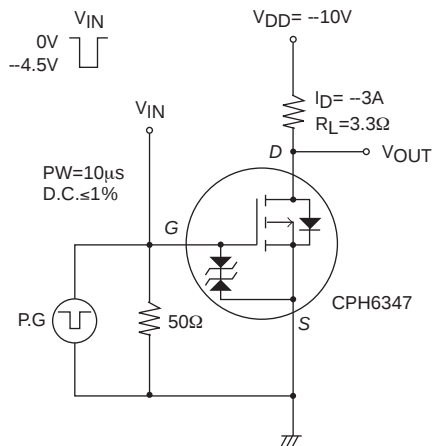
<http://semicon.sanyo.com/en/network>

CPH6347

Electrical Characteristics at Ta=25°C

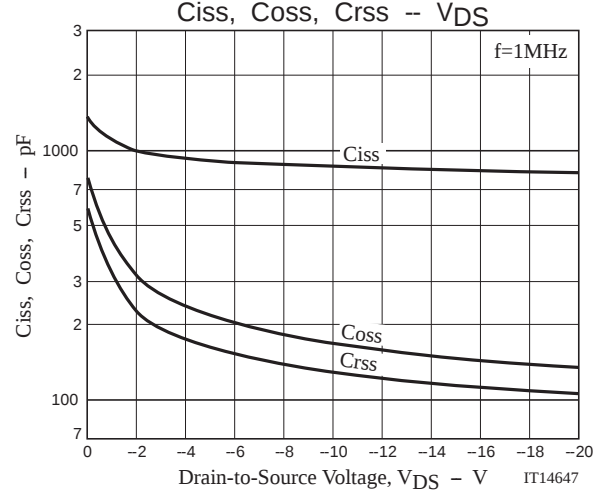
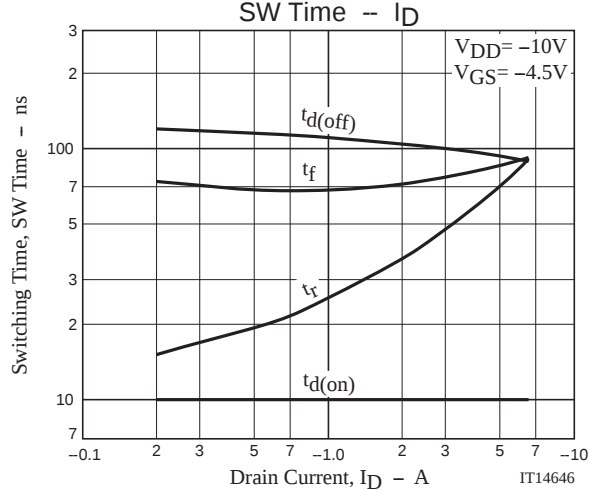
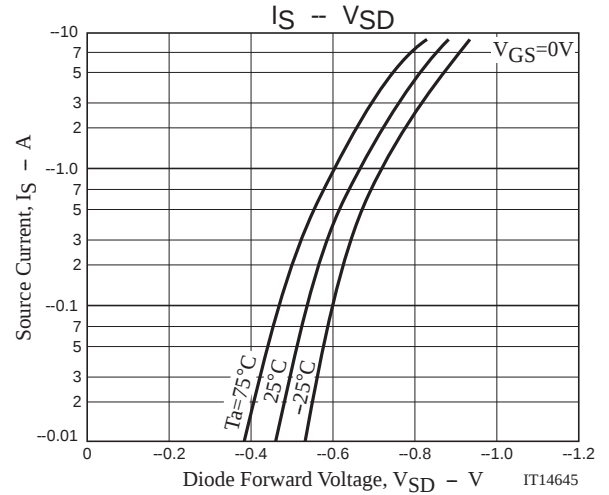
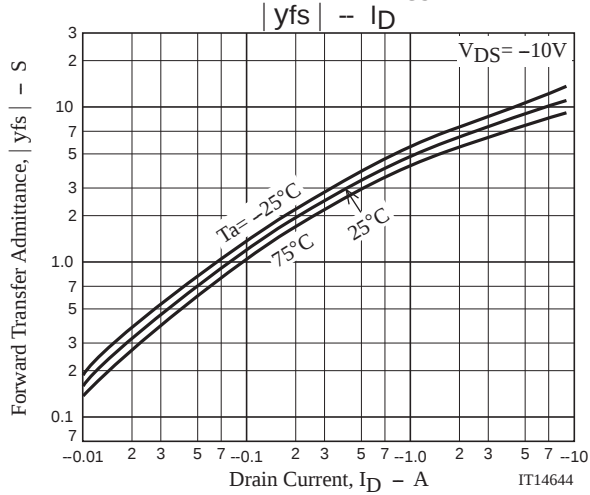
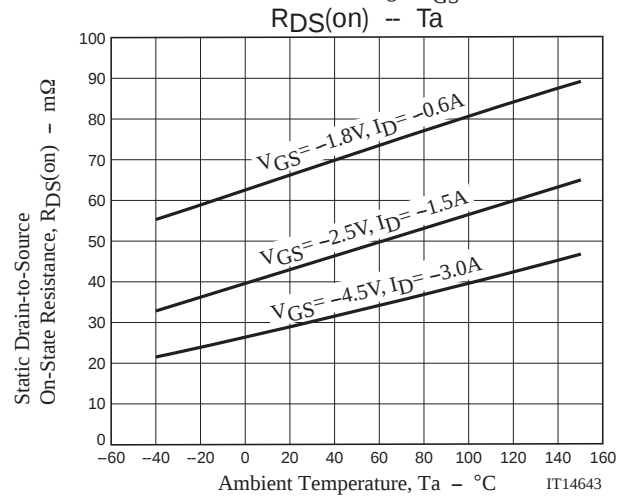
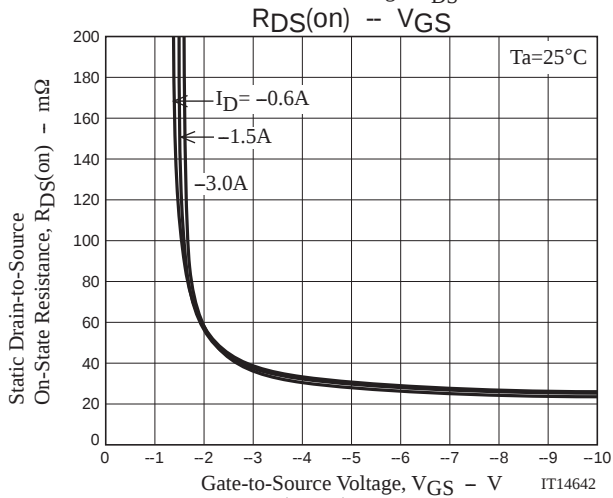
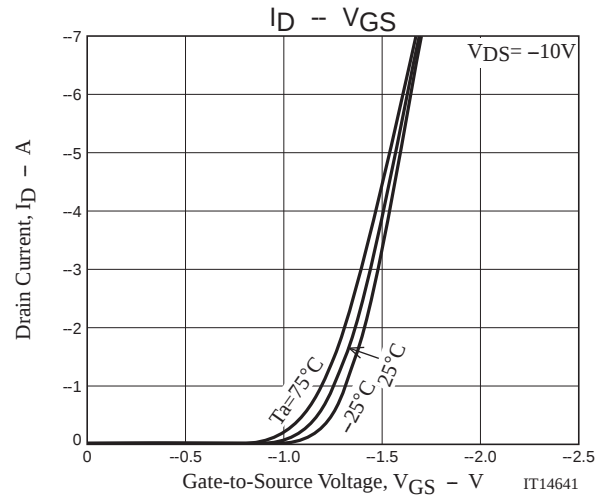
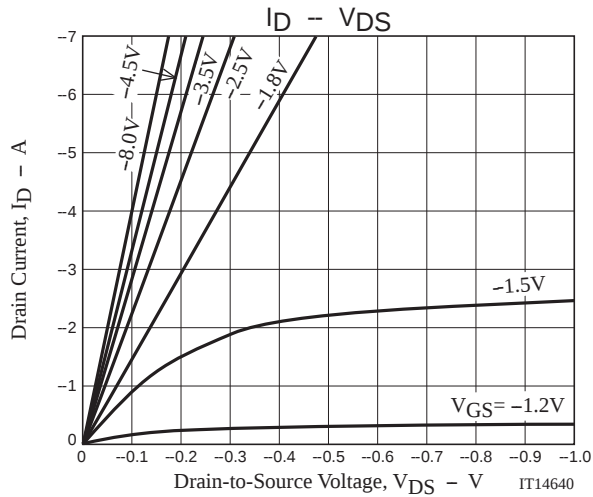
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = -1mA, V_{GS} = 0V$	-20			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -20V, V_{GS} = 0V$			-1	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS} = \pm 8V, V_{DS} = 0V$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS} = -10V, I_D = -1mA$	-0.4		-1.4	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS} = -10V, I_D = -3A$	4.3	7.3		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D = -3A, V_{GS} = -4.5V$		30	39	m Ω
	$R_{DS(on)2}$	$I_D = -1.5A, V_{GS} = -2.5V$		44	66	m Ω
	$R_{DS(on)3}$	$I_D = -0.6A, V_{GS} = -1.8V$		68	102	m Ω
Input Capacitance	C_{iss}	$V_{DS} = -10V, f = 1MHz$		860		pF
Output Capacitance	C_{oss}			170		pF
Reverse Transfer Capacitance	C_{rss}			130		pF
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit.		10		ns
Rise Time	t_r			48		ns
Turn-OFF Delay Time	$t_{d(off)}$			100		ns
Fall Time	t_f			78		ns
Total Gate Charge	Q_g	$V_{DS} = -10V, V_{GS} = -4.5V, I_D = -6A$		10.5		nC
Gate-to-Source Charge	Q_{gs}			2.0		nC
Gate-to-Drain "Miller" Charge	Q_{gd}			3.0		nC
Diode Forward Voltage	V_{SD}	$I_S = -6A, V_{GS} = 0V$		-0.82	-1.5	V

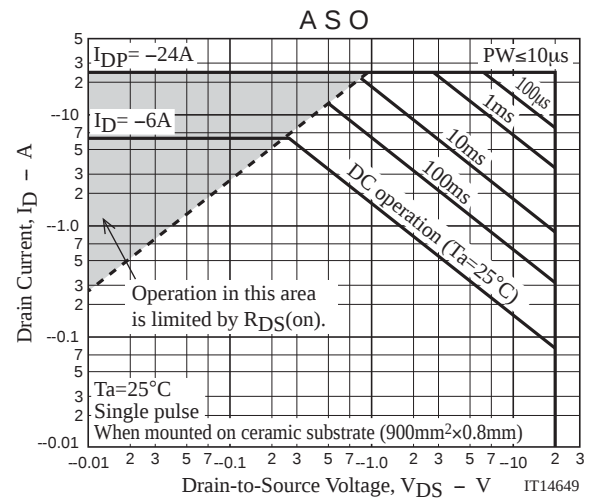
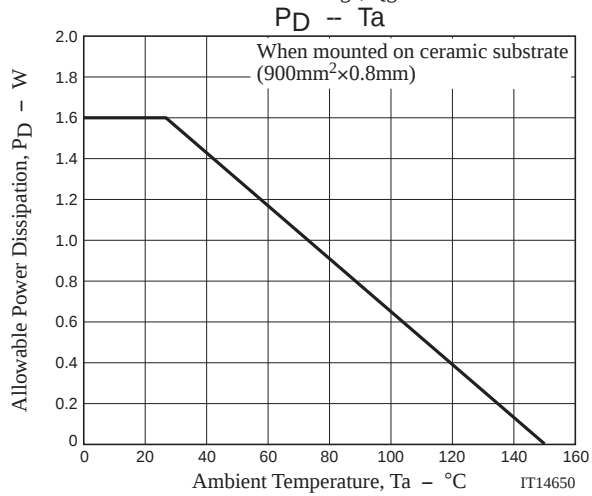
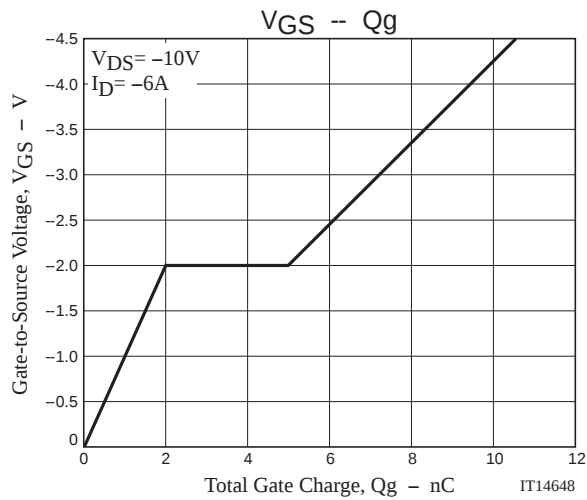
Switching Time Test Circuit



Ordering Information

Device	Package	Shipping	memo
CPH6347-TL-H	CPH6	3,000pcs./reel	Pb Free and Halogen Free





Embossed Taping Specification

CPH6347-TL-H

1. Packing Format

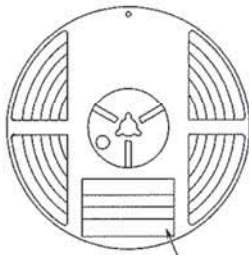
Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
CPH6	CPH6	3,000	15,000	90,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

Reel label, Inner box label
(unit:mm)

Outer box label

It is a label at the time of factory shipments.
The form of a label may change in physical distribution process.

Packing method



Reel label

Type No.
LOT No.
Quantity
Origin

TYPE	000000000
LOT	00
QTY	0,000 (1) LEAD FREE #
SPECIAL	*20722005310C*
ASSEMBLY	**** (DIFFUSION:****)

NOTE (1)

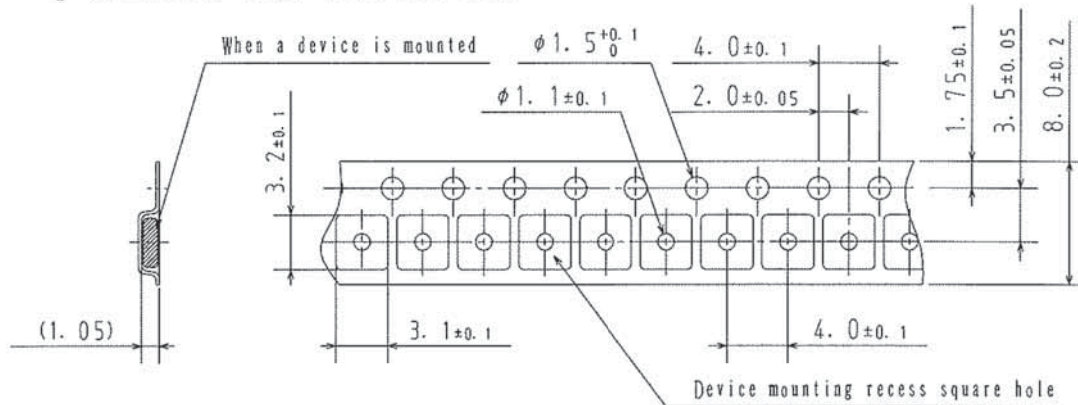
The LEAD FREE # description shows that the surface treatment of the terminal is lead free.

TYPE CODE	*****0*****
TYPE	00000000
QTY	0,000 PCS (1) LEAD FREE #
LOT	00000000
PACKAGE	00000000
SPECIAL	*20722005310C*
ASSEMBLY	**** (DIFFUSION:****)

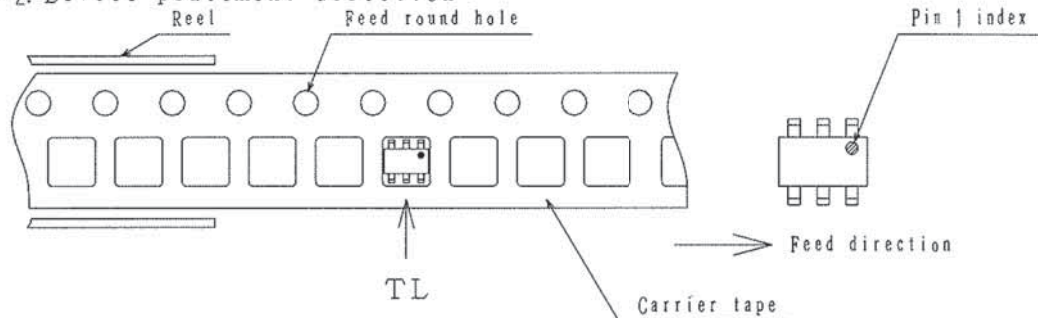
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)

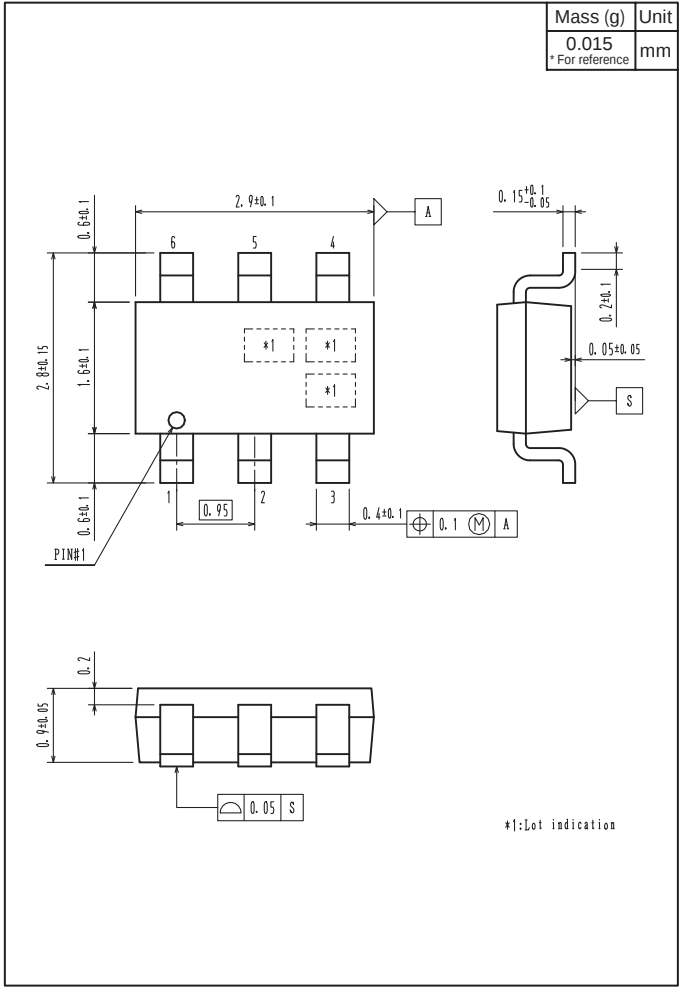


2-2. Device placement direction

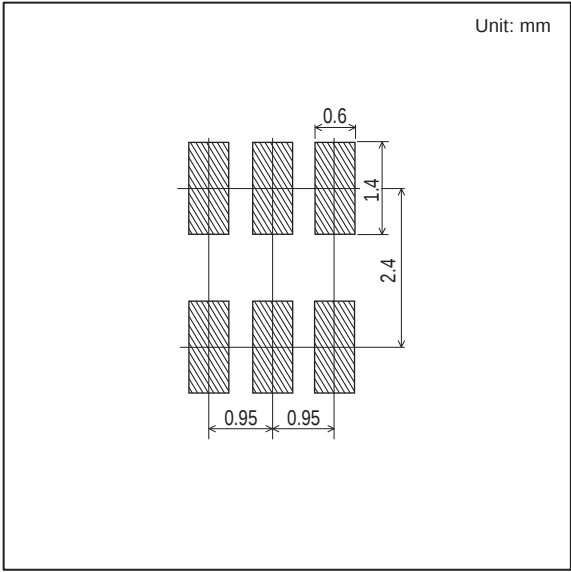


Those with pin 1 index on the feed hole side.....TL

Outline Drawing
CPH6347-TL-H



Land Pattern Example



Note on usage : Since the CPH6347 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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