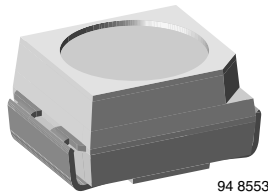


High Speed Infrared Emitting Diode, 850 nm, Surface Emitter Technology



FEATURES

- Package type: surface mount
- Package form: PLCC-2
- Dimensions (L x W x H in mm): 3.5 x 2.8 x 1.75
- Peak wavelength: $\lambda_p = 850$ nm
- High reliability
- High radiant power
- High radiant intensity
- Angle of half intensity: $\phi = \pm 60^\circ$
- Suitable for high pulse current operation
- Floor life: 168 h, MSL 3, acc. J-STD-020
- Lead (Pb)-free reflow soldering
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912



DESCRIPTION

As part of the [SurfLight™](#) portfolio, the VSMY3850 is an infrared, 850 nm emitting diode based on surface emitter technology with high radiant intensity, high optical power and high speed, molded in a PLCC-2 package for surface mounting (SMD).

RELEASED FOR APPLICATIONS

Infrared radiation source for operation with CMOS cameras (illumination)

- High speed IR data transmission
- IR touch panels
- 3D TV
- Light curtain

PRODUCT SUMMARY

COMPONENT	I_e (mW/sr)	ϕ (deg)	λ_p (nm)	t_r (ns)
VSMY3850	17	± 60	850	10

Note

- Test conditions see table “Basic Characteristics”

ORDERING INFORMATION

ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM
VSMY3850-GS08	Tape and reel	MOQ: 7500 pcs, 1500 pcs/reel	PLCC-2
VSMY3850-GS18	Tape and reel	MOQ: 8000 pcs, 8000 pcs/reel	PLCC-2

Note

- MOQ: minimum order quantity

ABSOLUTE MAXIMUM RATINGS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V_R	5	V
Forward current		I_F	100	mA
Pulse peak forward current	$t_p/T = 0.5$, $t_p = 100\text{ }\mu\text{s}$	I_{FM}	200	mA
Surge forward current	$t_p = 100\text{ }\mu\text{s}$	I_{FSM}	1	A
Power dissipation		P_V	200	mW
Junction temperature		T_j	100	$^{\circ}\text{C}$
Operating temperature range		T_{amb}	- 40 to + 85	$^{\circ}\text{C}$
Storage temperature range		T_{stg}	- 40 to + 100	$^{\circ}\text{C}$
Soldering temperature	acc. figure 7, J-STD-020	T_{sd}	260	$^{\circ}\text{C}$
Thermal resistance junction/ambient	J-STD-051, soldered on PCB	R_{thJA}	250	K/W

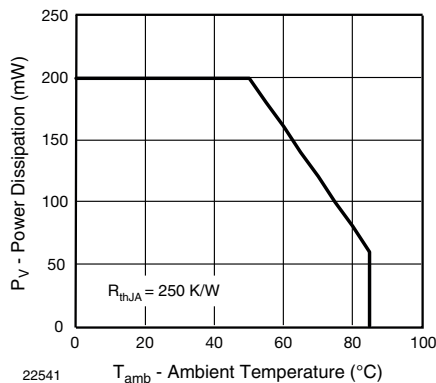


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

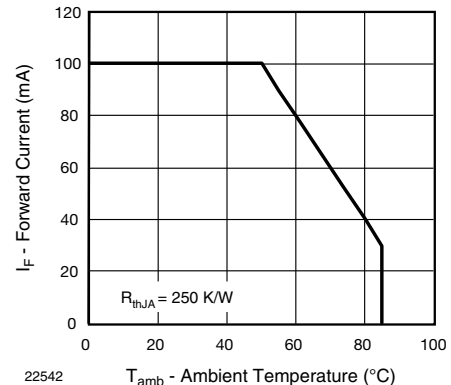
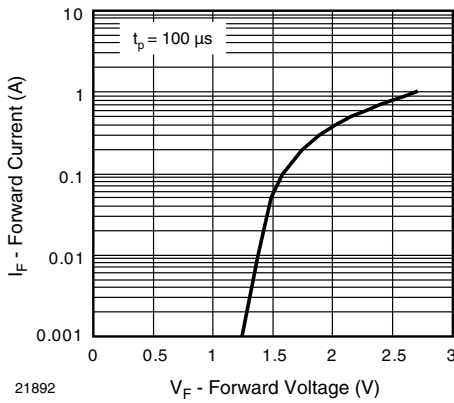


Fig. 2 - Forward Current Limit vs. Ambient Temperature

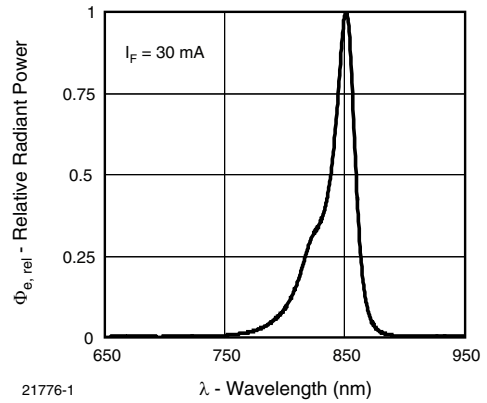
BASIC CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	$I_F = 100\text{ mA}$, $t_p = 20\text{ ms}$	V_F		1.6	2.0	V
	$I_F = 1\text{ A}$, $t_p = 100\text{ }\mu\text{s}$	V_F		2.9		V
Temperature coefficient of V_F	$I_F = 1\text{ mA}$	TK_{V_F}		- 1.45		mV/K
	$I_F = 10\text{ mA}$	TK_{V_F}		- 1.2		mV/K
Reverse current		I_R	not designed for reverse operation			μA
Junction capacitance	$V_R = 0\text{ V}$, $f = 1\text{ MHz}$, $E = 0$	C_j		125		pF
Radiant intensity	$I_F = 100\text{ mA}$, $t_p = 20\text{ ms}$	I_e	12	17	25	mW/sr
	$I_F = 1\text{ A}$, $t_p = 100\text{ }\mu\text{s}$	I_e		150		mW/sr
Radiant power	$I_F = 100\text{ mA}$, $t_p = 20\text{ ms}$	ϕ_e		55		mW
Temperature coefficient of ϕ_e	$I_F = 100\text{ mA}$	TK_{ϕ_e}		- 0.35		%/K
Angle of half intensity		ϕ		± 60		deg
Peak wavelength	$I_F = 100\text{ mA}$	λ_p	840	850	870	nm
Spectral bandwidth	$I_F = 30\text{ mA}$	$\Delta\lambda$		30		nm
Temperature coefficient of λ_p	$I_F = 100\text{ mA}$	TK_{λ_p}		0.25		nm/K
Rise time	$I_F = 100\text{ mA}$	t_r		10		ns
Fall time	$I_F = 100\text{ mA}$	t_f		10		ns
Virtual source diameter		d		0.44		mm

BASIC CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)



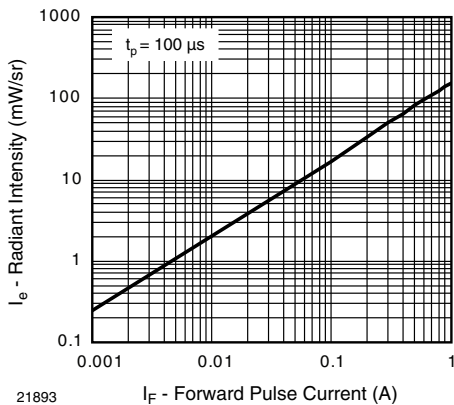
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Fig. 3 - Forward Current vs. Forward Voltage



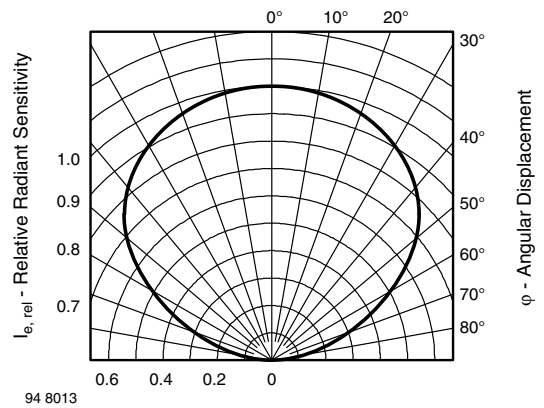
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Fig. 5 - Relative Radiant Power vs. Wavelength



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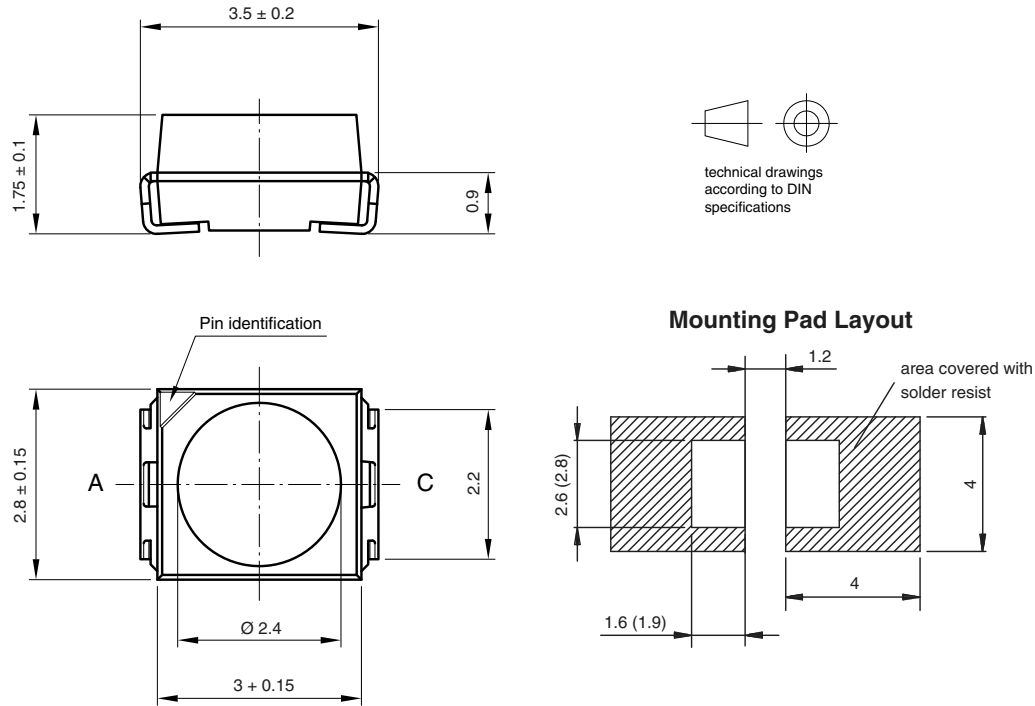
Fig. 4 - Radiant Intensity vs. Forward Current



94 8013

Fig. 6 - Relative Radiant Intensity vs. Angular Displacement

PACKAGE DIMENSIONS in millimeters



Drawing-No.: 6.541-5067.02-4
 Issue: 4; 19.07.10
 20767

SOLDER PROFILE

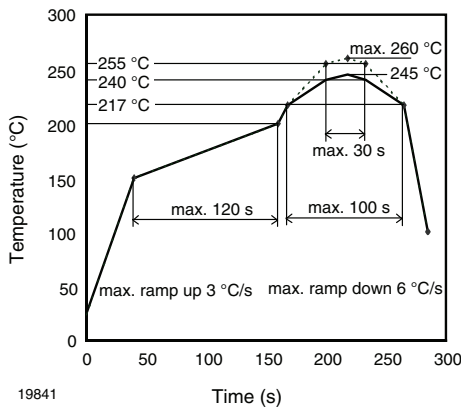


Fig. 7 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 168 h

Conditions: $T_{amb} < 30\text{ °C}$, $RH < 60\%$

Moisture sensitivity level 3, acc. to J-STD-020.

DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), $RH < 5\%$.

TAPE AND REEL

PLCC-2 components are packed in antistatic blister tape (DIN IEC (CO) 564) for automatic component insertion. Cavities of blister tape are covered with adhesive tape.

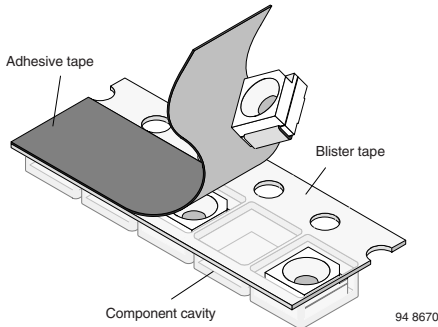


Fig. 8 - Blister Tape

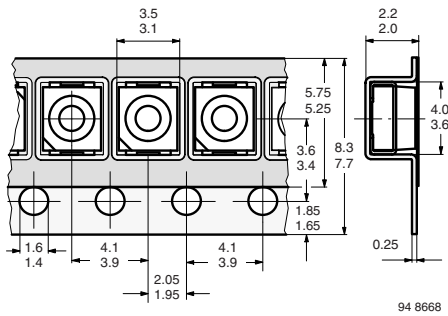


Fig. 9 - Tape Dimensions in mm for PLCC-2

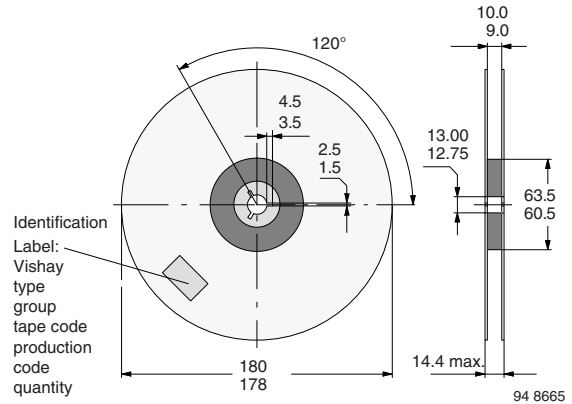


Fig. 11 - Dimensions of Reel-GS08

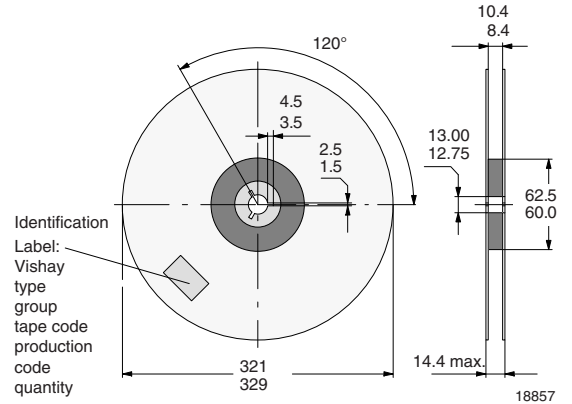


Fig. 12 - Dimensions of Reel-GS18

MISSING DEVICES

A maximum of 0.5 % of the total number of components per reel may be missing, exclusively missing components at the beginning and at the end of the reel. A maximum of three consecutive components may be missing, provided this gap is followed by six consecutive components.

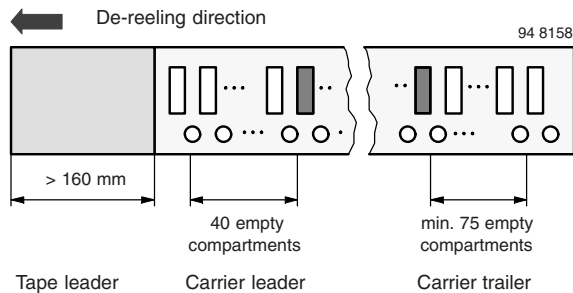


Fig. 10 - Beginning and End of Reel

The tape leader is at least 160 mm and is followed by a carrier tape leader with at least 40 empty compartments. The tape leader may include the carrier tape as long as the cover tape is not connected to the carrier tape. The least component is followed by a carrier tape trailer with a least 75 empty compartments and sealed with cover tape.

COVER TAPE REMOVAL FORCE

The removal force lies between 0.1 N and 1.0 N at a removal speed of 5 mm/s. In order to prevent components from popping out of the blisters, the cover tape must be pulled off at an angle of 180° with regard to the feed direction.



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