

Ultrafast recovery - 1200 V diode

Main product characteristics

$I_{F(AV)}$	12 A
V_{RRM}	1200 V
T_j	175° C
V_F (typ)	1.25 V
t_{rr} (typ)	50 ns

Features and benefits

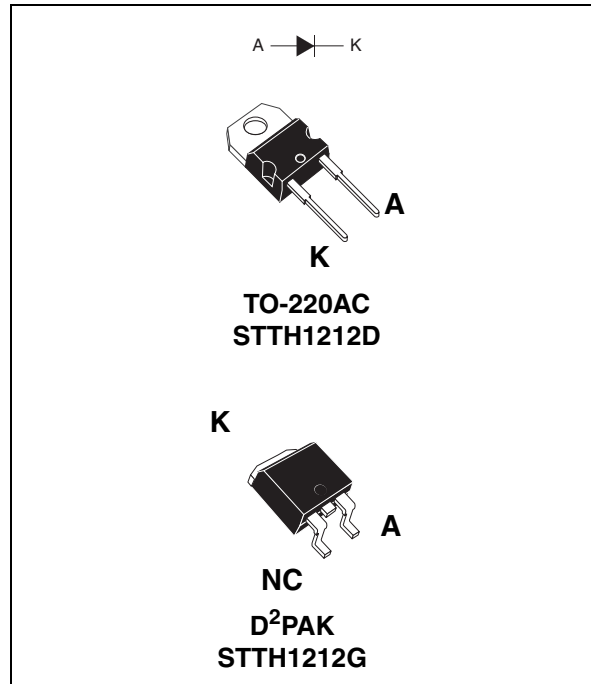
- Ultrafast, soft recovery
- Very low conduction and switching losses
- High frequency and/or high pulsed current operation
- High reverse voltage capability
- High junction temperature

Description

The high quality design of this diode has produced a device with low leakage current, regularly reproducible characteristics and intrinsic ruggedness. These characteristics make it ideal for heavy duty applications that demand long term reliability.

Such demanding applications include industrial power supplies, motor control, and similar mission-critical systems that require rectification and freewheeling. These diodes also fit into auxiliary functions such as snubber, bootstrap, and demagnetization applications.

The improved performance in low leakage current, and therefore thermal runaway guard band, is an immediate competitive advantage for this device.



Order codes

Part Number	Marking
STTH1212D	STTH1212D
STTH1212G	STTH1212G
STTH1212G-TR	STTH1212G

1 Characteristics

Table 1. Absolute ratings (limiting values at 25° C, unless otherwise specified)

Symbol	Parameter		Value	Unit
V_{RRM}	Repetitive peak reverse voltage		1200	V
$I_{F(RMS)}$	RMS forward current		30	A
$I_{F(AV)}$	Average forward current, $\delta = 0.5$	$T_c = 130^\circ \text{C}$	12	A
I_{FRM}	Repetitive peak forward current	$t_p = 5 \mu\text{s}$, $F = 5 \text{ kHz}$ square	160	A
I_{FSM}	Surge non repetitive forward current	$t_p = 10 \text{ ms}$ Sinusoidal	100	A
T_{stg}	Storage temperature range		-65 to + 175	°C
T_j	Maximum operating junction temperature		175	°C

Table 2. Thermal parameter

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	Junction to case	1.6	°C/W

Table 3. Static electrical characteristics

Symbol	Parameter	Test conditions		Min.	Typ	Max.	Unit
$I_R^{(1)}$	Reverse leakage current	$T_j = 25^\circ \text{C}$	$V_R = V_{RRM}$			10	μA
		$T_j = 125^\circ \text{C}$			7	70	
$V_F^{(2)}$	Forward voltage drop	$T_j = 25^\circ \text{C}$	$I_F = 12 \text{ A}$			2.2	V
		$T_j = 125^\circ \text{C}$			1.30	2.0	
		$T_j = 150^\circ \text{C}$			1.25	1.9	

1. Pulse test: $t_p = 5 \text{ ms}$, $\delta < 2 \%$

2. Pulse test: $t_p = 380 \mu\text{s}$, $\delta < 2 \%$

To evaluate the conduction losses use the following equation:

$$P = 1.5 \times I_{F(AV)} + 0.033 I_{F(RMS)}^2$$

Table 4. Dynamic characteristics

Symbol	Parameter	Test conditions	Min.	Typ	Max.	Unit
t_{rr}	Reverse recovery time	$I_F = 1\text{ A}$, $di_F/dt = -50\text{ A}/\mu\text{s}$, $V_R = 30\text{ V}$, $T_j = 25^\circ\text{C}$			100	ns
		$I_F = 1\text{ A}$, $di_F/dt = -100\text{ A}/\mu\text{s}$, $V_R = 30\text{ V}$, $T_j = 25^\circ\text{C}$		50	70	
I_{RM}	Reverse recovery current	$I_F = 12\text{ A}$, $di_F/dt = -200\text{ A}/\mu\text{s}$, $V_R = 600\text{ V}$, $T_j = 125^\circ\text{C}$		16	24	A
S	Softness factor	$I_F = 12\text{ A}$, $di_F/dt = -200\text{ A}/\mu\text{s}$, $V_R = 600\text{ V}$, $T_j = 125^\circ\text{C}$		2		
t_{fr}	Forward recovery time	$I_F = 12\text{ A}$, $di_F/dt = 50\text{ A}/\mu\text{s}$, $V_{FR} = 1.5 \times V_{Fmax}$, $T_j = 25^\circ\text{C}$			400	ns
V_{FP}	Forward recovery voltage	$I_F = 12\text{ A}$, $di_F/dt = 50\text{ A}/\mu\text{s}$, $T_j = 25^\circ\text{C}$		6		V

Figure 1. Conduction losses versus average current

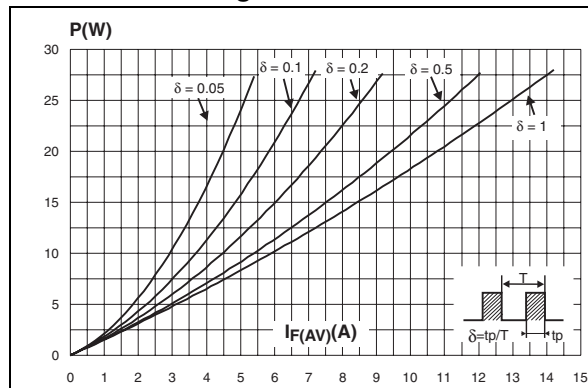


Figure 2. Forward voltage drop versus forward current

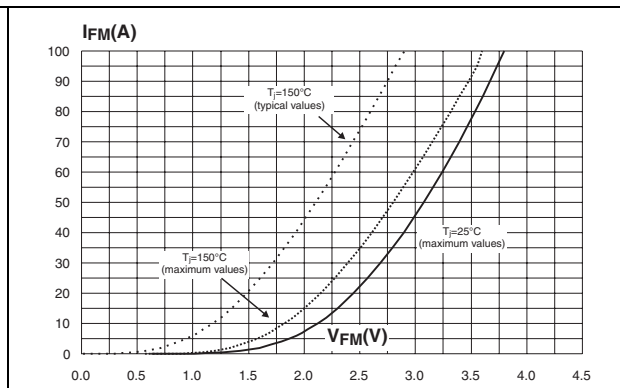


Figure 3. Relative variation of thermal impedance junction to case versus pulse duration

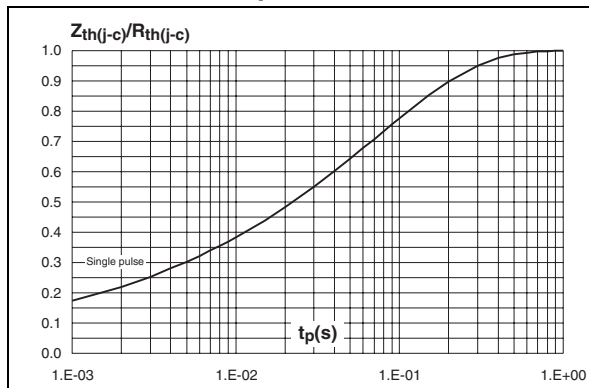


Figure 4. Peak reverse recovery current versus di_F/dt (typical values)

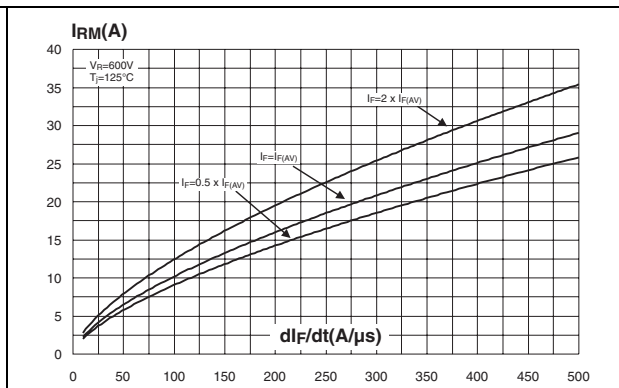


Figure 5. Reverse recovery time versus di_F/dt (typical values)

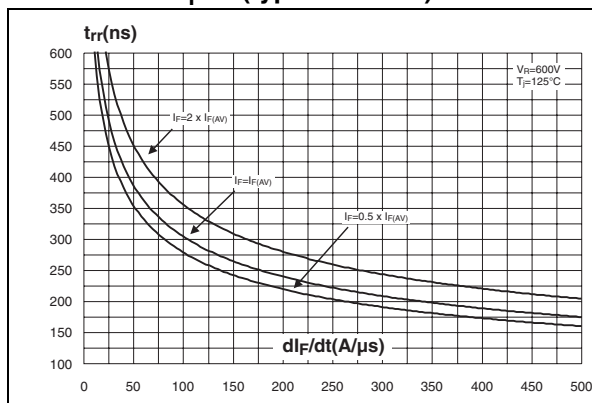


Figure 6. Reverse recovery charges versus di_F/dt (typical values)

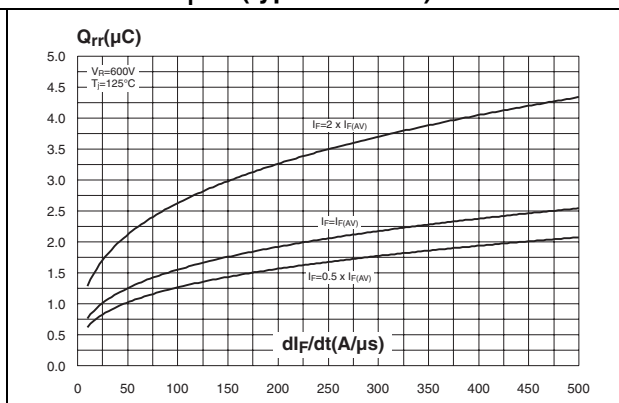


Figure 7. Softness factor versus di_F/dt (typical values)

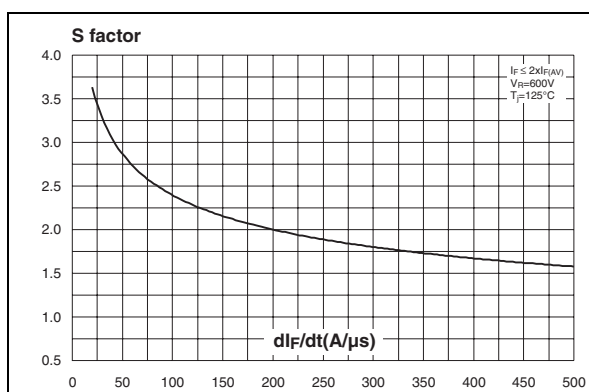


Figure 8. Relative variations of dynamic parameters versus junction temperature

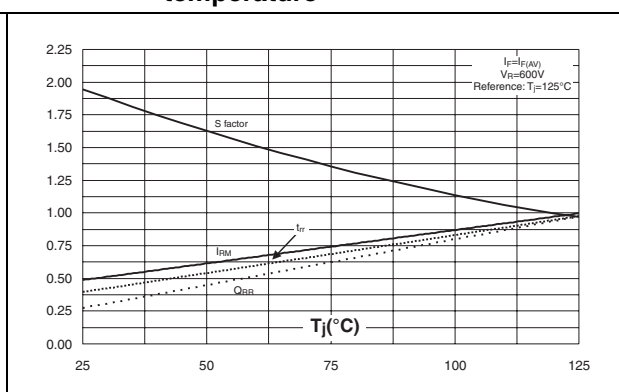


Figure 9. Transient peak forward voltage versus di_F/dt (typical values)

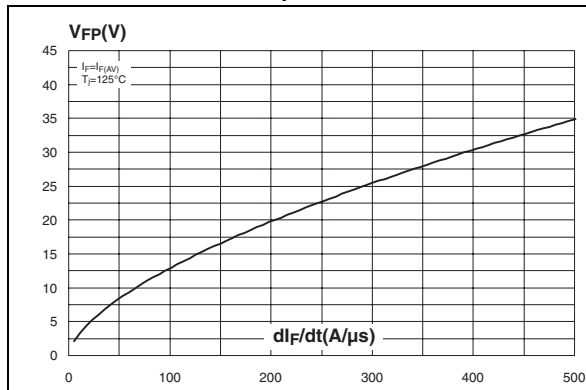


Figure 10. Forward recovery time versus di_F/dt (typical values)

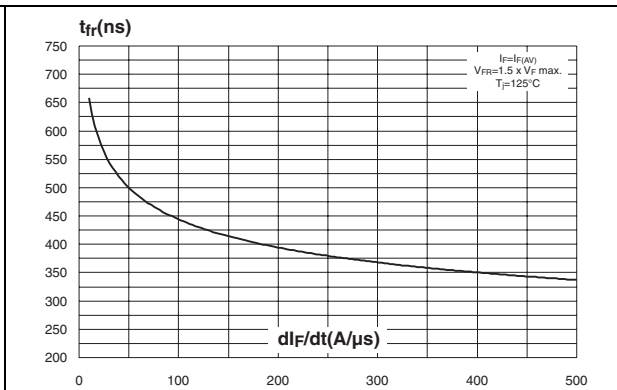


Figure 11. Junction capacitance versus reverse voltage applied (typical values)

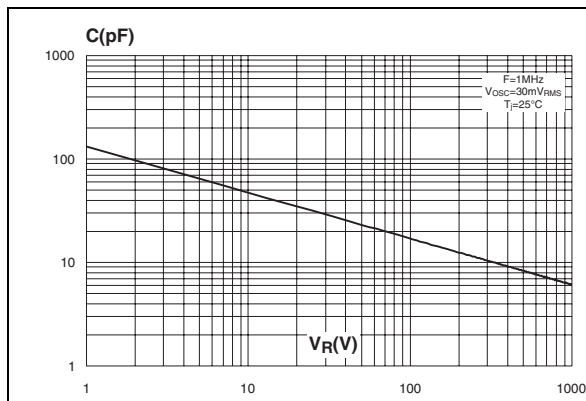
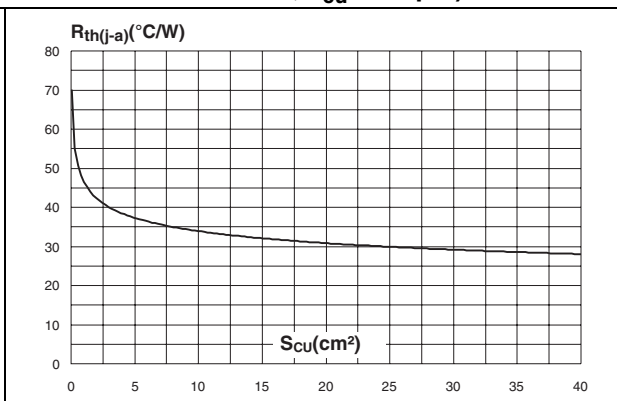


Figure 12. Thermal resistance junction to ambient versus copper surface under tab (Epoxy printed circuit board FR4, $e_{cu} = 35 \mu\text{m}$)



2 Package mechanical data

Epoxy meets UL94, V0

Cooling method: by conduction (C)

Recommended torque value: 0.55 Nm (TO-220AC)

Maximum torque value: 0.7 Nm (TO-220AC)

Table 5. T0-220AC dimensions

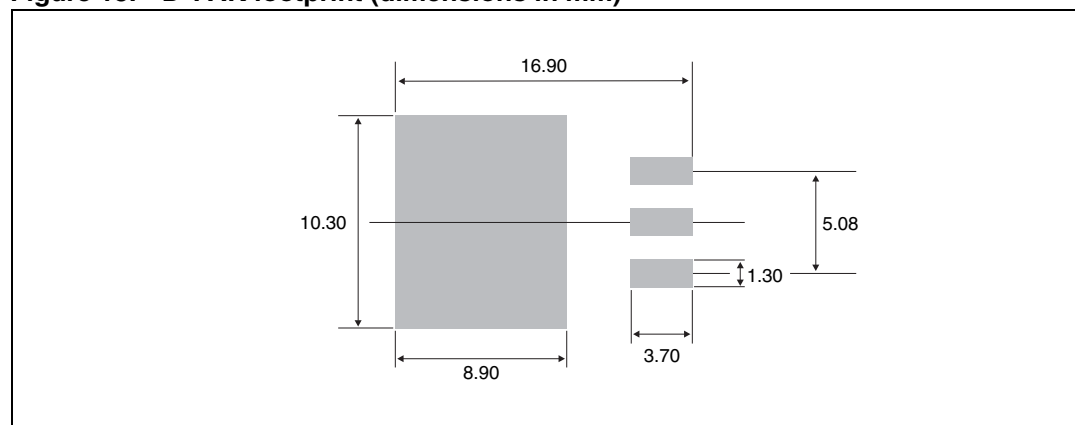
REF.	DIMENSIONS			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	4.40	4.60	0.173	0.181
C	1.23	1.32	0.048	0.051
D	2.40	2.72	0.094	0.107
E	0.49	0.70	0.019	0.027
F	0.61	0.88	0.024	0.034
F1	1.14	1.70	0.044	0.066
G	4.95	5.15	0.194	0.202
H2	10.00	10.40	0.393	0.409
L2	16.40 typ.		0.645 typ.	
L4	13.00	14.00	0.511	0.551
L5	2.65	2.95	0.104	0.116
L6	15.25	15.75	0.600	0.620
L7	6.20	6.60	0.244	0.259
L9	3.50	3.93	0.137	0.154
M	2.6 typ.		0.102 typ.	
Diam. I	3.75	3.85	0.147	0.151

Table 6. D²PAK dimensions

Technical drawing of a 2.5mm pitch connector showing top, side, and detail views with dimensions A through V2. The drawing includes a top view showing the connector's footprint with dimensions L, L2, L3, E, and G. A side view shows the profile with dimensions A, C2, C, and D. A detail view shows the internal structure with dimensions A1, C, R, A2, M, V2, and B2/B. A note indicates: * FLAT ZONE NO LESS THAN 2mm.

REF.	DIMENSIONS			
	Millimeters		Inches	
	Min.	Max	Min.	Max.
A	4.40	4.60	0.173	0.181
A1	2.49	2.69	0.098	0.106
A2	0.03	0.23	0.001	0.009
B	0.70	0.93	0.027	0.037
B2	1.14	1.70	0.045	0.067
C	0.45	0.60	0.017	0.024
C2	1.23	1.36	0.048	0.054
D	8.95	9.35	0.352	0.368
E	10.00	10.40	0.393	0.409
G	4.88	5.28	0.192	0.208
L	15.00	15.85	0.590	0.624
L2	1.27	1.40	0.050	0.055
L3	1.40	1.75	0.055	0.069
M	2.40	3.20	0.094	0.126
R	0.40 typ.		0.016 typ.	
V2	0°	8°	0°	8°

Figure 13. D²PAK footprint (dimensions in mm)



In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect . The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

3 Ordering information

Part Number	Marking	Package	Weight	Base qty	Delivery mode
STTH1212D	STTH1212D	TO-220AC	1.86 g	50	Tube
STTH1212G	STTH1212G	D ² PAK	1.48 g	50	Tube
STTH1212G-TR	STTH1212G	D ² PAK	1.48 g	1000	Tape & reel

4 Revision history

Date	Revision	Description of Changes
02-Mar-2006	1	First issue.

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