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November 2014

BC546 / BC547 / BC548 / BC549 / BC550 NPN Epitaxial Silicon Transistor

Features

• Switching and Amplifier

• High-Voltage: BC546, V_{CEO} = 65 V

• Low-Noise: BC549, BC550

• Complement to BC556, BC557, BC558, BC559, and BC560



Ordering Information

Part Number	Marking	Package	Packing Method
BC546ABU	BC546A	TO-92 3L	Bulk
BC546ATA	BC546A	TO-92 3L	Ammo
BC546BTA	BC546B	TO-92 3L	Ammo
BC546BTF	BC546B	TO-92 3L	Tape and Reel
BC546CTA	BC546C	TO-92 3L	Ammo
BC547ATA	BC547A	TO-92 3L	Ammo
BC547B	BC547B	TO-92 3L	Bulk
BC547BBU	BC547B	TO-92 3L	Bulk
BC547BTA	BC547B	TO-92 3L	Ammo
BC547BTF	BC547B	TO-92 3L	Tape and Reel
BC547CBU	BC547C	TO-92 3L	Bulk
BC547CTA	BC547C	TO-92 3L	Ammo
BC547CTFR	BC547C	TO-92 3L	Tape and Reel
BC548BU	BC548	TO-92 3L	Bulk
BC548BTA	BC548B	TO-92 3L	Ammo
BC548CTA	BC548C	TO-92 3L	Ammo
BC549BTA	BC549B	TO-92 3L	Ammo
BC549BTF	BC549B	TO-92 3L	Tape and Reel
BC549CTA	BC549C	TO-92 3L	Ammo
BC550CBU	BC550C	TO-92 3L	Bulk
BC550CTA	BC550C	TO-92 3L	Ammo

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}\text{C}$ unless otherwise noted.

Symbol	Param	Value	Unit				
		BC546	80				
V_{CBO}	Collector-Base Voltage	BC547 / BC550	50	V			
		BC548 / BC549	30	1			
		BC546	65				
V_{CEO}	Collector-Emitter Voltage	BC547 / BC550	45	V			
		BC548 / BC549	30				
V	Emitter Page Voltage	BC546 / BC547	6	V			
V _{EBO}	Emitter-Base Voltage	BC548 / BC549 / BC550	5]			
I _C	Collector Current (DC)	100	mA				
P _C	Collector Power Dissipation		500	mW			
T _J	Junction Temperature	150	°C				
T _{STG}	Storage Temperature Range	-65 to +150	°C				

Electrical Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol		Parameter	Conditions	Min.	Тур.	Max.	Unit
I _{CBO}	Collector Cut-Off Current		$V_{CB} = 30 \text{ V}, I_{E} = 0$			15	nA
h _{FE}	DC Curr	ent Gain	$V_{CE} = 5 \text{ V}, I_{C} = 2 \text{ mA}$	110		800	
\/ (oot)	Collector	-Emitter Saturation	$I_C = 10 \text{ mA}, I_B = 0.5 \text{ mA}$	90 250			mV
V _{CE} (sat)	Voltage		I _C = 100 mA, I _B = 5 mA		250	600	IIIV
\/ (oot)	Poss En	oittor Caturation Valtage	$I_C = 10 \text{ mA}, I_B = 0.5 \text{ mA}$		700		mV
V _{BE} (sat)	Dase-Ell	nitter Saturation Voltage	I _C = 100 mA, I _B = 5 mA	/	900		
\/ (on)	Dogo En	oitter On Veltage	$V_{CE} = 5 \text{ V}, I_{C} = 2 \text{ mA}$	580 660 700			m\/
V _{BE} (on) Base-En	nitter On Voltage	V _{CE} = 5 V, I _C = 10 mA			720	mV	
f _T	Current Gain Bandwidth Product		$V_{CE} = 5 \text{ V, } I_{C} = 10 \text{ mA,}$ f = 100 MHz		300		MHz
C _{ob}	Output C	apacitance	V _{CB} = 10 V, I _E = 0, f = 1 MHz		3.5	6.0	pF
C _{ib}	Input Capacitance		$V_{EB} = 0.5 \text{ V}, I_{C} = 0, f = 1 \text{ MHz}$		9		pF
		BC546 / BC547 / BC548	$V_{CE} = 5 \text{ V}, I_{C} = 200 \mu\text{A},$		2.0	10.0	dB
NE	NF Noise Figure	BC549 / BC550	$f = 1 \text{ kHz}, R_G = 2 \text{ k}\Omega$		1.2	4.0	
INF		BC549	$V_{CE} = 5 \text{ V}, I_{C} = 200 \mu\text{A},$		1.4	4.0	
		BC550	$R_G = 2 \text{ k}\Omega$, f = 30 to 15000 MHz		1.4	3.0	

h_{FE} Classification

Classification	A	В	С		
h _{FE}	110 ~ 220	200 ~ 450	420 ~ 800		

Typical Performance Characteristics

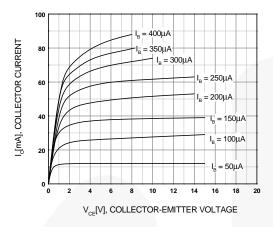


Figure 1. Static Characteristic

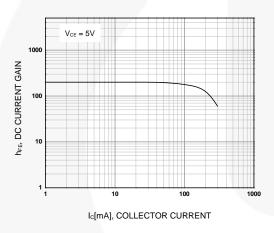


Figure 3. DC Current Gain

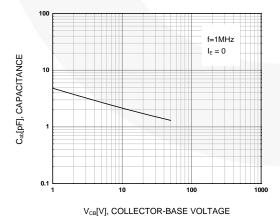


Figure 5. Output Capacitance

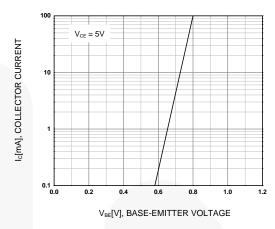


Figure 2. Transfer Characteristic

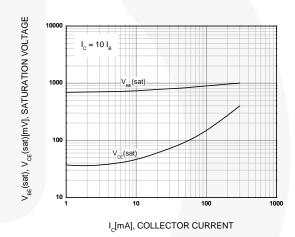


Figure 4. Base-Emitter Saturation Voltage and Collector-Emitter Saturation Voltage

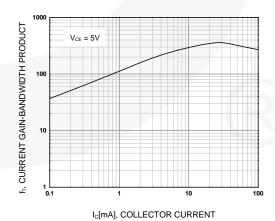
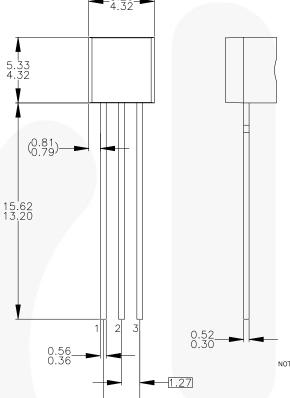


Figure 6. Current Gain Bandwidth Product

Physical Dimensions



2.54

2 3

4.19 3.05

NOTES: UNLESS OTHERWISE SPECIFIED

- DRAWING WITH REFERENCE TO JEDEC TO-92 RECOMMENDATIONS. ALL DIMENSIONS ARE IN MILLIMETERS. DRAWING CONFORMS TO ASME Y14.5M-1994. TO-92 (92,94,96,97,98) PIN CONFIGURATION:
- 92 94 96 97 98

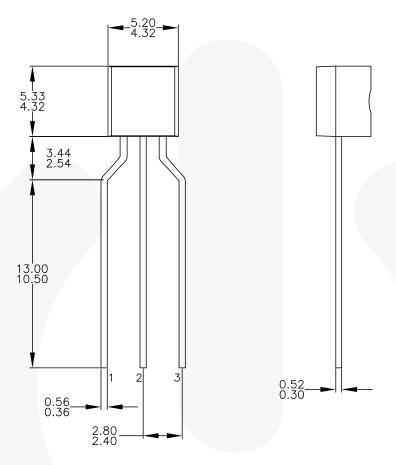
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₫	Ρ	F	М	Ρ	F	М	В	F	М	Ρ	F	М	Ρ	F	М
1	Ε	S	S	Ε	S	S	В	D	G	С	G	D	С	G	D
2	В	D	G	С	G	D	Ε	S	S	В	D	G	Ε	S	S
3	С	G	D	В	D	G	С	G	D	Ε	S	S	В	D	G
LEGEND: P - BIPOLAR															
E) FOR PACKAGE 92, 94, 96, 97 AND 98:															

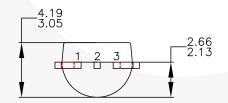
- FUR PACKAGE 92, 94, 96, 97 AND 99: PIN CONFIGURATION DRAIN "D" AND SOURCE "S" ARE INTERCHANGEAGLE AT JFET "F" OPTION. DRAWING FILENAME: MKT-ZAO3DREV3.

Figure 7. 3-Lead, TO-92, JEDEC TO-92 Compliant Straight Lead Configuration, Bulk Type

2.66 2.13

Physical Dimensions (Continued)





NOTES: UNLESS OTHERWISE SPECIFIED

- DRAWING CONFORMS TO JEDEC MS-013, VARIATION AC. ALL DIMENSIONS ARE IN MILLIMETERS. DRAWING CONFORMS TO ASME Y14.5M-2009. DRAWING FILENAME: MKT-ZAO3FREV3. FAIRCHILD SEMICONDUCTOR.

Figure 8. 3-Lead, TO-92, Molded, 0.2 In Line Spacing Lead Form, Ammo, Tape and Reel Type





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