

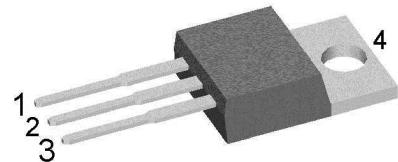
High Efficiency Thyristor

V_{RRM} = 1200 V
 I_{TAV} = 30 A
 V_T = 1.27 V

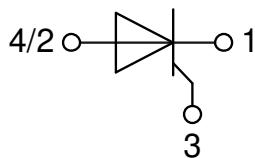
Single Thyristor

Part number

CLA30E1200PB



Backside: anode



Features / Advantages:

- Thyristor for line frequency
- Planar passivated chip
- Long-term stability

Applications:

- Line rectifying 50/60 Hz
- Softstart AC motor control
- DC Motor control
- Power converter
- AC power control
- Lighting and temperature control

Package: TO-220

- Industry standard outline
- RoHS compliant
- Epoxy meets UL 94V-0

Disclaimer Notice

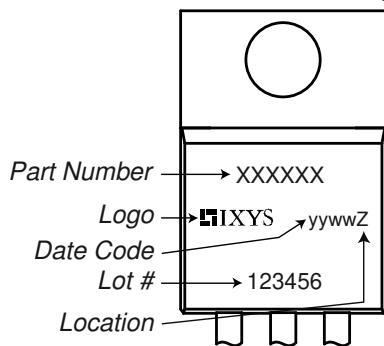
Information furnished is believed to be accurate and reliable. However, users should independently evaluate the suitability of and test each product selected for their own applications. Littelfuse products are not designed for, and may not be used in, all applications. Read complete Disclaimer Notice at www.littelfuse.com/disclaimer-electronics.

Thyristor

Symbol	Definition	Conditions	Ratings			
			min.	typ.	max.	
$V_{RSM/DSM}$	max. non-repetitive reverse/forward blocking voltage	$T_{VJ} = 25^\circ\text{C}$			1300	V
$V_{RRM/DRM}$	max. repetitive reverse/forward blocking voltage	$T_{VJ} = 25^\circ\text{C}$			1200	V
$I_{R/D}$	reverse current, drain current	$V_{R/D} = 1200 \text{ V}$ $V_{R/D} = 1200 \text{ V}$	$T_{VJ} = 25^\circ\text{C}$ $T_{VJ} = 125^\circ\text{C}$		10 2	μA mA
V_T	forward voltage drop	$I_T = 30 \text{ A}$	$T_{VJ} = 25^\circ\text{C}$		1.30	V
		$I_T = 60 \text{ A}$			1.59	V
		$I_T = 30 \text{ A}$	$T_{VJ} = 125^\circ\text{C}$		1.27	V
		$I_T = 60 \text{ A}$			1.65	V
I_{TAV}	average forward current	$T_C = 115^\circ\text{C}$	$T_{VJ} = 150^\circ\text{C}$		30	A
$I_{T(RMS)}$	RMS forward current	180° sine			47	A
V_{T0}	threshold voltage	$\left. \begin{array}{l} \text{slope resistance} \\ \end{array} \right\} \text{for power loss calculation only}$	$T_{VJ} = 150^\circ\text{C}$		0.86	V
r_T	slope resistance				13.2	$\text{m}\Omega$
R_{thJC}	thermal resistance junction to case				0.5	K/W
R_{thCH}	thermal resistance case to heatsink			0.5		K/W
P_{tot}	total power dissipation		$T_C = 25^\circ\text{C}$		250	W
I_{TSM}	max. forward surge current	$t = 10 \text{ ms}; (50 \text{ Hz}), \text{sine}$	$T_{VJ} = 45^\circ\text{C}$		300	A
		$t = 8,3 \text{ ms}; (60 \text{ Hz}), \text{sine}$	$V_R = 0 \text{ V}$		325	A
		$t = 10 \text{ ms}; (50 \text{ Hz}), \text{sine}$	$T_{VJ} = 150^\circ\text{C}$		255	A
		$t = 8,3 \text{ ms}; (60 \text{ Hz}), \text{sine}$	$V_R = 0 \text{ V}$		275	A
I^2t	value for fusing	$t = 10 \text{ ms}; (50 \text{ Hz}), \text{sine}$	$T_{VJ} = 45^\circ\text{C}$		450	A^2s
		$t = 8,3 \text{ ms}; (60 \text{ Hz}), \text{sine}$	$V_R = 0 \text{ V}$		440	A^2s
		$t = 10 \text{ ms}; (50 \text{ Hz}), \text{sine}$	$T_{VJ} = 150^\circ\text{C}$		325	A^2s
		$t = 8,3 \text{ ms}; (60 \text{ Hz}), \text{sine}$	$V_R = 0 \text{ V}$		315	A^2s
C_J	junction capacitance	$V_R = 400 \text{ V}$ $f = 1 \text{ MHz}$	$T_{VJ} = 25^\circ\text{C}$	13		pF
P_{GM}	max. gate power dissipation	$t_p = 30 \mu\text{s}$	$T_C = 150^\circ\text{C}$		10	W
		$t_p = 300 \mu\text{s}$			5	W
P_{GAV}	average gate power dissipation				0.5	W
$(di/dt)_{cr}$	critical rate of rise of current	$T_{VJ} = 150^\circ\text{C}; f = 50 \text{ Hz}$ repetitive, $I_T = 90 \text{ A}$			150	$\text{A}/\mu\text{s}$
		$t_p = 200 \mu\text{s}; di_G/dt = 0.3 \text{ A}/\mu\text{s};$				
		$I_G = 0.3 \text{ A}; V = \frac{2}{3} V_{DRM}$ non-repet., $I_T = 30 \text{ A}$			500	$\text{A}/\mu\text{s}$
$(dv/dt)_{cr}$	critical rate of rise of voltage	$V = \frac{2}{3} V_{DRM}$	$T_{VJ} = 150^\circ\text{C}$		500	$\text{V}/\mu\text{s}$
		$R_{GK} = \infty$; method 1 (linear voltage rise)				
V_{GT}	gate trigger voltage	$V_D = 6 \text{ V}$	$T_{VJ} = 25^\circ\text{C}$		1.3	V
			$T_{VJ} = -40^\circ\text{C}$		1.6	V
I_{GT}	gate trigger current	$V_D = 6 \text{ V}$	$T_{VJ} = 25^\circ\text{C}$		30	mA
			$T_{VJ} = -40^\circ\text{C}$		50	mA
V_{GD}	gate non-trigger voltage	$V_D = \frac{2}{3} V_{DRM}$	$T_{VJ} = 150^\circ\text{C}$		0.2	V
I_{GD}	gate non-trigger current				1	mA
I_L	latching current	$t_p = 10 \mu\text{s}$	$T_{VJ} = 25^\circ\text{C}$		90	mA
		$I_G = 0.3 \text{ A}; di_G/dt = 0.3 \text{ A}/\mu\text{s}$				
I_H	holding current	$V_D = 6 \text{ V}$ $R_{GK} = \infty$	$T_{VJ} = 25^\circ\text{C}$		60	mA
t_{gd}	gate controlled delay time	$V_D = \frac{1}{2} V_{DRM}$	$T_{VJ} = 25^\circ\text{C}$		2	μs
		$I_G = 0.3 \text{ A}; di_G/dt = 0.3 \text{ A}/\mu\text{s}$				
t_q	turn-off time	$V_R = 100 \text{ V}; I_T = 30 \text{ A}; V = \frac{2}{3} V_{DRM}$ $T_{VJ} = 125^\circ\text{C}$	$di/dt = 10 \text{ A}/\mu\text{s}$ $dv/dt = 20 \text{ V}/\mu\text{s}$ $t_p = 200 \mu\text{s}$	150		μs

Package TO-220

Symbol	Definition	Conditions	min.	typ.	max.	Unit
I_{RMS}	RMS current	per terminal			35	A
T_{VJ}	virtual junction temperature		-40		150	°C
T_{op}	operation temperature		-40		125	°C
T_{stg}	storage temperature		-40		150	°C
Weight				2		g
M_d	mounting torque		0.4		0.6	Nm
F_c	mounting force with clip		20		60	N

Product Marking

Part description

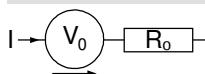
C = Thyristor (SCR)
 L = High Efficiency Thyristor
 A = (up to 1200V)
 30 = Current Rating [A]
 E = Single Thyristor
 1200 = Reverse Voltage [V]
 PB = TO-220AB (3)

Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	CLA30E1200PB	CLA30E1200PB	Tube	50	508228

Similar Part	Package	Voltage class
CLA30E1200HB	TO-247AD (3)	1200
CLA30E1200PC	TO-263AB (D2Pak) (2)	1200
CS22-12io1M	TO-220ABFP (3)	1200
CS22-08io1M	TO-220ABFP (3)	800

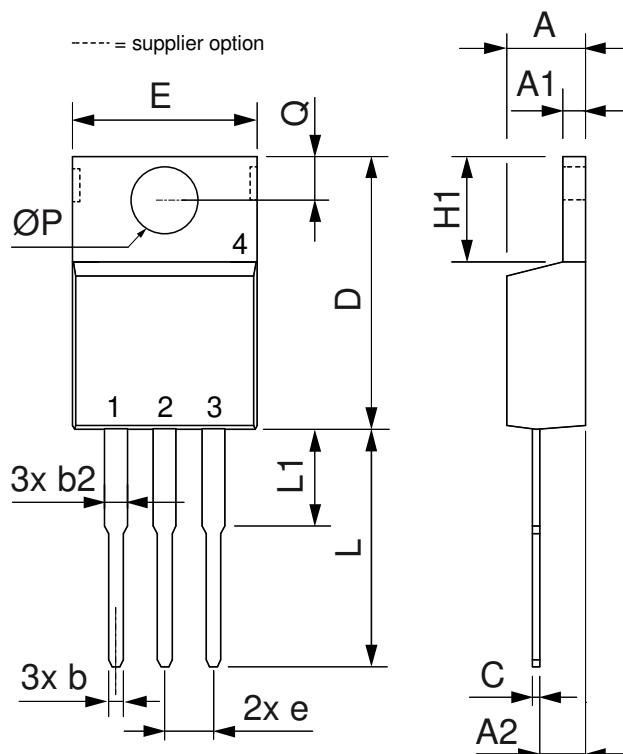
CMA30E1600PN	TO-220ABFP (3)	1600
CMA30E1600PB	TO-220AB (3)	1600

Equivalent Circuits for Simulation
** on die level*
 $T_{VJ} = 150^\circ\text{C}$

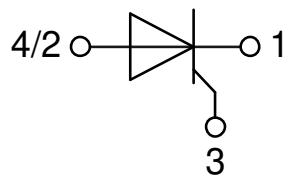
	Thyristor	
$V_{0\max}$	threshold voltage	0.86 V
$R_{0\max}$	slope resistance *	10 mΩ

Outlines TO-220

----- = supplier option



Dim.	Millimeter		Inches	
	Min.	Max.	Min.	Max.
A	4.32	4.82	0.170	0.190
A1	1.14	1.39	0.045	0.055
A2	2.29	2.79	0.090	0.110
b	0.64	1.01	0.025	0.040
b2	1.15	1.65	0.045	0.065
C	0.35	0.56	0.014	0.022
D	14.73	16.00	0.580	0.630
E	9.91	10.66	0.390	0.420
e	2.54	BSC	0.100	BSC
H1	5.85	6.85	0.230	0.270
L	12.70	13.97	0.500	0.550
L1	2.79	5.84	0.110	0.230
ØP	3.54	4.08	0.139	0.161
Q	2.54	3.18	0.100	0.125



Thyristor

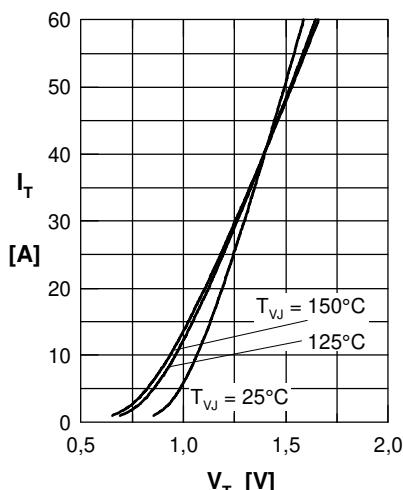


Fig. 1 Forward characteristics

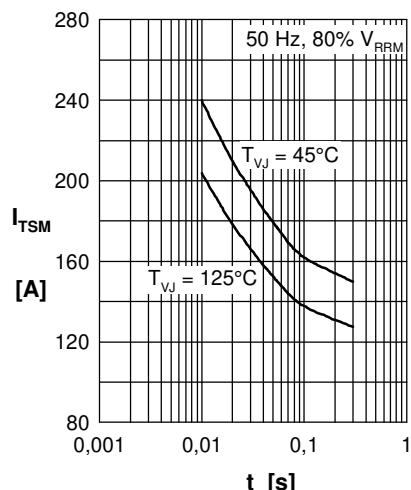


Fig. 2 Surge overload current
 I_{TSM} : crest value, t : duration

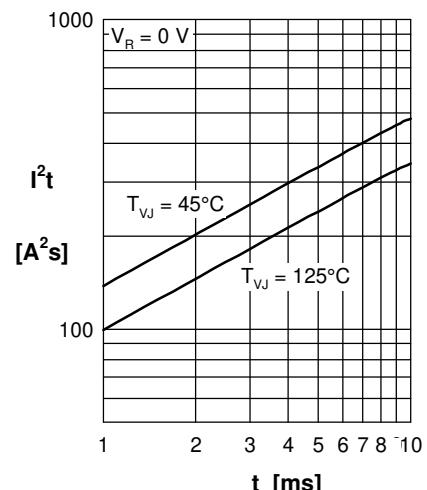


Fig. 3 I^2t versus time (1-10 s)

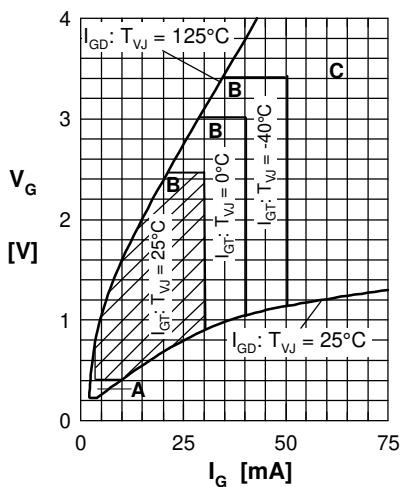


Fig. 4 Gate voltage & gate current
Triggering: A = no; B = possible; C = safe

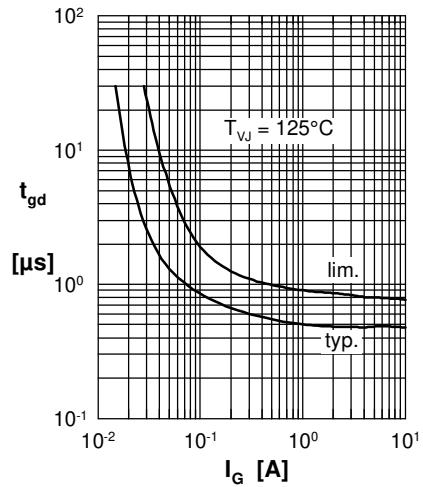


Fig. 5 Gate controlled delay time t_{gd}

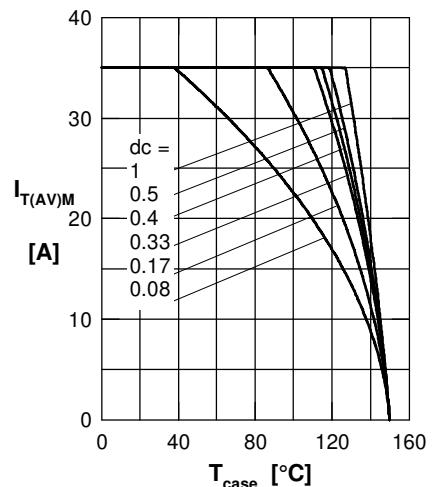


Fig. 6 Max. forward current at case temperature

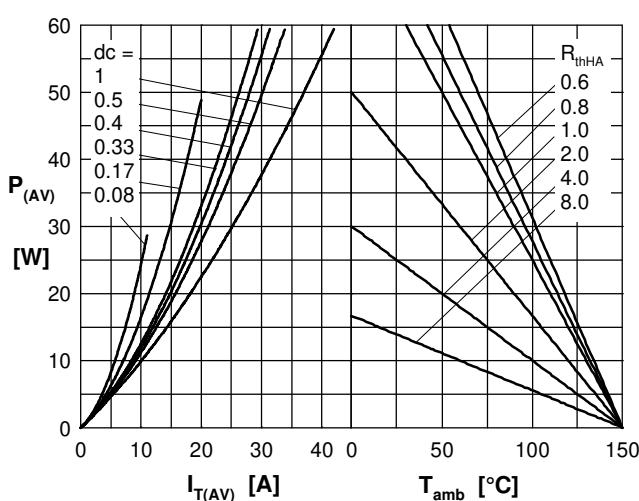


Fig. 7a Power dissipation versus direct output current
Fig. 7b and ambient temperature

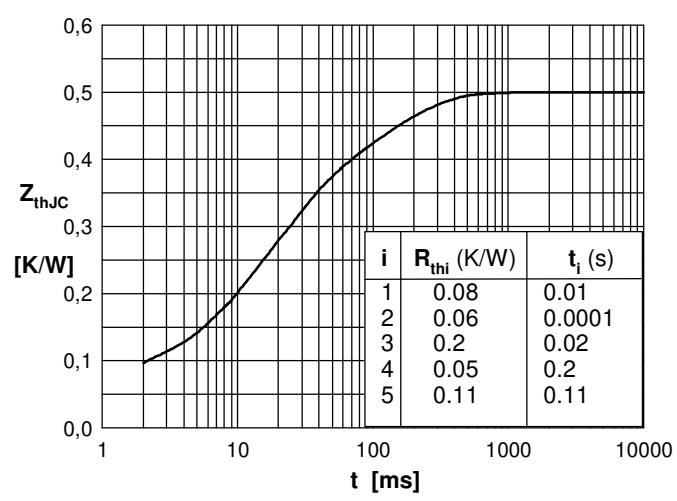


Fig. 7 Transient thermal impedance junction to case

Данный компонент на территории Российской Федерации**Вы можете приобрести в компании MosChip.**

Для оперативного оформления запроса Вам необходимо перейти по данной ссылке:

<http://moschip.ru/get-element>

Вы можете разместить у нас заказ для любого Вашего проекта, будь то серийное производство или разработка единичного прибора.

В нашем ассортименте представлены ведущие мировые производители активных и пассивных электронных компонентов.

Нашей специализацией является поставка электронной компонентной базы двойного назначения, продукции таких производителей как XILINX, Intel (ex.ALTERA), Vicor, Microchip, Texas Instruments, Analog Devices, Mini-Circuits, Amphenol, Glenair.

Сотрудничество с глобальными дистрибуторами электронных компонентов, предоставляет возможность заказывать и получать с международных складов практически любой перечень компонентов в оптимальные для Вас сроки.

На всех этапах разработки и производства наши партнеры могут получить квалифицированную поддержку опытных инженеров.

Система менеджмента качества компании отвечает требованиям в соответствии с ГОСТ Р ИСО 9001, ГОСТ Р В 0015-002 и ЭС РД 009

Офис по работе с юридическими лицами:

105318, г.Москва, ул.Щербаковская д.3, офис 1107, 1118, ДЦ «Щербаковский»

Телефон: +7 495 668-12-70 (многоканальный)

Факс: +7 495 668-12-70 (доб.304)

E-mail: info@moschip.ru

Skype отдела продаж:

moschip.ru
moschip.ru_4

moschip.ru_6
moschip.ru_9