

Product Summary

BV_{DS}	$R_{DS(ON)}$ Max	I_D $T_C = +25^\circ C$
40V	10m Ω @ $V_{GS} = 10V$	50A

Description and Applications

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

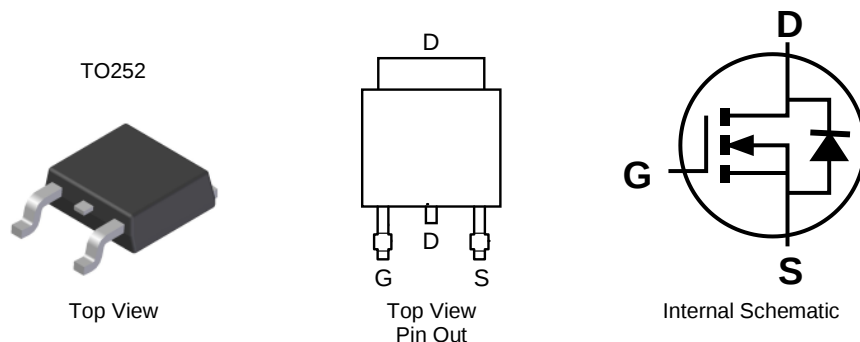
- DC-DC Converters
- Power Management Functions

Features and Benefits

- Rated to +175°C – Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching – ensures more reliable and robust end application
- Low On-Resistance
- Low Input Capacitance
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **PPAP Capable (Note 4)**

Mechanical Data

- Case: TO252
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 0.33 grams (Approximate)

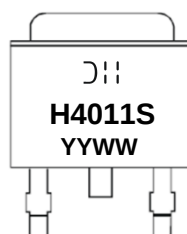


Ordering Information (Note 5)

Part Number	Case	Packaging
DMNH4011SK3Q-13	TO252	2,500/Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Please refer to http://www.diodes.com/product_compliance_definitions.html.
 5. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



= Manufacturer's Marking
 H4011S = Product Type Marking Code
 YYWW = Date Code Marking
 YY = Last Two Digits of Year
 (ex: 15 = 2015)
 WW = Week Code (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	40	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 7)	Steady State	T _C = +25°C	I _D	50	A
		T _C = +100°C		27	
Maximum Body Diode Forward Current (Note 7)			I _S	40	A
Pulsed Drain Current (380μs Pulse, Duty Cycle = 1%)			I _{DM}	120	A
Avalanche Current, L=0.1mH			I _{AS}	45	A
Avalanche Energy, L=0.1mH			E _{AS}	100	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 6)	P _D	2.6	W
Thermal Resistance, Junction to Ambient (Note 6)	R _{θJA}	47	°C/W
Total Power Dissipation (Note 7)	P _D	50	W
Thermal Resistance, Junction to Case (Note 7)	R _{θJC}	3	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	40	—	—	V	V _{GS} = 0V, I _D = 250µA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	µA	V _{DS} = 40V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	2	—	4	V	V _{DS} = V _{GS} , I _D = 250µA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	8.5	10	mΩ	V _{GS} = 10V, I _D = 50A
Diode Forward Voltage	V _{SD}	—	0.9	1.2	V	V _{GS} = 0V, I _S = 20A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	—	1,405	—	pF	V _{DS} = 20V, V _{GS} = 0V, f = 1MHz
Output Capacitance	C _{oss}	—	247	—		
Reverse Transfer Capacitance	C _{rss}	—	108	—		
Gate Resistance	R _g	—	2.2	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge	Q _g	—	25.5	—	nC	V _{DS} = 20V, V _{GS} = 10V, I _D = 50A
Gate-Source Charge	Q _{gs}	—	4.6	—		
Gate-Drain Charge	Q _{gd}	—	6.9	—		
Turn-On Delay Time	t _{D(ON)}	—	4.6	—	ns	V _{DD} = 20V, V _{GS} = 10V, I _D = 50A, R _g = 3.5Ω
Turn-On Rise Time	t _r	—	3.7	—		
Turn-Off Delay Time	t _{D(OFF)}	—	16	—		
Turn-Off Fall Time	t _f	—	5.1	—		
Body Diode Reverse Recovery Time	t _{RR}	—	22.1	—	ns	I _F = 50A, di/dt = 100A/µs
Body Diode Reverse Recovery Charge	Q _{RR}	—	13.4	—	nC	

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
 - Thermal resistance from junction to soldering point (on the exposed drain pad).
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.

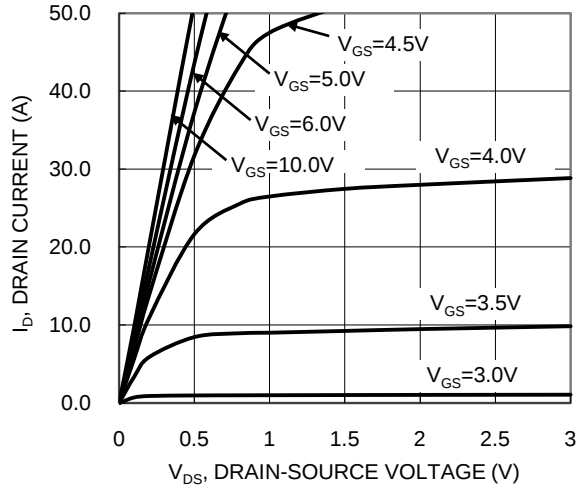


Figure 1. Typical Output Characteristic

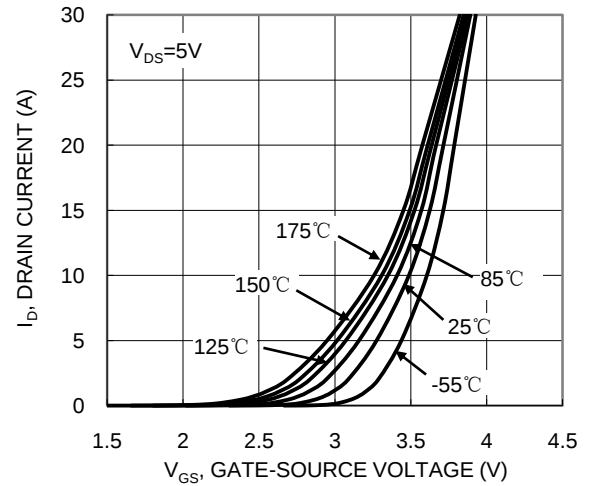


Figure 2. Typical Transfer Characteristic

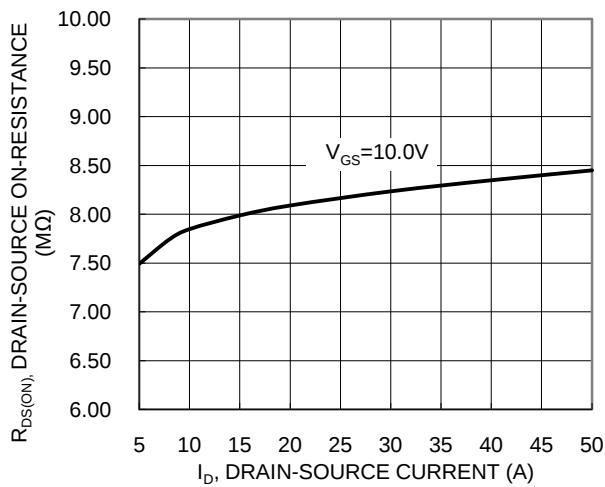


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

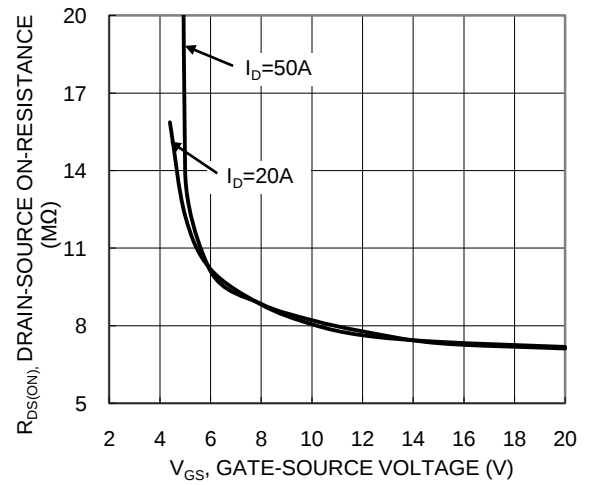


Figure 4. Typical Transfer Characteristic

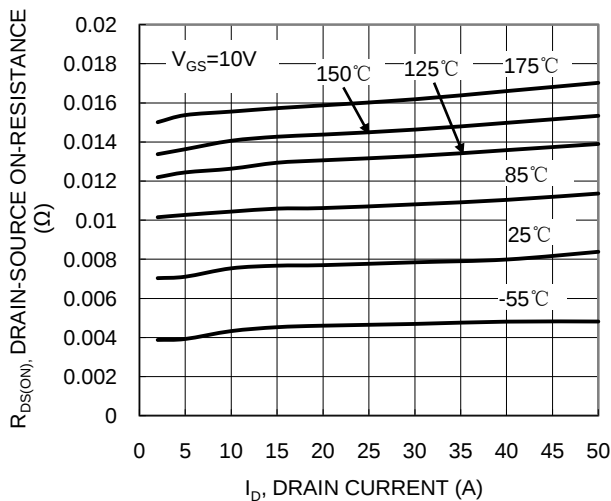


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

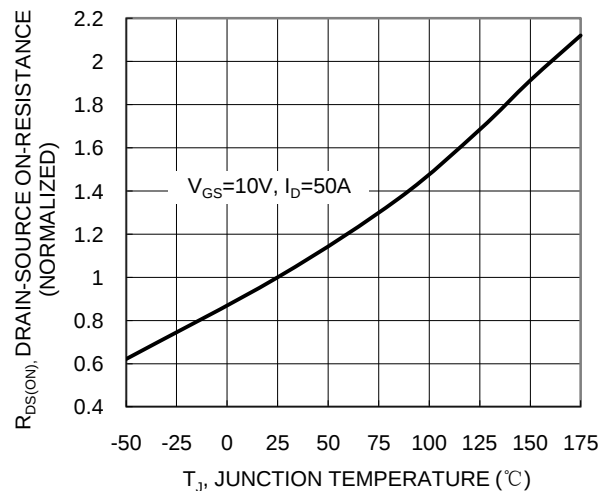
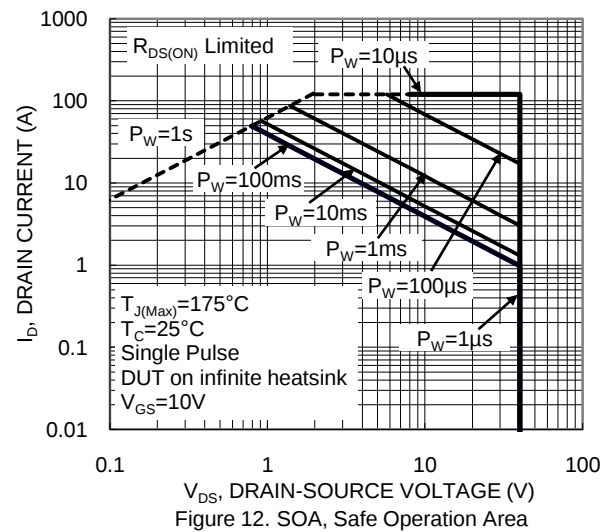
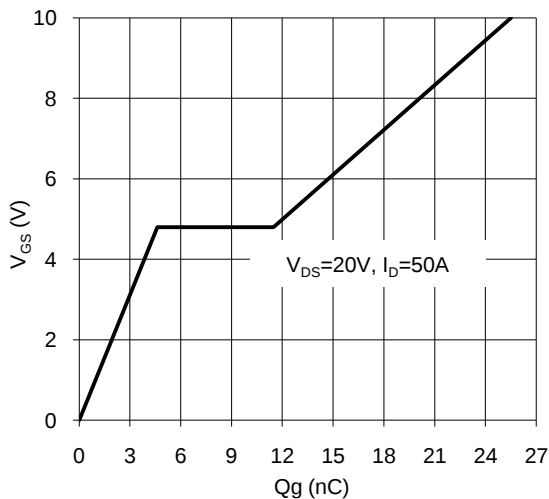
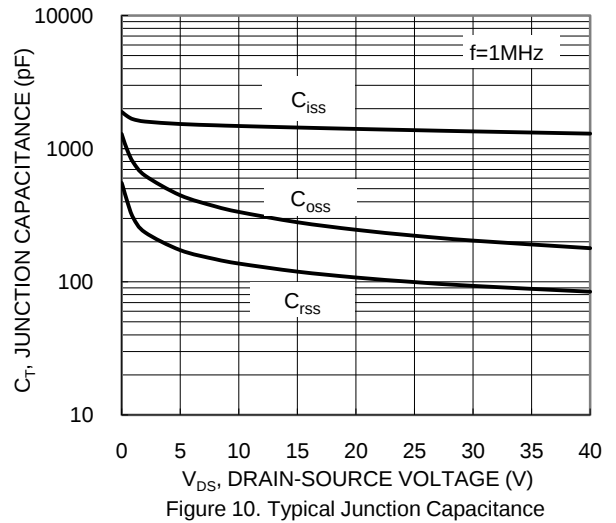
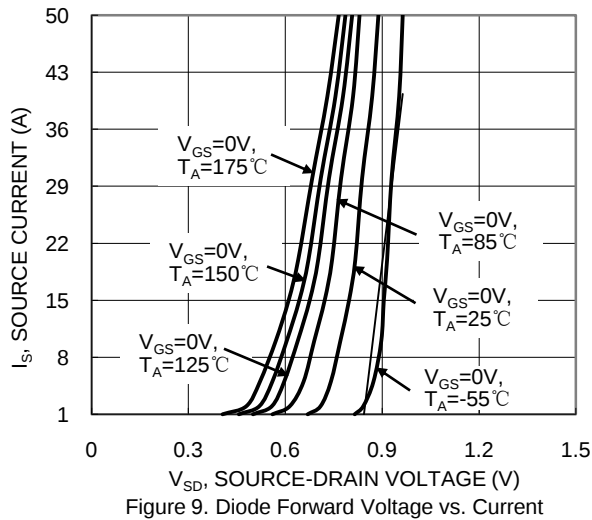
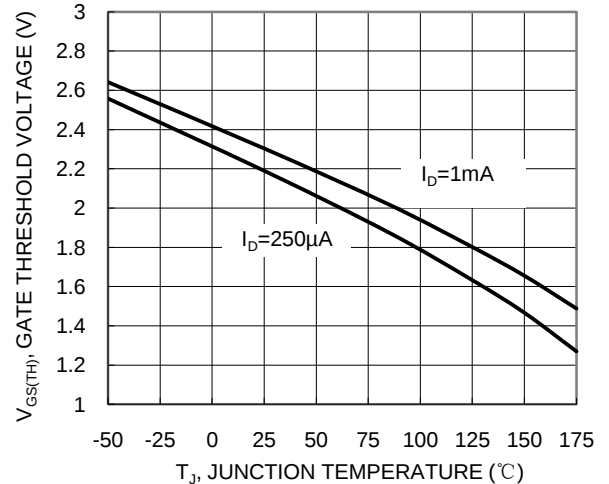
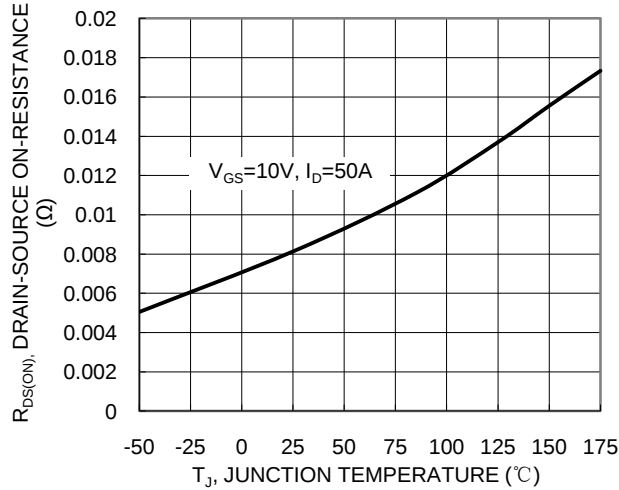


Figure 6. On-Resistance Variation with Temperature



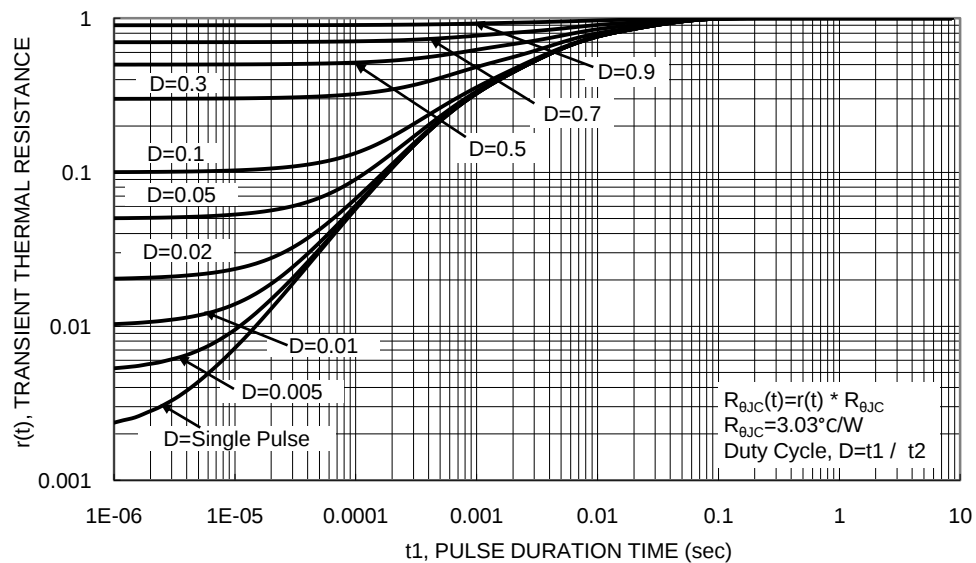
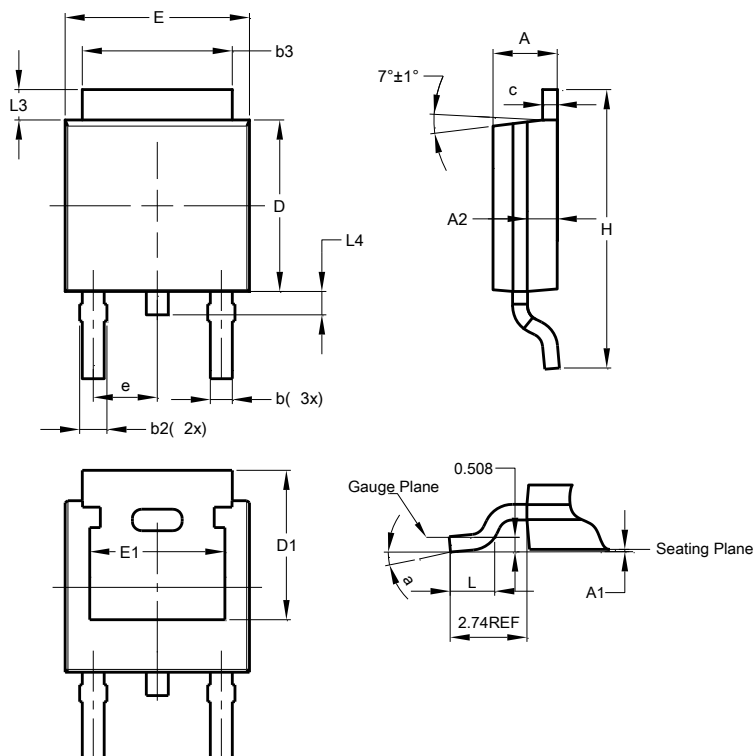


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.

TO252 (DPAK)

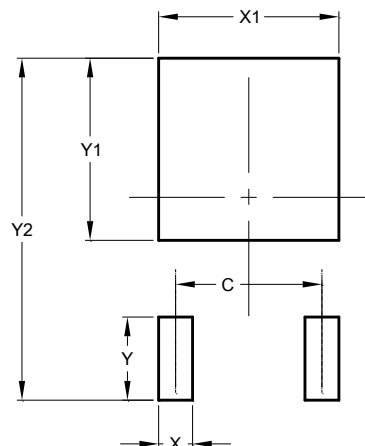


TO252 (DPAK)			
Dim	Min	Max	Typ
A	2.19	2.39	2.29
A1	0.00	0.13	0.08
A2	0.97	1.17	1.07
b	0.64	0.88	0.783
b2	0.76	1.14	0.95
b3	5.21	5.46	5.33
c	0.45	0.58	0.531
D	6.00	6.20	6.10
D1	5.21	-	-
e	-	-	2.286
E	6.45	6.70	6.58
E1	4.32	-	-
H	9.40	10.41	9.91
L	1.40	1.78	1.59
L3	0.88	1.27	1.08
L4	0.64	1.02	0.83
a	0°	10°	-
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.

TO252 (DPAK)



Dimensions	Value (in mm)
C	4.572
X	1.060
X1	5.632
Y	2.600
Y1	5.700
Y2	10.700

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Офис по работе с юридическими лицами:

105318, г.Москва, ул.Щербаковская д.3, офис 1107, 1118, ДЦ «Щербаковский»

Телефон: +7 495 668-12-70 (многоканальный)

Факс: +7 495 668-12-70 (доб.304)

E-mail: info@moschip.ru

Skype отдела продаж:

moschip.ru

moschip.ru_4

moschip.ru_6

moschip.ru_9