

EVALUATION KIT  
AVAILABLE

# DirectDrive Headphone Amplifier with External Gain

MAX9820

## General Description

The MAX9820 Windows Vista®-compliant stereo headphone amplifier is designed for portable equipment where board space is at a premium. It features Maxim's DirectDrive® architecture to produce a ground-referenced output from a single supply, eliminating the large output-coupling capacitors required by conventional single-supply headphone amplifiers.

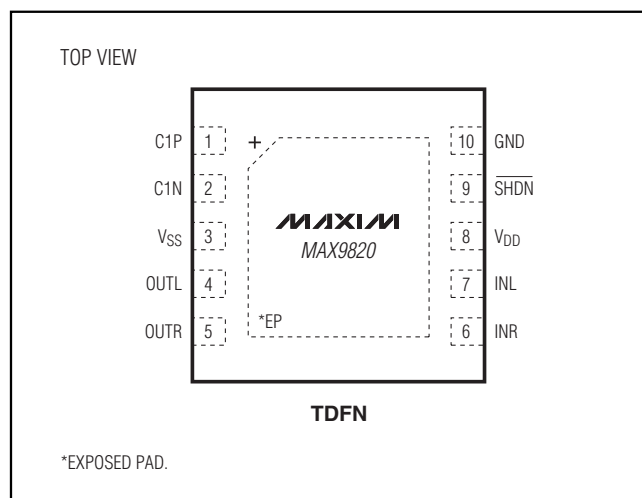
The MAX9820 features an undervoltage lockout that prevents over discharging of the battery during brownout conditions, click-and-pop suppression that eliminates audible transients on startup, a low-power shutdown mode, and thermal-overload and short-circuit protection. Additionally, the MAX9820 suppresses RF radiation received by input and supply traces acting as antennas and prevents the amplifier from demodulating the coupled noise.

The MAX9820 is available in a 10-pin TDFN package (3mm x 3mm x 0.8mm) and specified over the -40°C to +85°C extended temperature range.

## Applications

Cell Phones  
MP3 Players  
Notebook PCs  
PDAs

## Pin Configuration



## Features

- ◆ Clickless/Popless Operation
- ◆ High RF Noise Rejection
- ◆ Windows Vista Premium Mobile Compliant
- ◆ 2.7V to 5.5V Single-Supply Operation
- ◆ 95mW Output Power (32Ω, V<sub>DD</sub> = 5V)
- ◆ Low-Current Shutdown Mode, < 1μA
- ◆ Low 3mA (V<sub>DD</sub> = 3.3V) Quiescent Current
- ◆ Space-Saving, 3mm x 3mm, 10-Pin TDFN Package

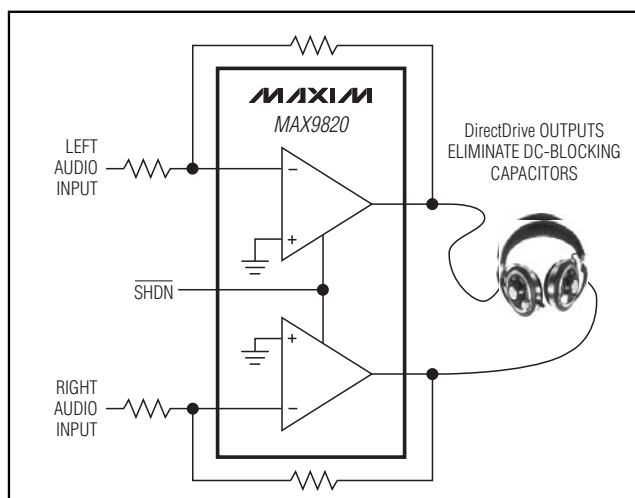
## Ordering Information

| PART        | TEMP RANGE     | PIN-PACKAGE | TOP MARK |
|-------------|----------------|-------------|----------|
| MAX9820ETB+ | -40°C to +85°C | 10 TDFN-EP* | AUU      |

+Denotes a lead(Pb)-free/RoHS-compliant package.

\*EP = Exposed pad.

## Simplified Block Diagram



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DirectDrive is a registered trademark of Maxim Integrated Products, Inc.



Maxim Integrated Products 1

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# DirectDrive Headphone Amplifier with External Gain

## ABSOLUTE MAXIMUM RATINGS

|                                                                 |                                   |
|-----------------------------------------------------------------|-----------------------------------|
| V <sub>DD</sub> to GND .....                                    | -0.3V to +6V                      |
| C1P to GND .....                                                | -0.3V to (V <sub>DD</sub> + 0.3V) |
| C1N to GND .....                                                | (V <sub>SS</sub> - 0.3V) to +0.3V |
| V <sub>SS</sub> to GND .....                                    | -6V to +0.3V                      |
| OUTR, OUTL to GND .....                                         | ±3V                               |
| SHDN to GND .....                                               | -0.3V to +6V                      |
| INR, INL to GND .....                                           | -0.3V to (V <sub>DD</sub> + 0.3V) |
| OUTR, OUTL Short Circuit to GND, V <sub>DD</sub> .....          | Continuous                        |
| Short Circuit Between OUTL and OUTR .....                       | Continuous                        |
| Continuous Input Current (Into All Other Pins) .....            | ±20mA                             |
| Continuous Power Dissipation (T <sub>A</sub> = +70°C)           |                                   |
| 10-Pin TDFN Single-Layer PCB (derate 18.5mW/°C                  |                                   |
| above +70°C) .....                                              | 1481.5mW                          |
| Junction-to-Case Thermal Resistance (θ <sub>JC</sub> ) (Note 1) |                                   |
| 10-Pin TDFN .....                                               | 8.5°C/W                           |

|                                                                    |                 |
|--------------------------------------------------------------------|-----------------|
| Junction-to-Ambient Thermal Resistance (θ <sub>JA</sub> ) (Note 1) |                 |
| 10-Pin TDFN .....                                                  | 41.0°C/W        |
| Continuous Power Dissipation (T <sub>A</sub> = +70°C)              |                 |
| 10-Pin TDFN Multilayer PCB (derate 24.4mW/°C                       |                 |
| above +70°C) .....                                                 | 1951mW          |
| Junction-to-Case Thermal Resistance (θ <sub>JC</sub> ) (Note 1)    |                 |
| 10-Pin TDFN .....                                                  | 9.0°C/W         |
| Junction-to-Ambient Thermal Resistance (θ <sub>JA</sub> ) (Note 1) |                 |
| 10-Pin TDFN .....                                                  | 41.0°C/W        |
| Operating Temperature Range .....                                  | -40°C to +85°C  |
| Storage Temperature Range .....                                    | -65°C to +150°C |
| Junction Temperature .....                                         | +150°C          |
| Lead Temperature (soldering, 10s) .....                            | +300°C          |

**Note 1:** Package thermal resistances were obtained using the method described in JEDEC specification JESD51-7, using a four-layer board. For detailed information on package thermal considerations, refer to [www.maxim-ic.com/thermal-tutorial](http://www.maxim-ic.com/thermal-tutorial).

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## ELECTRICAL CHARACTERISTICS

(V<sub>DD</sub> = V<sub>SHDN</sub> = 5V, V<sub>GND</sub> = 0V, R<sub>IN</sub> = R<sub>FB</sub> = 40.2kΩ (gain = -1V/V), C1 = C2 = 1μF, C3 = 10μF, R<sub>LOAD</sub> = ∞, T<sub>A</sub> = -40°C to +85°C, unless otherwise noted. Typical values are at T<sub>A</sub> = +25°C, unless otherwise noted.) (Note 2)

| PARAMETER                             | SYMBOL            | CONDITIONS                                                                                  | MIN             | TYP   | MAX  | UNITS |
|---------------------------------------|-------------------|---------------------------------------------------------------------------------------------|-----------------|-------|------|-------|
| <b>GENERAL</b>                        |                   |                                                                                             |                 |       |      |       |
| Supply Voltage Range                  | V <sub>DD</sub>   | Guaranteed by PSRR test                                                                     | 2.7             |       | 5.5  | V     |
| Undervoltage Lockout                  | UVLO              |                                                                                             | 2.2             |       |      | V     |
| Quiescent Current                     | I <sub>DD</sub>   | V <sub>DD</sub> = 3.3V                                                                      |                 | 3.0   | 4.6  | mA    |
|                                       |                   | V <sub>DD</sub> = 5V                                                                        |                 | 4.0   | 6.0  |       |
| Shutdown Current                      | I <sub>SHDN</sub> | V <sub>SHDN</sub> = 0V, T <sub>A</sub> = +25°C                                              |                 | < 0.1 | 1    | μA    |
| Output Signal Attenuation in Shutdown |                   | V <sub>SHDN</sub> = 0V, V <sub>IN</sub> = 1V <sub>RMS</sub> , R <sub>LOAD</sub> = 10kΩ      |                 | -110  |      | dBV   |
| Output Impedance in Shutdown          |                   | V <sub>SHDN</sub> = 0V                                                                      |                 | 0.6   |      | kΩ    |
| Turn-On Time                          | t <sub>ON</sub>   |                                                                                             |                 | 0.56  |      | ms    |
| Output Offset Voltage                 | V <sub>OS</sub>   | T <sub>A</sub> = +25°C (Note 3)                                                             |                 | ±0.1  | ±0.5 | mV    |
| Click-and-Pop Level                   | K <sub>CP</sub>   | Z <sub>LOAD</sub> = 32Ω + 1μH, peak voltage, A-weighted, 32 samples per second (Notes 3, 4) | Into shutdown   | -79   |      | dBV   |
|                                       |                   |                                                                                             | Out of shutdown | -77   |      |       |
|                                       |                   | Z <sub>LOAD</sub> = 10kΩ, peak voltage, A-weighted, 32 samples per second (Notes 3, 4)      | Into shutdown   | -62   |      |       |
|                                       |                   |                                                                                             | Out of shutdown | -58   |      |       |

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MAX9820

## ELECTRICAL CHARACTERISTICS (continued)

( $V_{DD} = V_{SHDN} = 5V$ ,  $V_{GND} = 0V$ ,  $R_{IN} = R_{FB} = 40.2k\Omega$  (gain =  $-1V/V$ ),  $C1 = C2 = 1\mu F$ ,  $C3 = 10\mu F$ ,  $R_{LOAD} = \infty$ ,  $T_A = -40^\circ C$  to  $+85^\circ C$ , unless otherwise noted. Typical values are at  $T_A = +25^\circ C$ , unless otherwise noted.) (Note 2)

| PARAMETER                            | SYMBOL               | CONDITIONS                                                               |                                                                                                         | MIN   | TYP | MAX | UNITS |
|--------------------------------------|----------------------|--------------------------------------------------------------------------|---------------------------------------------------------------------------------------------------------|-------|-----|-----|-------|
| Power-Supply Rejection Ratio         | PSRR                 | V <sub>DD</sub> = 2.7V to 5.5V, T <sub>A</sub> = +25°C (Note 3)          |                                                                                                         | 75    | 90  |     | dB    |
|                                      |                      | f = 1kHz, 200mV <sub>P-P</sub> (Note 3)                                  |                                                                                                         |       | 73  |     |       |
|                                      |                      | f = 20kHz, 200mV <sub>P-P</sub> (Note 3)                                 |                                                                                                         |       | 55  |     |       |
| Output Power                         | P <sub>OUT</sub>     | Z <sub>LOAD</sub> = 32Ω + 1μH, f = 1kHz, THD+N = 1%                      | V <sub>DD</sub> = 3.6V                                                                                  | 45    |     | mW  |       |
|                                      |                      |                                                                          | V <sub>DD</sub> = 5.0V                                                                                  | 95    |     |     |       |
|                                      |                      | Z <sub>LOAD</sub> = 16Ω + 1μH, f = 1kHz, THD+N = 1%                      | V <sub>DD</sub> = 3.6V                                                                                  | 32    |     |     |       |
|                                      |                      |                                                                          | V <sub>DD</sub> = 5.0V                                                                                  | 75    |     |     |       |
| Total Harmonic Distortion Plus Noise | THD+N                | Z <sub>LOAD</sub> = 16Ω + 1μH, f = 1kHz, P <sub>OUT</sub> = 20mW         |                                                                                                         | 0.014 |     | %   |       |
|                                      |                      | Z <sub>LOAD</sub> = 32Ω + 1μH, f = 1kHz, P <sub>OUT</sub> = 20mW         |                                                                                                         | 0.005 |     |     |       |
|                                      |                      | Z <sub>LOAD</sub> = 10kΩ, f = 1kHz, V <sub>OUT</sub> = 1V <sub>RMS</sub> |                                                                                                         | 0.001 |     |     |       |
| Signal-to-Noise Ratio                | SNR                  | Z <sub>LOAD</sub> = 32Ω + 1μH, P <sub>OUT</sub> = 25mW, A-weighted       |                                                                                                         | 105   |     | dB  |       |
| Output Noise                         | V <sub>NOISE</sub>   | A-weighted (Note 3)                                                      |                                                                                                         | 9     |     | μV  |       |
| Crosstalk                            |                      | L to R, R to L, BW = 20Hz to 15kHz                                       | Z <sub>LOAD</sub> = 32Ω + 1μH<br>FS = 0.300V <sub>RMS</sub> ,<br>V <sub>OUT</sub> = 30mV <sub>RMS</sub> | 73    |     | dB  |       |
|                                      |                      |                                                                          | Z <sub>LOAD</sub> = 10kΩ<br>FS = 0.707V <sub>RMS</sub> ,<br>V <sub>OUT</sub> = 70.7mV <sub>RMS</sub>    | 73    |     |     |       |
| Capacitive Load Drive                | C <sub>L</sub>       | No sustained oscillations                                                |                                                                                                         | 200   |     | pF  |       |
| Oscillator Frequency                 | f <sub>OSC</sub>     | T <sub>A</sub> = +25°C                                                   |                                                                                                         | 300   | 500 | 800 | kHz   |
| Thermal Shutdown                     |                      |                                                                          |                                                                                                         | 145   |     | °C  |       |
| Thermal Shutdown Hysteresis          |                      |                                                                          |                                                                                                         | 15    |     | °C  |       |
| DIGITAL INPUT (SHDN)                 |                      |                                                                          |                                                                                                         |       |     |     |       |
| Input Voltage High                   | V <sub>INH</sub>     |                                                                          |                                                                                                         | 1.2   |     |     | V     |
| Input Voltage Low                    | V <sub>INL</sub>     |                                                                          |                                                                                                         |       |     | 0.3 | V     |
| Input Leakage Current                | I <sub>LEAKAGE</sub> | T <sub>A</sub> = +25°C                                                   |                                                                                                         |       |     | ±1  | μA    |

**Note 2:** All specifications are 100% tested at  $T_A = +25^\circ C$ ; temperature limits are guaranteed by design.

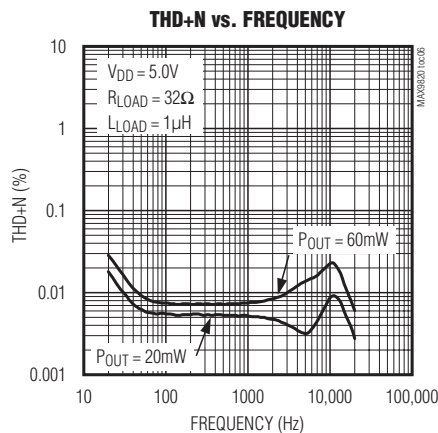
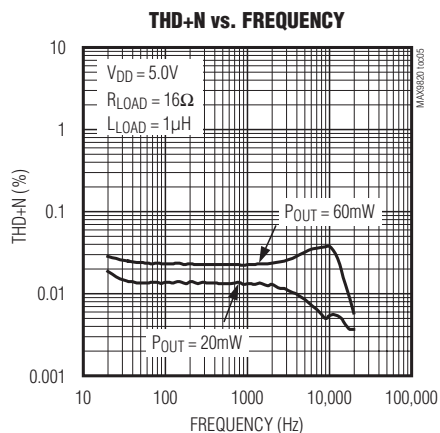
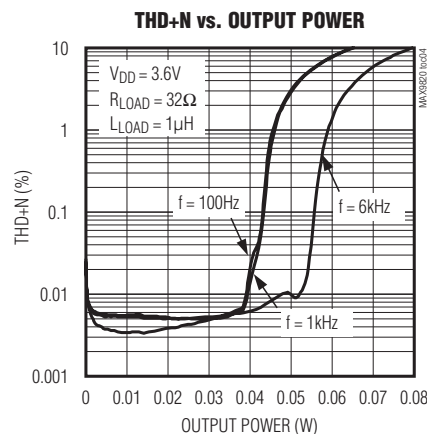
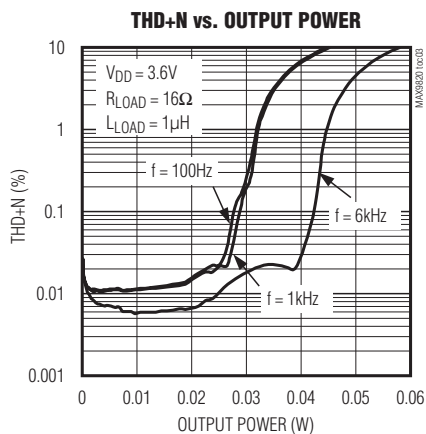
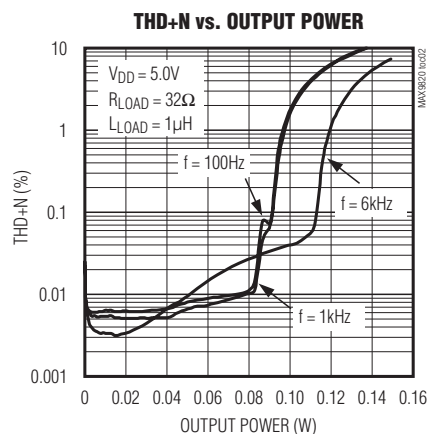
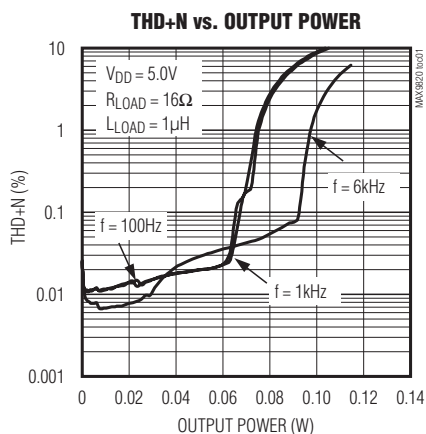
**Note 3:** The amplifier inputs are AC-coupled to GND.

**Note 4:** Mode transitions are controlled by  $\overline{SHDN}$ .

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## Typical Operating Characteristics

( $V_{DD} = V_{SHDN} = 5V$ ,  $V_{GND} = 0V$ ,  $R_{IN} = R_{FB} = 40.2k\Omega$  (gain =  $-1V/V$ ),  $C_1 = C_2 = 1\mu F$ ,  $C_3 = 10\mu F$ ,  $R_{LOAD} = \infty$ . Typical values are at  $T_A = +25^\circ C$ , unless otherwise noted.)

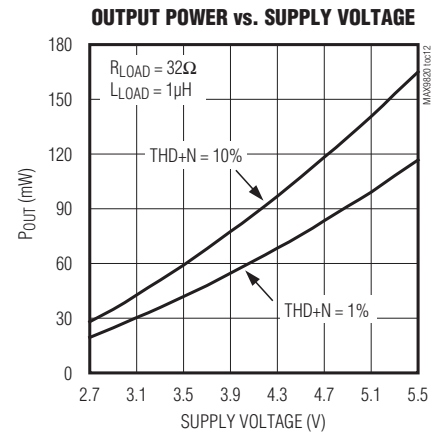
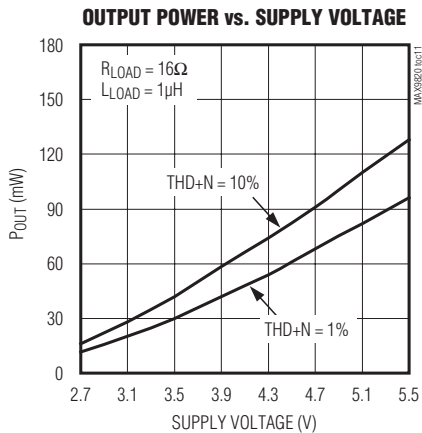
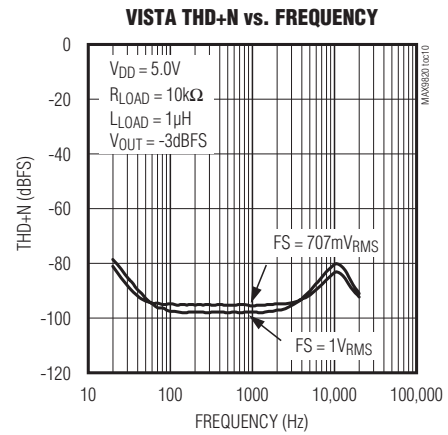
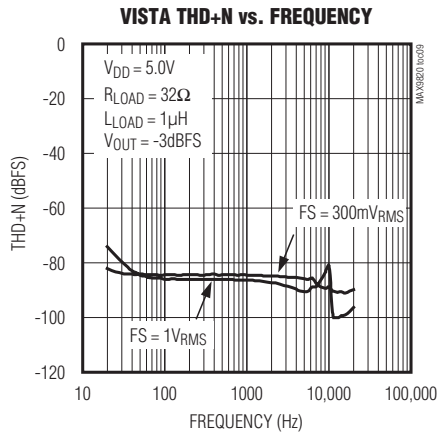
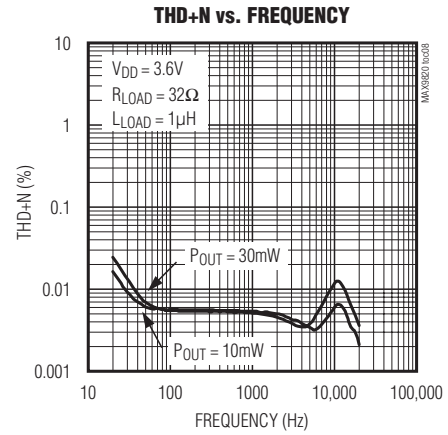
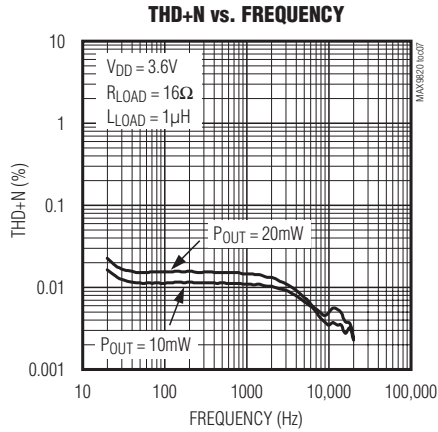


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MAX9820

## Typical Operating Characteristics

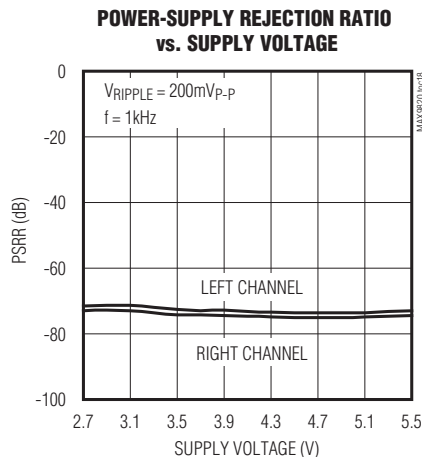
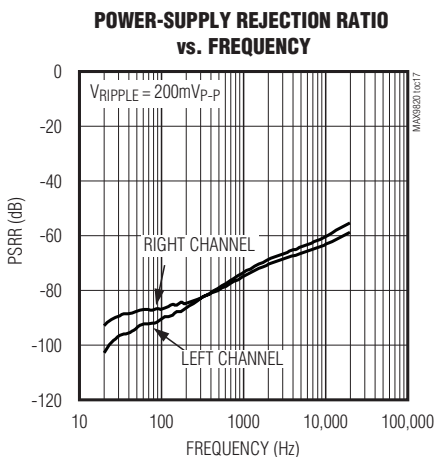
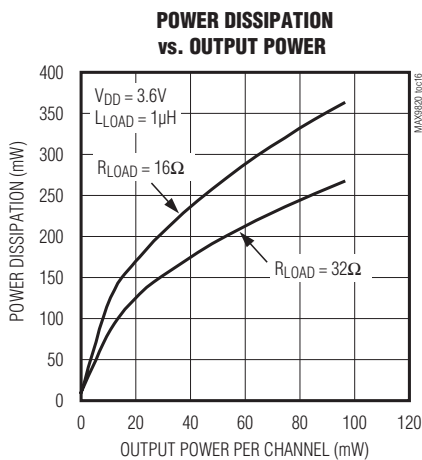
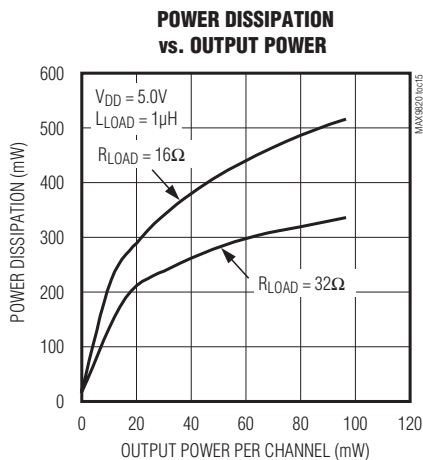
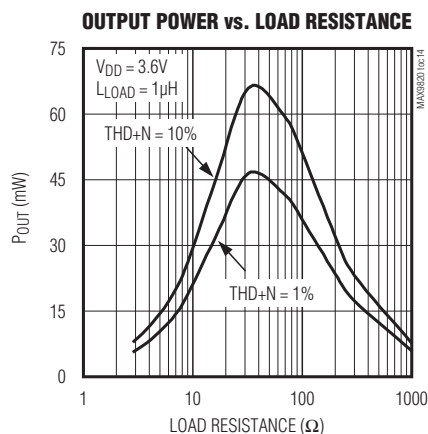
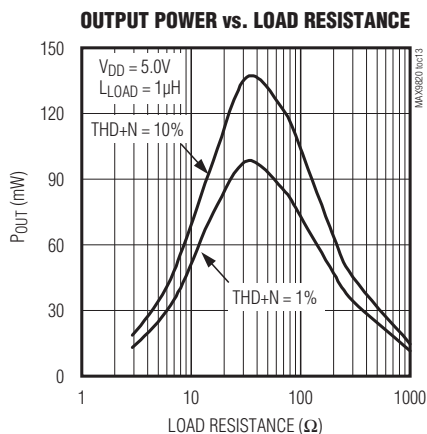
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# DirectDrive Headphone Amplifier with External Gain

## Typical Operating Characteristics (continued)

( $V_{DD} = V_{SHDN} = 5V$ ,  $V_{GND} = 0V$ ,  $R_{IN} = R_{FB} = 40.2k\Omega$  (gain =  $-1V/V$ ),  $C1 = C2 = 1\mu F$ ,  $C3 = 10\mu F$ ,  $R_{LOAD} = \infty$ . Typical values are at  $T_A = +25^\circ C$ , unless otherwise noted.)

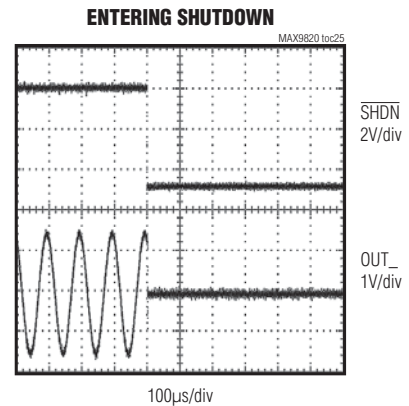
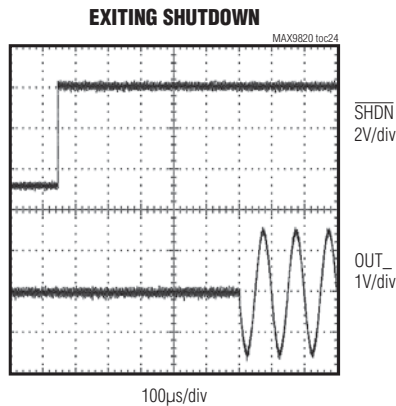
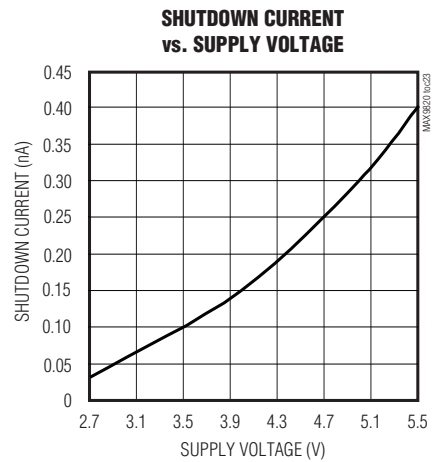
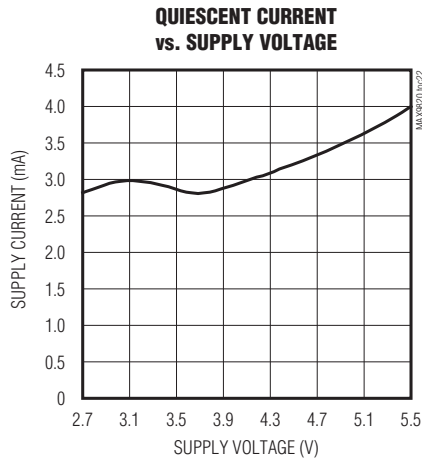
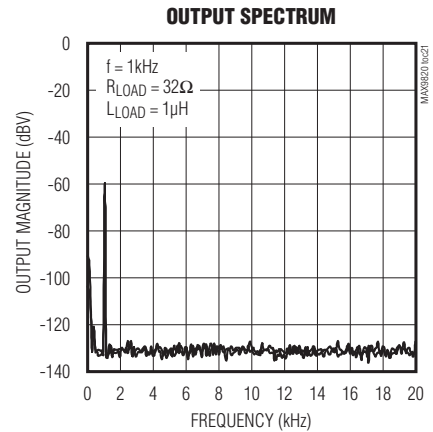
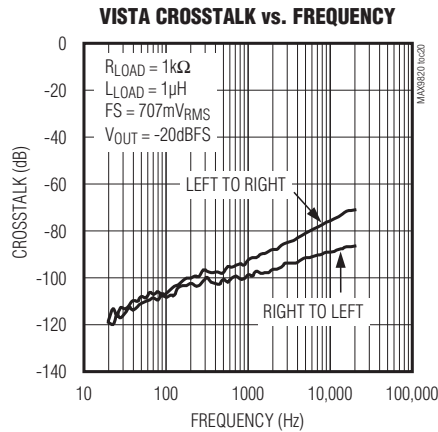
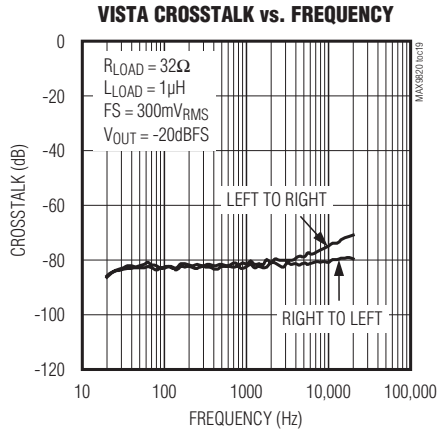


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## Typical Operating Characteristics (continued)

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# DirectDrive Headphone Amplifier with External Gain

## Pin Description

| PIN | NAME                     | FUNCTION                                                                                                                                                   |
|-----|--------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 1   | C1P                      | Flying Capacitor Positive Terminal. Connect a 1 $\mu$ F ceramic capacitor from C1P to C1N.                                                                 |
| 2   | C1N                      | Flying Capacitor Negative Terminal. Connect a 1 $\mu$ F ceramic capacitor from C1N to C1P.                                                                 |
| 3   | V <sub>SS</sub>          | Charge-Pump Output. Bypass with a 1 $\mu$ F capacitor to GND.                                                                                              |
| 4   | OUTL                     | Left-Channel Output                                                                                                                                        |
| 5   | OUTR                     | Right-Channel Output                                                                                                                                       |
| 6   | INR                      | Right-Channel Input                                                                                                                                        |
| 7   | INL                      | Left-Channel Input                                                                                                                                         |
| 8   | V <sub>DD</sub>          | Positive Power-Supply Input. Bypass with a 10 $\mu$ F capacitor to GND.                                                                                    |
| 9   | $\overline{\text{SHDN}}$ | Active-Low Shutdown Input                                                                                                                                  |
| 10  | GND                      | Signal Ground                                                                                                                                              |
| —   | EP                       | Exposed Pad. Internally connected to GND. Connect to a large ground plane to maximize thermal performance. Not intended as an electrical connection point. |

## Detailed Description

The MAX9820 95mW stereo headphone amplifier features Maxim's DirectDrive architecture, eliminating the large output-coupling capacitors required by conventional single-supply headphone amplifiers. The device features low RF susceptibility, extensive click-and-pop suppression, undervoltage lockout (UVLO) and shutdown control. The MAX9820 also features thermal-overload and short-circuit protection.

The MAX9820 is Windows Vista Premium Mobile compliant (Table 1).

## DirectDrive

Conventional single-supply headphone amplifiers have their outputs biased about a nominal DC voltage (typically half the supply) for maximum dynamic range. Large-coupling capacitors are needed to block this DC bias from the headphone. Without these capacitors, a significant amount of DC current flows to the headphone, resulting in unnecessary power dissipation and possible damage to both headphone and headphone amplifier.

Maxim's DirectDrive architecture uses a charge pump to create an internal negative supply voltage, allowing the MAX9820 outputs to be biased at

**Table 1. Windows Vista Premium Mobile Specifications vs. MAX9820 Specifications**

| DEVICE TYPE                                                          | REQUIREMENT                       | WINDOWS VISTA PREMIUM MOBILE SPECIFICATIONS | MAX9820 TYPICAL PERFORMANCE     |
|----------------------------------------------------------------------|-----------------------------------|---------------------------------------------|---------------------------------|
| Analog Line Output Jack ( $R_L = 10k\Omega$ , $FS = 0.707V_{RMS}$ )  | THD+N                             | $\leq -65dB$ FS (100Hz, 20kHz)              | -83dBFS (100Hz, 20kHz)          |
|                                                                      | Dynamic range with signal present | $\leq -80dBV$ , A-weighted (20Hz, 20kHz)    | -101dB A-weighted (20Hz, 20kHz) |
|                                                                      | Line output crosstalk             | $\leq -50dB$ (20Hz, 15kHz)                  | -73dB (20Hz, 15kHz)             |
| Analog Headphone Out Jack ( $R_L = 32\Omega$ , $FS = 0.300V_{RMS}$ ) | THD+N                             | $\leq -45dB$ FS (100Hz, 20kHz)              | -85dBFS (100Hz, 20kHz)          |
|                                                                      | Dynamic range with signal present | $\leq -60dBV$ , A-weighted (20Hz, 20kHz)    | -94dB A-weighted (20Hz, 20kHz)  |
|                                                                      | Headphone output crosstalk        | $\leq -50dB$ (20Hz, 15kHz)                  | -73dB (20Hz, 15kHz)             |

**Note:** THD+N, dynamic range, and crosstalk are measured in accordance with AES-17 audio measurements standards.

# DirectDrive Headphone Amplifier with External Gain

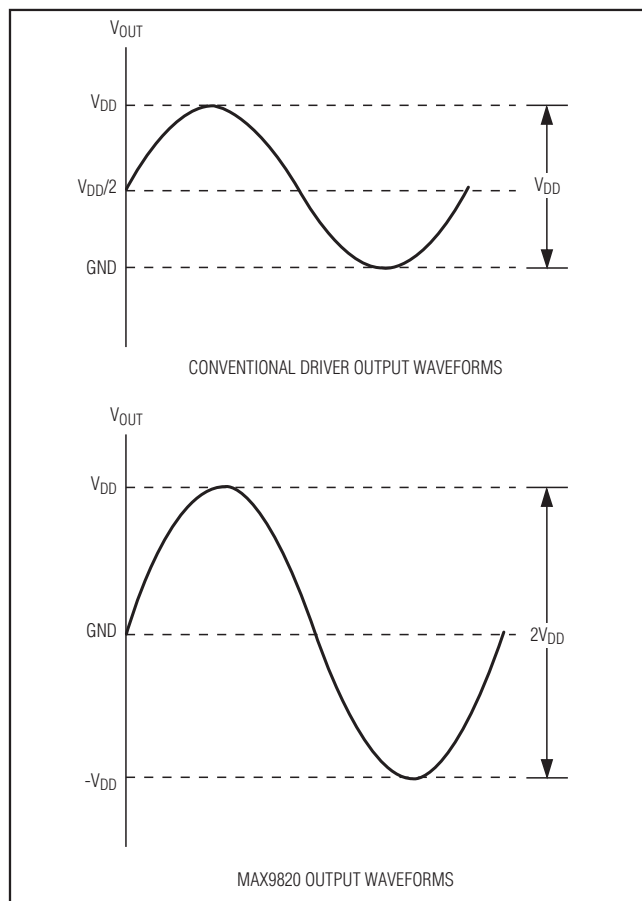


Figure 1. Conventional Driver Output Waveform vs. MAX9820 Output Waveform

$GND$  (Figure 1). With no DC component, there is no need for the large DC-blocking capacitors. The MAX9820 charge pump requires two small ceramic capacitors, conserving board space, reducing cost, and improving the frequency response of the headphone amplifier.

## Charge Pump

The MAX9820 features a low-noise charge pump. The 500kHz (typ) charge pump switching frequency is well beyond the audio range and does not interfere with audio signals.

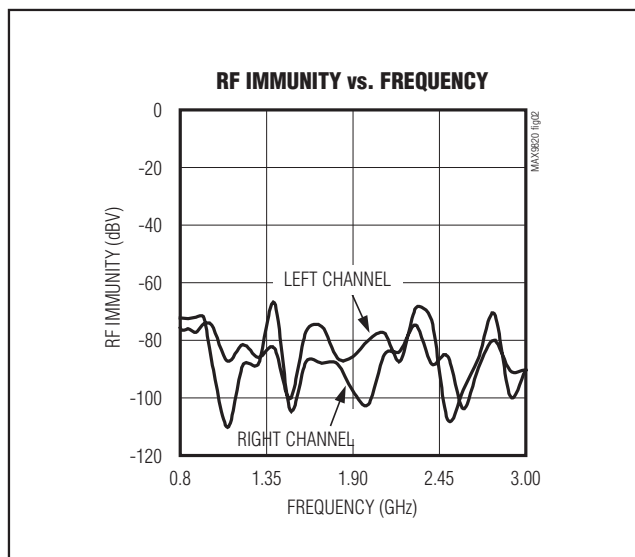


Figure 2. MAX9820 RF Susceptibility

## Click-and-Pop Suppression

In conventional single-supply audio amplifiers, the output-coupling capacitor contributes significantly to audible clicks and pops. Upon startup, the amplifier charges the coupling capacitor to its bias voltage, typically half the supply. Likewise, on shutdown, the capacitor is discharged. This results in a DC shift across the capacitor, which appears as an audible transient at the speaker. Since DirectDrive biases the outputs at ground, this problem does not arise. Additionally, the MAX9820 features extensive click-and-pop suppression that eliminates any audible transient sources internal to the device.

## RF Susceptibility

Modern audio systems are often subject to RF radiation from sources such as wireless and cellular phone networks. Although the RF radiation is out of the audio band, many signals, GSM signals in particular, contain bursts or modulation at audible frequencies. Most analog amplifiers demodulate the low-frequency envelope, adding noise to the audio signal. The MAX9820 architecture addresses the RF susceptibility problem by rejecting RF noise and preventing it from coupling into the audio band.

# DirectDrive Headphone Amplifier with External Gain

## Shutdown

The MAX9820 features a low-power shutdown mode that reduces quiescent current consumption to less than 1μA, extending battery life for portable applications. Drive SHDN low to disable the amplifiers and the charge pump. In shutdown mode, the amplifier output impedance is set to 600Ω || R<sub>FB</sub>. The amplifiers and charge pump are enabled once SHDN is driven high.

## Applications Information

### Power Dissipation

Under normal operating conditions, linear power amplifiers can dissipate a significant amount of power. The maximum power dissipation for each package is given in the *Absolute Maximum Ratings* section or can be calculated by the following equation:

$$P_{DISSPKG(MAX)} = \frac{T_{J(MAX)} - T_A}{\theta_{JA}}$$

where  $T_{J(MAX)}$  is +150°C,  $T_A$  is the ambient temperature, and  $\theta_{JA}$  is the reciprocal of the derating factor in °C/W as specified in the *Absolute Maximum Ratings* section.

The MAX9820 has two power dissipation sources: a charge pump and the two output amplifiers. If power dissipation for a given application exceeds the maximum allowed package power dissipation, reduce  $V_{DD}$ , increase load impedance, decrease the ambient temperature, or add heatsinking to the device. Large output, supply, and ground traces decrease  $\theta_{JA}$ , allowing more heat to be transferred from the package to the surrounding air.

Thermal-overload protection limits total power dissipation in the MAX9820. When the junction temperature exceeds 145°C (typ), the thermal protection circuitry disables the amplifier output stage. The amplifiers are enabled once the junction temperature cools by approximately 15°C.

## Undervoltage Lockout (UVLO)

The MAX9820 features a UVLO function that prevents the device from operating if the supply voltage falls below 2.2V (min). This feature ensures proper operation during brownout conditions and prevents deep battery discharge. Once the supply voltage reaches the minimum supply voltage range, the MAX9820 charge pump is turned on and the amplifiers are powered, provided that SHDN is high.

## Component Selection

### Input-Coupling Capacitor

The input capacitor ( $C_{IN}$ ), in conjunction with the input resistor ( $R_{IN}$ ), forms a highpass filter that removes the DC bias from an incoming signal (see the *Functional Diagram/Typical Operating Circuit*). The AC-coupling capacitor allows the device to bias the signal to an optimum DC level. Assuming zero-source impedance, the -3dB point of the highpass filter is given by:

$$f_{-3dB} = \frac{1}{2\pi R_{IN} C_{IN}}$$

Choose the  $C_{IN}$  such that  $f_{-3dB}$  is well below the lowest frequency of interest. Setting  $f_{-3dB}$  too high affects the device's low-frequency response. Use capacitors whose dielectrics have low-voltage coefficients, such as tantalum or aluminum electrolytic. Capacitors with high-voltage coefficients, such as ceramics, can result in increased distortion at low frequencies.

### Charge-Pump Capacitor Selection

Use ceramic capacitors with a low ESR for optimum performance. For optimal performance over the extended temperature range, select capacitors with an X7R or X5R dielectric. Table 2 lists suggested manufacturers.

**Table 2. Suggested Capacitor Vendors**

| SUPPLIER    | PHONE        | FAX          | WEBSITE               |
|-------------|--------------|--------------|-----------------------|
| Taiyo Yuden | 800-348-2496 | 847-925-0899 | www.t-yuden.com       |
| TDK         | 847-803-6100 | 847-390-4405 | www.component.tdk.com |
| Murata      | 770-436-1300 | 770-436-3030 | www.murata.com        |

# DirectDrive Headphone Amplifier with External Gain

## Amplifier Gain

The gain of the MAX9820 is set externally using input and feedback resistors (see the *Functional Diagram/Typical Operating Circuit*). The gain is:

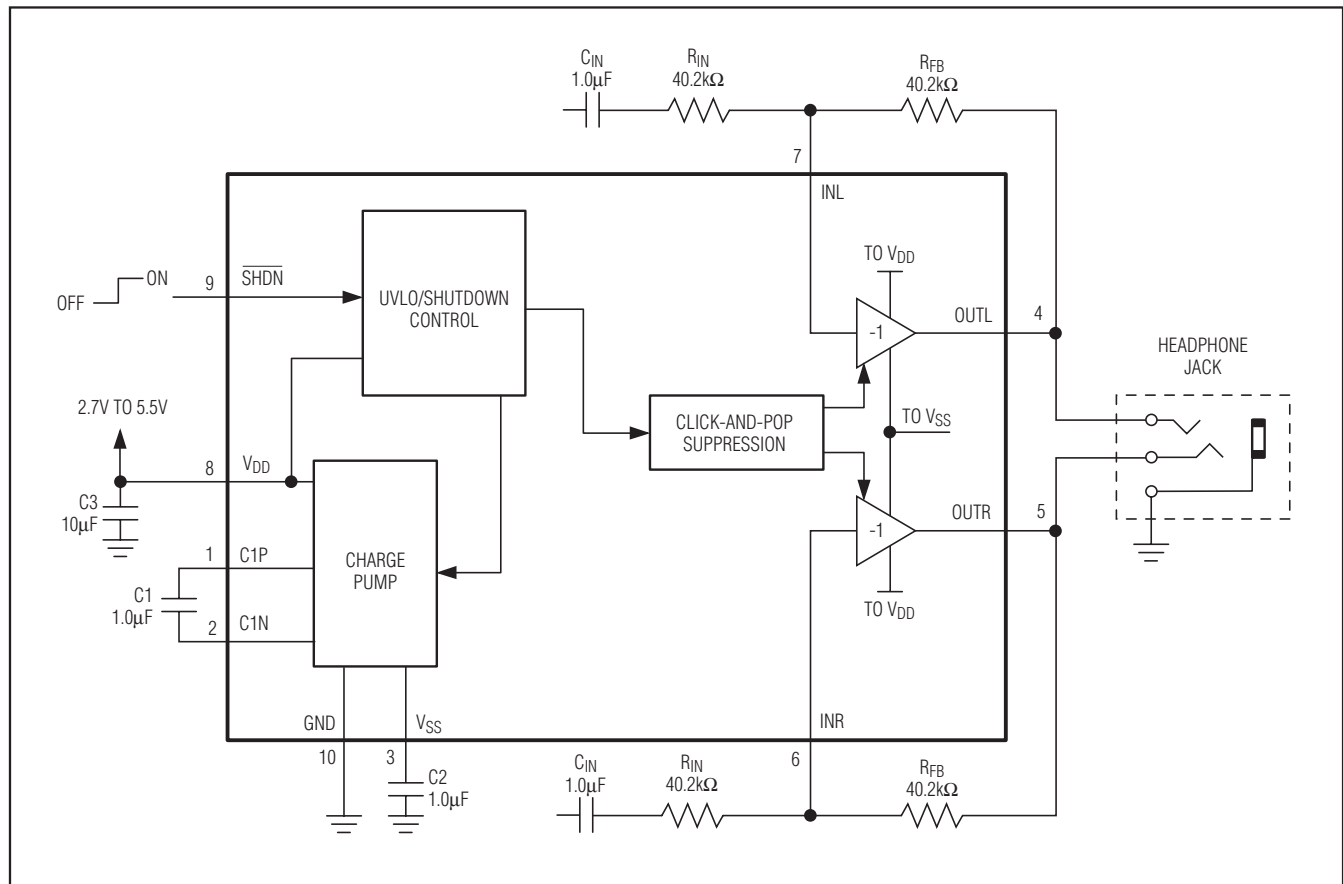
$$A_V = -\frac{R_{FB}}{R_{IN}} (V/V)$$

Choose feedback resistor values in the tens of k $\Omega$  range.

## Layout and Grounding

Proper layout and grounding are essential for optimum performance. Connect EP and GND together at a single point on the PCB. Ensure ground return resistance is minimized for optimum crosstalk performance. Place the power-supply bypass capacitor, the charge-pump hold capacitor, and the charge-pump flying capacitor as close as possible to the MAX9820. Route all traces that carry switching transients away from the audio signal path.

## Functional Diagram/Typical Operating Circuit



## Chip Information

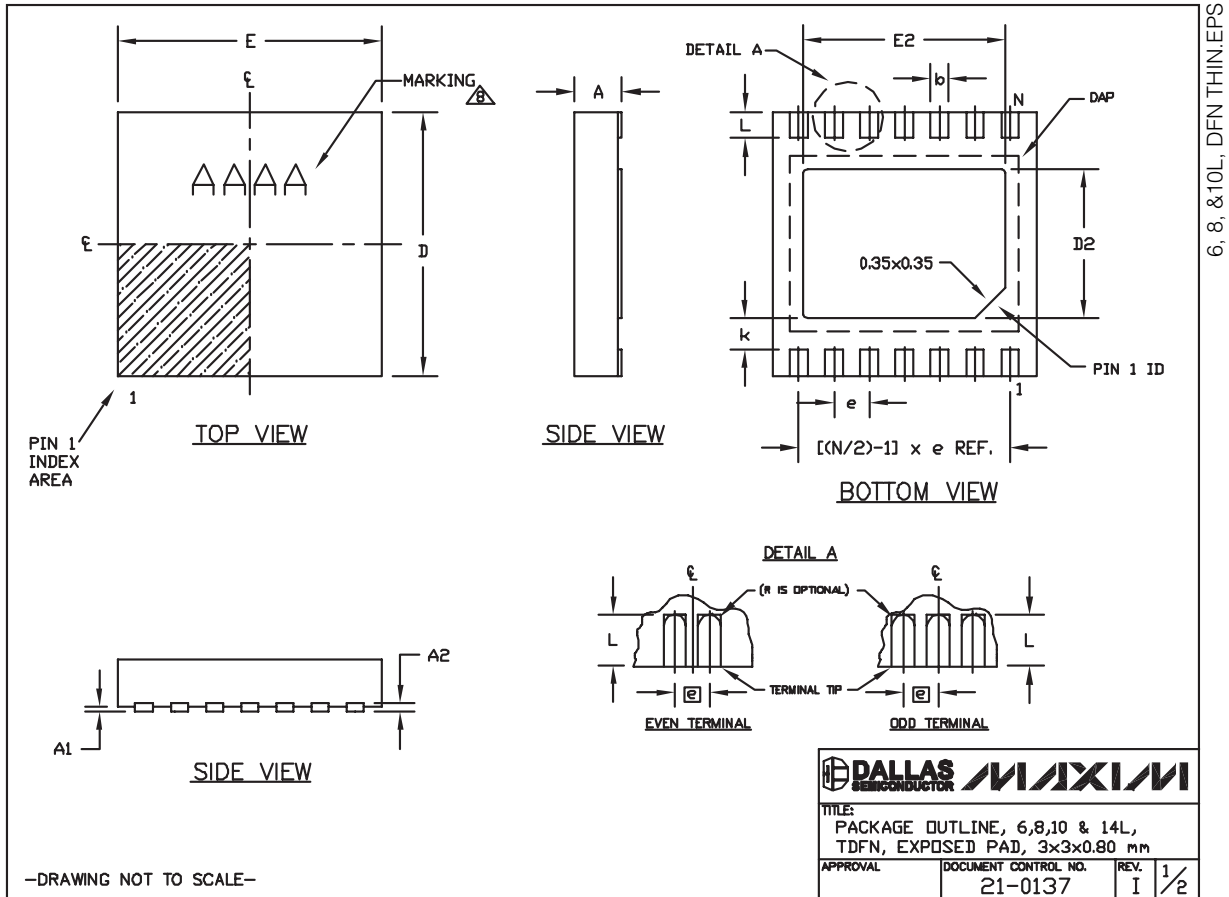
PROCESS: BiCMOS

## ***DirectDrive Headphone Amplifier with External Gain***

## ***\_Package Information***

For the latest package outline information and land patterns, go to [www.maxim-ic.com/packages](http://www.maxim-ic.com/packages).

| PACKAGE TYPE | PACKAGE CODE | DOCUMENT NO.            |
|--------------|--------------|-------------------------|
| 10 TDFN-EP   | T1033+1      | <a href="#">21-0137</a> |



# DirectDrive Headphone Amplifier with External Gain

## Package Information (continued)

For the latest package outline information and land patterns, go to [www.maxim-ic.com/packages](http://www.maxim-ic.com/packages).

MAX9820

| COMMON DIMENSIONS |           |      |
|-------------------|-----------|------|
| SYMBOL            | MIN.      | MAX. |
| A                 | 0.70      | 0.80 |
| D                 | 2.90      | 3.10 |
| E                 | 2.90      | 3.10 |
| A1                | 0.00      | 0.05 |
| L                 | 0.20      | 0.40 |
| k                 | 0.25 MIN. |      |
| A2                | 0.20 REF. |      |

| PACKAGE VARIATIONS |    |           |           |          |                |           |               |
|--------------------|----|-----------|-----------|----------|----------------|-----------|---------------|
| PKG. CODE          | N  | D2        | E2        | e        | JEDEC SPEC     | b         | [(N/2)-1] x e |
| T633-2             | 6  | 1.50±0.10 | 2.30±0.10 | 0.95 BSC | MO229 / WEEA   | 0.40±0.05 | 1.90 REF      |
| T833-2             | 8  | 1.50±0.10 | 2.30±0.10 | 0.65 BSC | MO229 / WEEC   | 0.30±0.05 | 1.95 REF      |
| T833-3             | 8  | 1.50±0.10 | 2.30±0.10 | 0.65 BSC | MO229 / WEEC   | 0.30±0.05 | 1.95 REF      |
| T1033-1            | 10 | 1.50±0.10 | 2.30±0.10 | 0.50 BSC | MO229 / WEED-3 | 0.25±0.05 | 2.00 REF      |
| T1033-2            | 10 | 1.50±0.10 | 2.30±0.10 | 0.50 BSC | MO229 / WEED-3 | 0.25±0.05 | 2.00 REF      |
| T1433-1            | 14 | 1.70±0.10 | 2.30±0.10 | 0.40 BSC | ----           | 0.20±0.05 | 2.40 REF      |
| T1433-2            | 14 | 1.70±0.10 | 2.30±0.10 | 0.40 BSC | ----           | 0.20±0.05 | 2.40 REF      |

### NOTES:

1. ALL DIMENSIONS ARE IN mm. ANGLES IN DEGREES.
2. COPLANARITY SHALL NOT EXCEED 0.08 mm.
3. WARPAGE SHALL NOT EXCEED 0.10 mm.
4. PACKAGE LENGTH/PACKAGE WIDTH ARE CONSIDERED AS SPECIAL CHARACTERISTIC(S).
5. DRAWING CONFORMS TO JEDEC MO229, EXCEPT DIMENSIONS "D2" AND "E2", AND T1433-1 & T1433-2.
6. "N" IS THE TOTAL NUMBER OF LEADS.
7. NUMBER OF LEADS SHOWN ARE FOR REFERENCE ONLY.
8. MARKING IS FOR PACKAGE ORIENTATION REFERENCE ONLY.

—DRAWING NOT TO SCALE—

|                                                                                       |                                        |                      |
|---------------------------------------------------------------------------------------|----------------------------------------|----------------------|
|  |                                        |                      |
| <b>TITLE:</b><br>PACKAGE OUTLINE, 6,8,10 & 14L,<br>TDFN, EXPOSED PAD, 3x3x0.80 mm     |                                        |                      |
| <b>APPROVAL</b>                                                                       | <b>DOCUMENT CONTROL NO.</b><br>21-0137 | <b>REV.</b><br>I 2/2 |

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