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February 2016

# FDT86246L

# N-Channel PowerTrench<sup>®</sup> MOSFET 150 V, 2 A, 228 m $\Omega$

#### **Features**

- Max  $r_{DS(on)}$  = 228 m $\Omega$  at  $V_{GS}$  = 10 V,  $I_D$  = 2 A
- Max  $r_{DS(on)}$  = 280 m $\Omega$  at  $V_{GS}$  = 4.5 V,  $I_D$  = 1.8 A
- High performance trench technology for extremely low r<sub>DS(on)</sub>
- High power and current handling capability in a widely used surface mount package
- Fast switching speed
- 100% UIL Tested
- RoHS Compliant



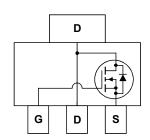
#### **General Description**

This N-Channel MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench process that has been optimized for  $r_{DS(on)}$ , switching performance and ruggedness.

### **Applications**

- Load Switch
- Primary Switch
- Buck/Boost Switch





## **MOSFET Maximum Ratings** T<sub>C</sub> = 25 °C unless otherwise noted

| Symbol                            | F                                 | Parameter              |           | Ratings     | Units |
|-----------------------------------|-----------------------------------|------------------------|-----------|-------------|-------|
| $V_{DS}$                          | Drain to Source Voltage           |                        |           | 150         | V     |
| $V_{GS}$                          | Gate to Source Voltage            |                        |           | ±20         | V     |
|                                   | Drain Current -Continuous         | T <sub>A</sub> = 25 °C | (Note 1a) | 2           | Α     |
| I <sub>D</sub>                    | -Pulsed                           |                        | (Note 4)  | 20          | _ ^   |
| E <sub>AS</sub>                   | Single Pulse Avalanche Energy     |                        | (Note 3)  | 6           | mJ    |
| D                                 | Power Dissipation                 | T <sub>A</sub> = 25 °C | (Note 1a) | 2.2         | W     |
| $P_{D}$                           | Power Dissipation                 | T <sub>A</sub> = 25 °C | (Note 1b) | 1.0         | VV    |
| T <sub>J</sub> , T <sub>STG</sub> | Operating and Storage Junction Te | emperature Range       |           | -55 to +150 | °C    |

#### **Thermal Characteristics**

| $R_{\theta JC}$ | Thermal Resistance, Junction to Case    | (Note 1)  | 12 | °C/W |
|-----------------|---|-----------|----|------|
| $R_{\theta JA}$ | Thermal Resistance, Junction to Ambient | (Note 1a) | 55 | C/VV |

#### Package Marking and Ordering Information

| Device Marking | Device    | Package | Reel Size | Tape Width | Quantity   |
|----------------|-----------|---------|-----------|------------|------------|
| 86246L         | FDT86246L | SOT-223 | 13 "      | 12 mm      | 2500 units |

# Electrical Characteristics T<sub>J</sub> = 25 °C unless otherwise noted

| Symbol                                 | Parameter                                    | Test Conditions                                | Min | Тур | Max  | Units |
|--|--|--|-----|-----|------|-------|
| Off Chara                              | acteristics                                  |  |     |     |      |       |
| BV <sub>DSS</sub>                      | Drain to Source Breakdown Voltage            | $I_D = 250 \mu A, V_{GS} = 0 V$                | 150 |     |      | V     |
| $\frac{\Delta BV_{DSS}}{\Delta T_{J}}$ | Breakdown Voltage Temperature<br>Coefficient | I <sub>D</sub> = 250 μA, referenced to 25 °C   |     | 110 |      | mV/°C |
| I <sub>DSS</sub>                       | Zero Gate Voltage Drain Current              | V <sub>DS</sub> = 120 V, V <sub>GS</sub> = 0 V |     |     | 1    | μΑ    |
| I <sub>GSS</sub>                       | Gate to Source Leakage Current               | V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0 V |     |     | ±100 | nA    |

#### **On Characteristics**

| V <sub>GS(th)</sub>                    | Gate to Source Threshold Voltage                            | $V_{GS} = V_{DS}, I_D = 250 \mu A$                                     | 0.8 | 1.6 | 2.5 | V     |
|--|---|--|-----|-----|-----|-------|
| $\frac{\Delta V_{GS(th)}}{\Delta T_J}$ | Gate to Source Threshold Voltage<br>Temperature Coefficient | I <sub>D</sub> = 250 μA, referenced to 25 °C                           |     | -5  |     | mV/°C |
|  |   | V <sub>GS</sub> = 10 V, I <sub>D</sub> = 2 A                           |     | 189 | 228 |       |
| r <sub>DS(on)</sub>                    | Static Drain to Source On Resistance                        | $V_{GS} = 4.5 \text{ V}, I_D = 1.8 \text{ A}$                          |     | 208 | 280 | mΩ    |
|  |   | $V_{GS} = 10 \text{ V}, I_D = 2 \text{ A}, T_J = 125 ^{\circ}\text{C}$ |     | 375 | 452 |       |
| 9 <sub>FS</sub>                        | Forward Transconductance                                    | V <sub>DS</sub> = 10 V, I <sub>D</sub> = 2 A                           |     | 7.3 |     | S     |

### **Dynamic Characteristics**

| C <sub>iss</sub> | Input Capacitance            | 75.77.77  |     | 238 | 335 | pF |
|------------------|------------------------------|---|-----|-----|-----|----|
| C <sub>oss</sub> | Output Capacitance           | V <sub>DS</sub> = 75 V, V <sub>GS</sub> = 0 V,<br>f = 1 MHz |     | 20  | 30  | pF |
| C <sub>rss</sub> | Reverse Transfer Capacitance | 1 – 1 1011 12   |     | 2   | 5   | pF |
| $R_a$            | Gate Resistance              |   | 0.1 | 0.9 | 2.7 | Ω  |

## **Switching Characteristics**

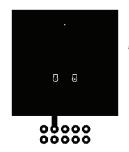
| t <sub>d(on)</sub>  | Turn-On Delay Time            |   | 4.5 | 10  | ns |
|---------------------|-------------------------------|---|-----|-----|----|
| t <sub>r</sub>      | Rise Time                     | $V_{DD}$ = 75 V, $I_{D}$ = 2 A,<br>$V_{GS}$ = 10 V, $R_{GEN}$ = 6 $\Omega$              | 1.3 | 10  | ns |
| t <sub>d(off)</sub> | Turn-Off Delay Time           | $V_{GS}$ = 10 V, $R_{GEN}$ = 6 $\Omega$   | 11  | 20  | ns |
| t <sub>f</sub>      | Fall Time                     |   | 2   | 10  | ns |
| $Q_{g(TOT)}$        | Total Gate Charge             | V <sub>GS</sub> = 0 V to 10 V   | 4.5 | 6.3 | nC |
| $Q_{g(TOT)}$        | Total Gate Charge             | $V_{GS} = 0 \text{ V to } 4.5 \text{ V}$ $I_{DD} = 75 \text{ V},$ $I_{D} = 2 \text{ A}$ | 2.3 | 3.3 | nC |
| Q <sub>gs</sub>     | Total Gate Charge             | 1 <sub>D</sub> - 2 A  | 0.7 |     | nC |
| $Q_{gd}$            | Gate to Drain "Miller" Charge |   | 1.0 |     | nC |

#### **Drain-Source Diode Characteristics**

| $V_{SD}$        | Source to Drain Diode Forward Voltage | V <sub>GS</sub> = 0 V, I <sub>S</sub> = 2 A | (Note 2) | 0.8 | 1.3 | V  |
|-----------------|---------------------------------------|---|----------|-----|-----|----|
| t <sub>rr</sub> | Reverse Recovery Time                 | I <sub>E</sub> = 2 A. di/dt = 100 A/μs      |          | 44  | 71  | ns |
| Q <sub>rr</sub> | Reverse Recovery Charge               | 1F - 2 A, αι/αι - 100 A/μS                  |          | 31  | 50  | nC |

#### NOTES

<sup>1.</sup>  $R_{\theta,M}$  is determined with the device mounted on a 1 in<sup>2</sup> pad 2 oz copper pad on a 1.5 x 1.5 in. board of FR-4 material.  $R_{\theta,JC}$  is guaranteed by design while  $R_{\theta,CA}$  is determined by the user's board design.



 a) 55 °C/W when mounted on a 1 in<sup>2</sup> pad of 2 oz copper



b) 118 °C/W when mounted on a minimum pad of 2 oz copper

- 2. Pulse Test: Pulse Width < 300  $\mu\text{s},$  Duty cycle < 2.0%.
- 3.  $E_{AS}$  of 6 mJ is based on starting  $T_J$  = 25 °C; N-ch: L = 3 mH,  $I_{AS}$  = 2 A,  $V_{DD}$  = 150 V,  $V_{GS}$  = 10 V. 100% test at L = 0.1 mH,  $I_{AS}$  = 7 A.
- 4. Pulsed Id please refer to Fig 11 SOA graph for more details.
- 5. Computed continuous current limited to Max Junction Temperature only, actual continuous current will be limited by thermal & electro-mechanical application board design.

# Typical Characteristics T<sub>J</sub> = 25 °C unless otherwise noted.

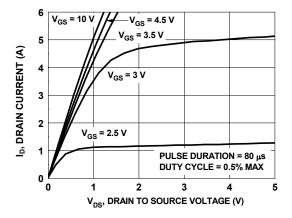


Figure 1. On Region Characteristics

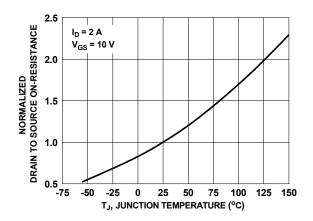


Figure 3. Normalized On Resistance vs. Junction Temperature

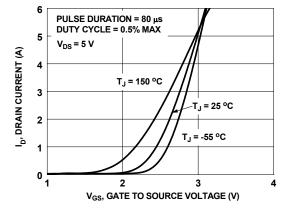


Figure 5. Transfer Characteristics

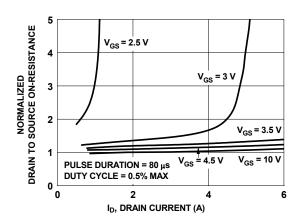


Figure 2. Normalized On-Resistance vs. Drain Current and Gate Voltage

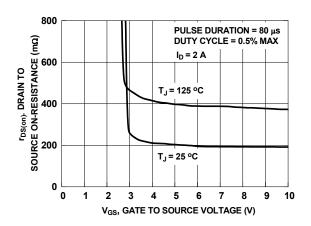


Figure 4. On-Resistance vs. Gate to Source Voltage

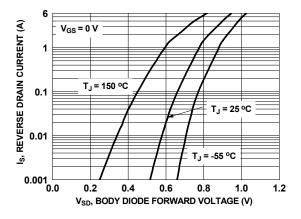


Figure 6. Source to Drain Diode Forward Voltage vs. Source Current

# Typical Characteristics $T_J$ = 25 °C unless otherwise noted.

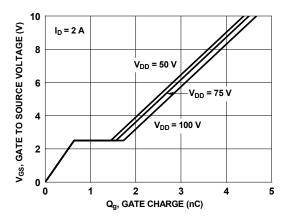


Figure 7. Gate Charge Characteristics

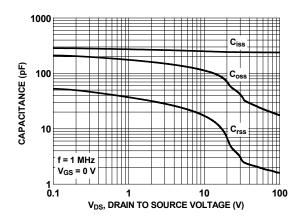


Figure 8. Capacitance vs. Drain to Source Voltage

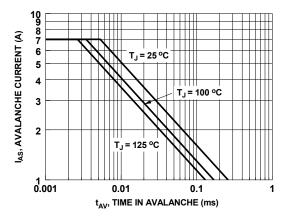


Figure 9. Unclamped Inductive Switching Capability

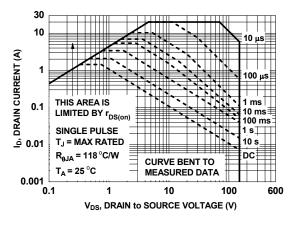


Figure 10. Forward Bias Safe Operating Area

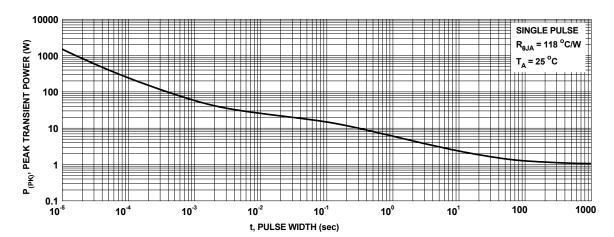


Figure 11. Single Pulse Maximum Power Dissipation

# **Typical Characteristics** $T_J = 25$ °C unless otherwise noted.

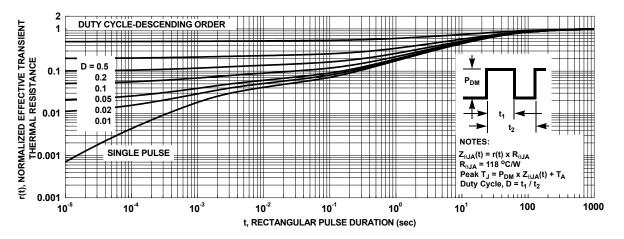
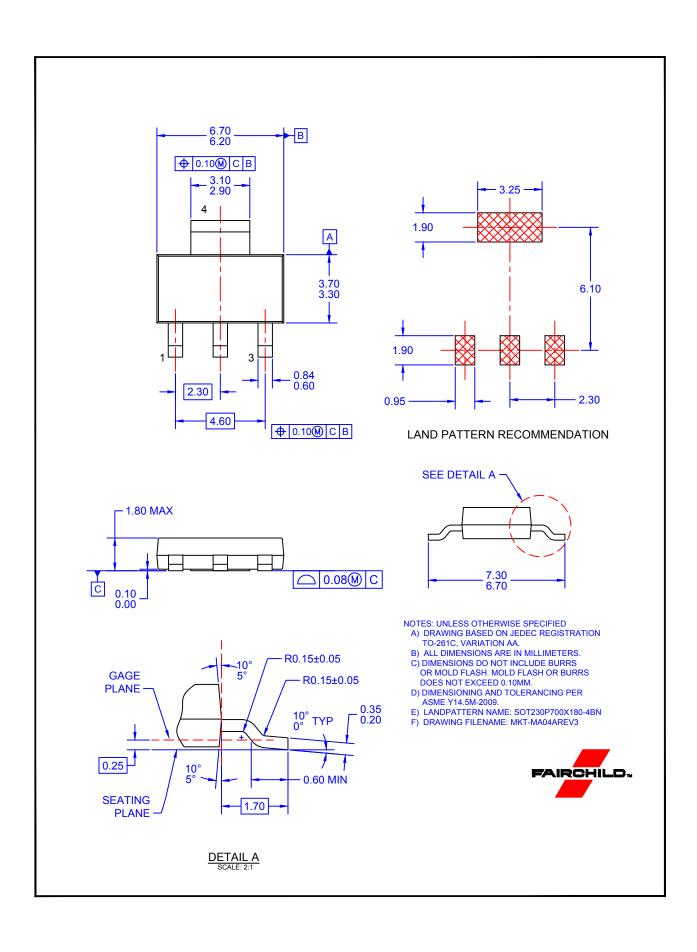


Figure 12. Junction-to-Ambient Transient Thermal Response Curve



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## Офис по работе с юридическими лицами:

105318, г. Москва, ул. Щербаковская д. 3, офис 1107, 1118, ДЦ «Щербаковский»

Телефон: +7 495 668-12-70 (многоканальный)

Факс: +7 495 668-12-70 (доб.304)

E-mail: info@moschip.ru

Skype отдела продаж:

moschip.ru moschip.ru\_6 moschip.ru\_4 moschip.ru\_9