

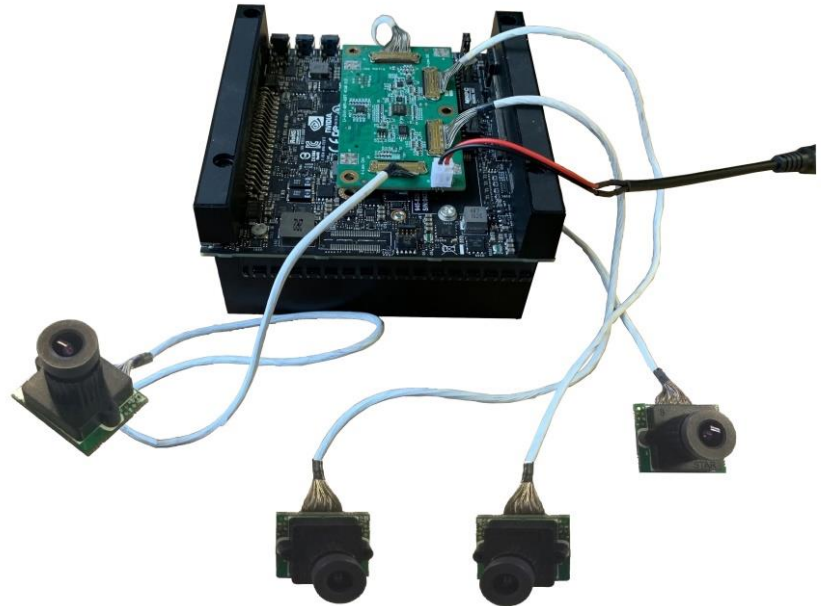


LEOPARD IMAGING INC

# LI-XAVIER-KIT-IMX290M12-X Data Sheet

## Key Features

- Compatible with Nvidia® Jetson AGX Xavier™ Developer Kit
- MIPI CSI-2 interface
- Support up to four 4-lane cameras
- Sony Diagonal 6.46 mm (Type 1/2.8) CMOS Image Sensor IMX290
- Active pixels: 1945H x 1109V
- Pixel size: 2.9 um x 2.9 um
- Color camera
- High sensitivity
- Length of the I-PEX cable: 300mm
- Support multiple length cables
- Connector Part#: 20525-030E-02C
- Support M12 x P0.5 lens
- Provide customization services
- Part#:
  - (1 cam) **LI-XAVIER-KIT-IMX290M12**
  - (2 cam) **LI-XAVIER-KIT-IMX290M12-D**
  - (3 cam) **LI-XAVIER-KIT-IMX290M12-T**
  - (4 cam) **LI-XAVIER-KIT-IMX290M12-Q**



## Lens Spec

- Model: LC001E-0530
- Focal length: 3.98 mm +/- 5%
- Aperture, F/#: 2.3 +/- 5%
- Built in 650nm IR cut filter
- FOV (D/H/V): 96 °/ 82.4 °/50 °
- TV Distortion: < 12%
- Mount: M12 x P0.5

## BOM

Nvidia AGX Xavier Developer Kit not included

#	Items	QTY
1	LI-JXAV-MIPI-ADPT-4CAM	1
2	LI-IMX290-MIPI-M12	1, 2,3 or 4
3	FAW-1233-03 cable	1, 2,3 or 4

## Applications

- Industrial Cameras
- Surveillance cameras



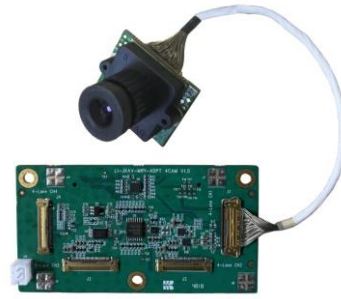
Leopard Imaging Inc.

48820 Kato Rd, Suite 100B, Fremont, CA 94538, USA  
 Phone: +1-408-263-0988  
 Fax: +1-408-217-1960  
 Email: sales@leopardimaging.com  
 Website: www.leopardimaging.com

### LI-XAVIER-KIT-IMX290M12

#### BOM

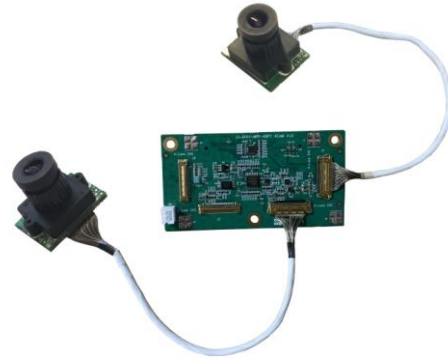
#	Items	QTY
1	LI-JXAV-MIPI-ADPT-4CAM	1
2	LI-IMX290-MIPI-M12	1
3	FAW-1233-03 cable	1



### LI-XAVIER-KIT-IMX290M12-D

#### BOM

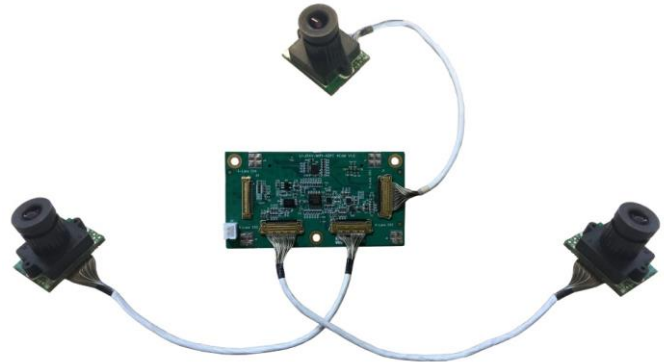
#	Items	QTY
1	LI-JXAV-MIPI-ADPT-4CAM	1
2	LI-IMX290-MIPI-M12	2
3	FAW-1233-03 cable	2



### LI-XAVIER-KIT-IMX290M12-T

#### BOM

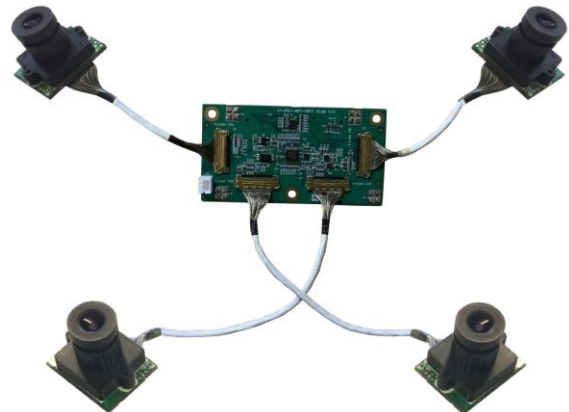
#	Items	QTY
1	LI-JXAV-MIPI-ADPT-4CAM	1
2	LI-IMX290-MIPI-M12	3
3	FAW-1233-03 cable	3



### LI-XAVIER-KIT-IMX290M12-Q

#### BOM

#	Items	QTY
1	LI-JXAV-MIPI-ADPT-4CAM	1
2	LI-IMX290-MIPI-M12	4
3	FAW-1233-03 cable	4

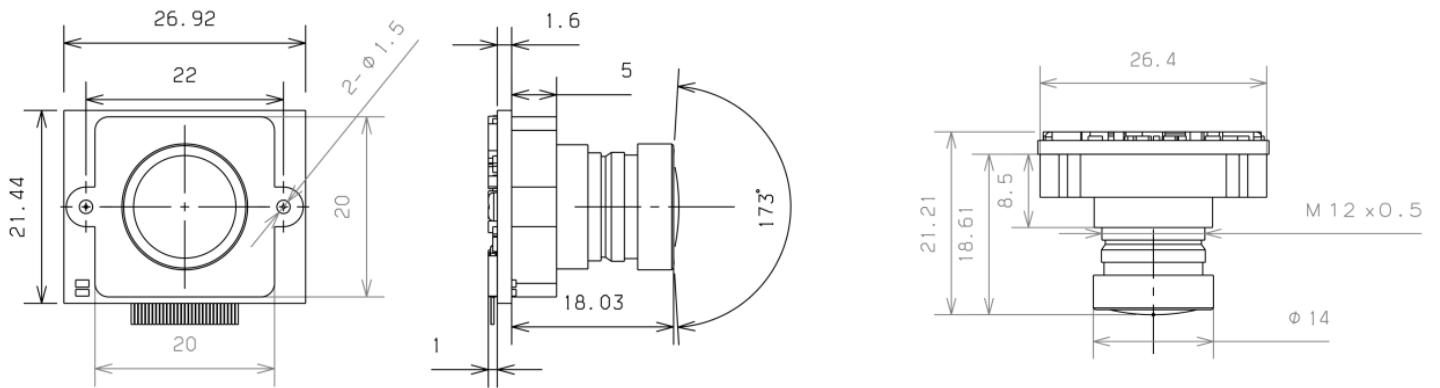


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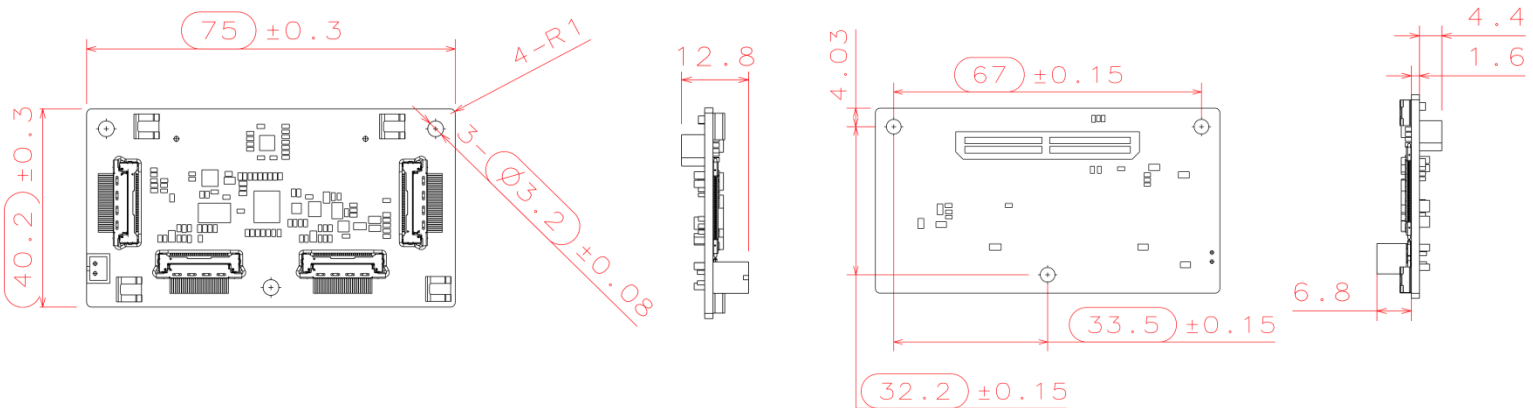
48820 Kato Rd, Suite 100B, Fremont, CA 94538, USA  
Phone: +1-408-263-0988  
Fax: +1-408-217-1960  
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# Dimensions

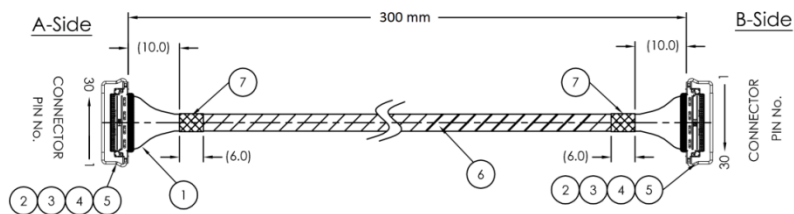
## LI-IMX290-MIPI-M12



## LI-JXAV-MIPI-ADPT-4CAM



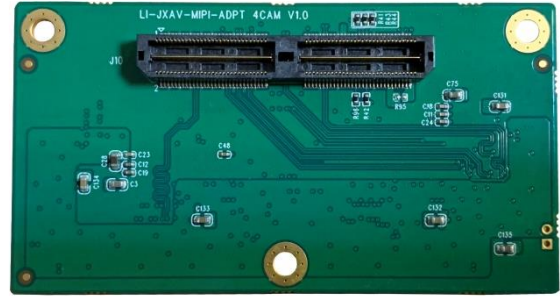
## FAW-1233-03



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48820 Kato Rd, Suite 100B, Fremont, CA 94538, USA  
Phone: +1-408-263-0988  
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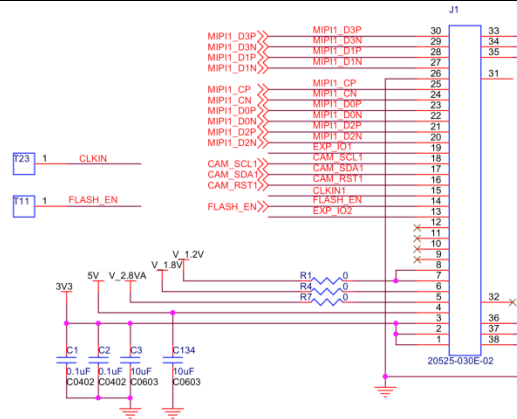
# LI-JXAV-MIPI-ADPT-4CAM



## Interfaces

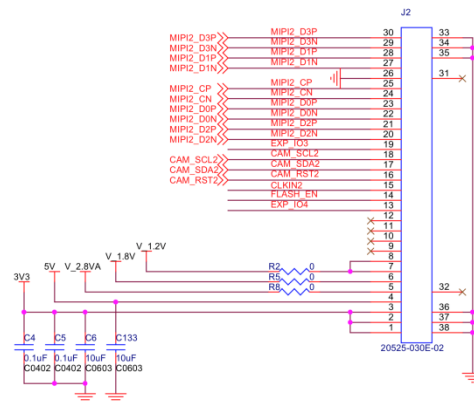
### Interface J1

- Part#: 20525-030E-02C
- Number of Positions: 30
- Pitch: 0.4mm
- Mating I-PEX cable: FAW-1233-03 (300mm)



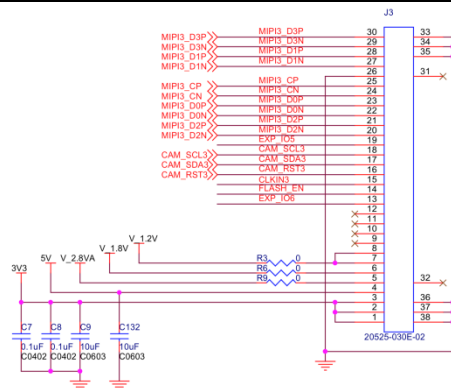
### Interface J2

- Part#: 20525-030E-02C
- Number of Positions: 30
- Pitch: 0.4mm
- Mating I-PEX cable: FAW-1233-03 (300mm)



### Interface J3

- Part#: 20525-030E-02C
- Number of Positions: 30
- Pitch: 0.4mm
- Mating I-PEX cable: FAW-1233-03 (300mm)

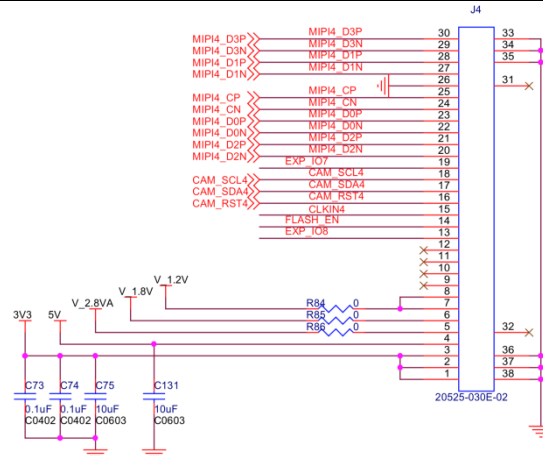


Leopard Imaging Inc.

48820 Kato Rd, Suite 100B, Fremont, CA 94538, USA  
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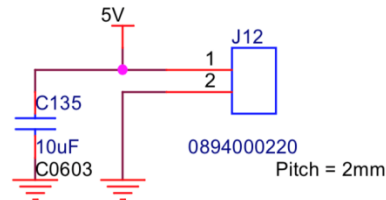
## Interface J4

- Part#: 20525-030E-02C
- Number of Positions: 30
- Pitch: 0.4mm
- Mating I-PEX cable: FAW-1233-03 (300mm)



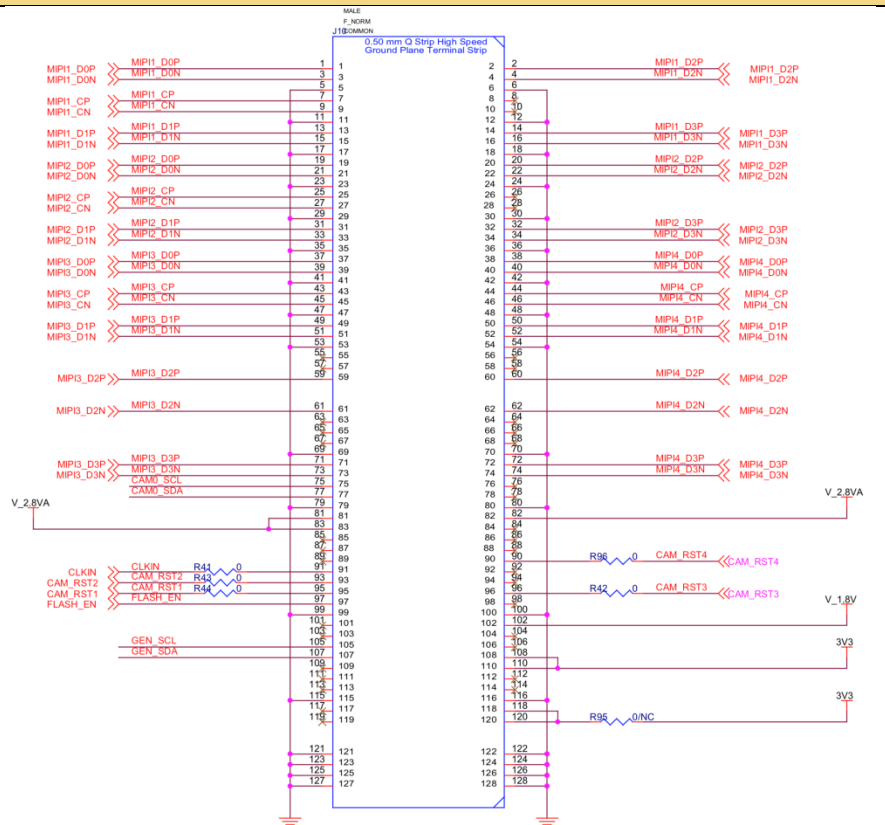
## Interface J5

- Part#: 0894000220
- Number of Positions: 2
- Pitch: 2mm



## Interface J10

- Part#: QTH-060-01-L-D-A
- Number of Positions: 120
- Number of Rows: 2
- Pitch: 0.5 mm



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48820 Kato Rd, Suite 100B, Fremont, CA 94538, USA  
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 Fax: +1-408-217-1960  
 Email: sales@leopardimaging.com  
 Website: www.leopardimaging.com

# LI-IMX290-MIPI-M12



Camera Spec	
Image Sensor	Sony Diagonal 6.46 mm (Type 1/2.8) CMOS Image Sensor IMX290
Optical format	1/2.8"
Number of active pixels	1945 (H) x 1109(V)
Pixel size	2.9um (H) x 2.9um (V)
Color or Mono	Color
Interface	MIPI interface
Lens mount	M12
Weight	14 g
Interfaces	
Interface J2:	<ul style="list-style-type: none"> <li>Part#: 20525-030E-02C</li> <li>Number of Positions: 30</li> <li>Pitch: 0.4mm</li> <li>Mating I-PEX cable: FAW-1233-03 (300mm)</li> </ul>



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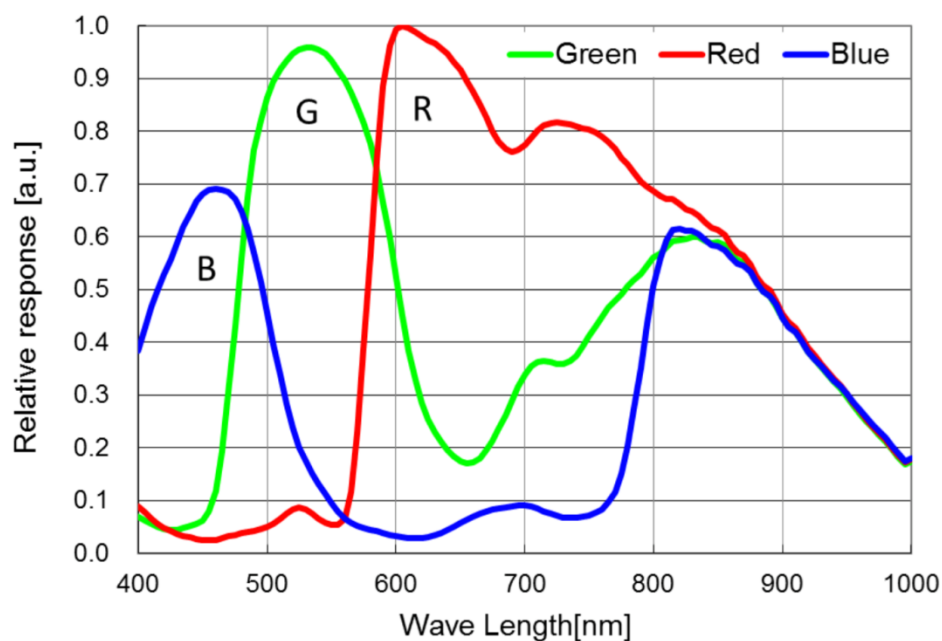
## Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit	Remarks
Supply voltage (analog 2.9 V)	AV <sub>DD</sub>	-0.3	3.3	V	
Supply voltage (interface 1.8 V)	OV <sub>DD</sub>	-0.3	3.3	V	
Supply voltage (digital 1.2 V)	DV <sub>DD</sub>	-0.3	2.0	V	
Input voltage	VI	-0.3	OV <sub>DD</sub> + 0.3	V	Not exceed 3.3 V
Output voltage	VO	-0.3	OV <sub>DD</sub> + 0.3	V	Not exceed 3.3 V

## Recommended Operating Conditions

Item	Symbol	Min.	Typ.	Max.	Unit
Supply voltage (analog 2.9 V)	AV <sub>DD</sub>	2.80	2.90	3.00	V
Supply voltage (Interface 1.8 V)	OV <sub>DD</sub>	1.70	1.80	1.90	V
Supply voltage (digital 1.2 V)	DV <sub>DD</sub>	1.10	1.20	1.30	V
Performance guarantee temperature	Tspec	-10	—	60	°C
Operating guarantee temperature	Topr	-30	—	85	°C
Storage guarantee temperature	Tstg	-40	—	85	°C

## Spectral Sensitivity Characteristics



## DC Characteristics

Item	Pins	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage	analog	VDDHx	AV <sub>DD</sub>	2.80	2.90	3.00	V
	Interface	VDDMx	OV <sub>DD</sub>	1.70	1.80	1.90	V
	digital	VDDLx	DV <sub>DD</sub>	1.10	1.20	1.30	V
Digital input voltage	XHS XVS XCLR INCK XMASTER OMODE SCK SDI XCE XTRIG	VIH	XVS / XHS Slave Mode	0.80V <sub>DD</sub>	—	—	V
		VIL		—	—	0.20V <sub>DD</sub>	V
Digital output voltage	DLOP [A:F] DLOM [A:F] DLCKP DLCKM	VOH	IOH = -2 mA	OV <sub>DD</sub> -0.4	—	—	V
		VOL	IOL = 2 mA	—	—	0.4	V
		VCM	Low voltage LVDS	—	OV <sub>DD</sub> /2	—	V
		VOD	Low voltage LVDS (Termination resistance: 100 Ω)	100	150	220	mV
	XHS XVS SDO TOUT	VOH	XVS / XHS Master Mode	OV <sub>DD</sub> -0.4	—	—	V
		VOL		—	—	0.4	V

## Current Consumption

Item	pin	Symbol	Typ.		Max.		Unit
			Standard luminous intensity	Saturated luminous intensity	Standard luminous intensity	Saturated luminous intensity	
Operating current Low voltage LVDS serial 8 ch 12 bit 60 frame / s Full HD 1080p mode	VDDH	IAV <sub>DD</sub>	54	53	111	108	mA
	VDDM	IOV <sub>DD</sub>	16	15	29	27	mA
	VDDL	IDV <sub>DD</sub>	77	95	123	214	mA
Operating current MIPI CSI-2 / 4 Lane 12 bit, 60 frame/s Full HD 1080p mode	VDDH	IAV <sub>DD</sub>	55	54	111	108	mA
	VDDM	IOV <sub>DD</sub>	1	1	2	2	mA
	VDDL	IDV <sub>DD</sub>	94	111	143	252	mA
Operating current CMOS parallel SDR 12 bit, 30 frame/s Full HD 1080p	VDDH	IAV <sub>DD</sub>	55	54	111	110	mA
	VDDM	IOV <sub>DD</sub>	17	17	28	28	mA
	VDDL	IDV <sub>DD</sub>	49	59	90	159	mA
Standby current	VDDH	IAV <sub>DD_STB</sub>	—		0.1		mA
	VDDM	IOV <sub>DD_STB</sub>	—		0.1		mA
	VDDL	IDV <sub>DD_STB</sub>	—		14.0		mA



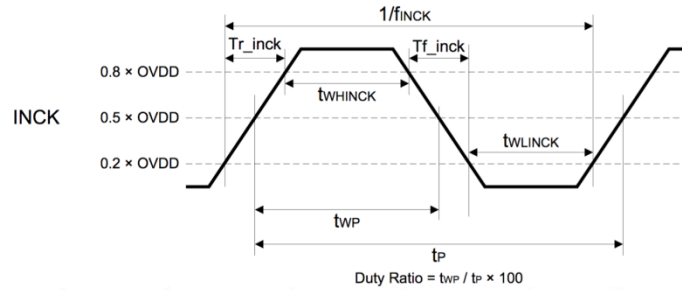
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# AC Characteristics

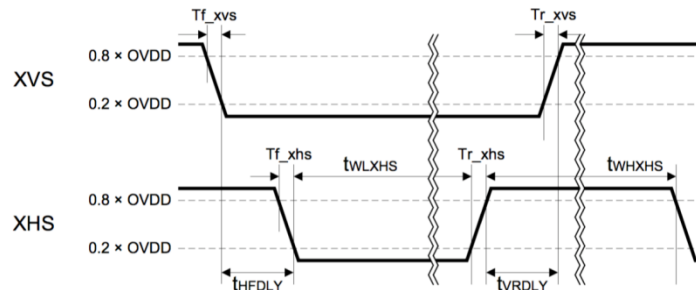
## INCK



Item	Symbol	Min.	Typ.	Max.	Unit	Remarks
INCK clock frequency	$f_{INCK}$	$f_{INCK} \times 0.96$	$f_{INCK}$	$f_{INCK} \times 1.02$	MHz	$f_{INCK} = 37.125 \text{ MHz}, 74.25 \text{ MHz}$
INCK Low level pulse width	$t_{WLINCK}$	4	—	—	ns	$f_{INCK} = 37.125 \text{ MHz}, 74.25 \text{ MHz}$
INCK High level pulse width	$t_{WHINCK}$	4	—	—	ns	$f_{INCK} = 37.125 \text{ MHz}, 74.25 \text{ MHz}$
INCK clock duty	—	45.0	50.0	55.0	%	Define with $0.5 \times OV_{DD}$
INCK Rise time	$Tr\_inck$	—	—	5	ns	20 % to 80 %
INCK Fall time	$Tf\_inck$	—	—	5	ns	80 % to 20 %

\*The INCK fluctuation affects the frame rate.

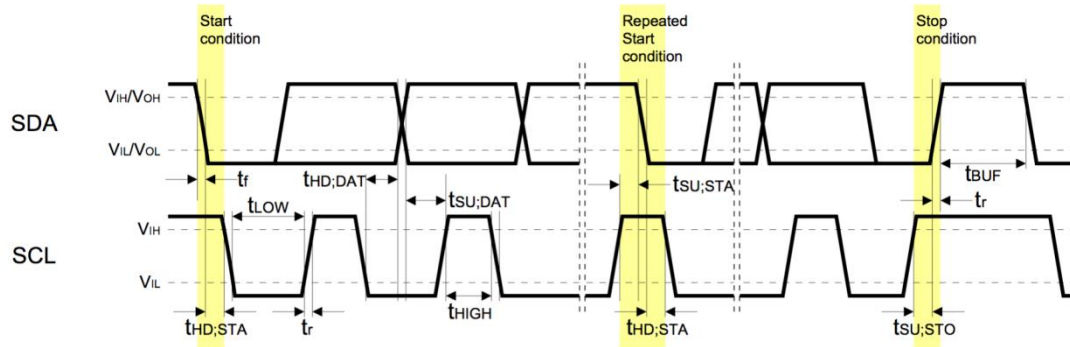
## XHS, XVS Input Characteristics In Slave Mode (XMASTER pin = High)



Item	Symbol	Min.	Typ.	Max.	Unit	Remarks
XHS Low level pulse width	$t_{W LXHS}$	$4 / f_{INCK}$	—	—	ns	
XHS High level pulse width	$t_{W HXHS}$	$4 / f_{INCK}$	—	—	ns	
XVS - XHS fall width	$t_{HFDLY}$	$1 / f_{INCK}$	—	—	ns	
XHS - XVS rise width	$t_{VRDLY}$	$1 / f_{INCK}$	—	—	ns	
XVS Rise time	$Tr\_xvs$	—	—	5	ns	20 % to 80 %
XVS Fall time	$Tf\_xvs$	—	—	5	ns	80 % to 20 %
XHS Rise time	$Tr\_xhs$	—	—	5	ns	20 % to 80 %
XHS Fall time	$Tf\_xhs$	—	—	5	ns	80 % to 20 %



## I<sup>2</sup>C Communication



## I<sup>2</sup>C Specification

Item	Symbol	Min.	Typ.	Max.	Unit	条件
Low level input voltage	VIL	-0.3	—	$0.3 \times OV_{DD}$	V	
High level input voltage	VIH	$0.7 \times OV_{DD}$	—	1.9	V	
Low level input voltage	VOL	0	—	$0.2 \times OV_{DD}$	V	$OV_{DD} < 2\text{ V}$ , Sink 3 mA
High level input voltage	VOH	$0.8 \times OV_{DD}$	—	—	V	
Output fall time	tof	—	—	250	ns	Load 10 pF – 400 pF, $0.7 \times OV_{DD} - 0.3 \times OV_{DD}$
Input current	li	-10	—	10	$\mu\text{A}$	$0.1 \times OV_{DD} - 0.9 \times OV_{DD}$
Capacitance for SCK (SCL) /SDI (SDA)	Ci	—	—	10	pF	

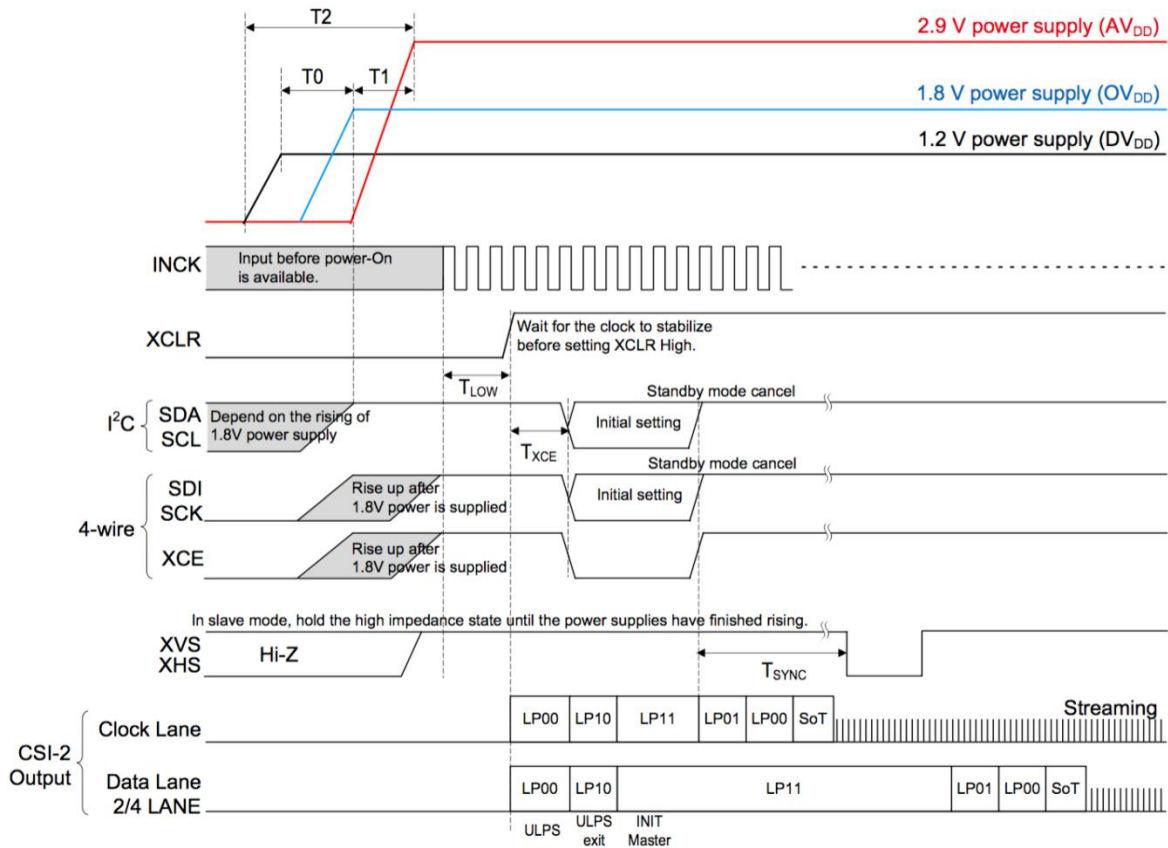
## I<sup>2</sup>C AC Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit
SCL clock frequency	$f_{SCL}$	0	—	400	kHz
Hold time (Start Condition)	$t_{HD:STA}$	0.6	—	—	$\mu\text{s}$
Low period of the SCL clock	$t_{LOW}$	1.3	—	—	$\mu\text{s}$
High period of the SCL clock	$t_{HIGH}$	0.6	—	—	$\mu\text{s}$
Set-up time (Repeated Start Condition)	$t_{SU:STA}$	0.6	—	—	$\mu\text{s}$
Data hold time	$t_{HD:DAT}$	0	—	0.9	$\mu\text{s}$
Data set-up time	$t_{SU:DAT}$	100	—	—	ns
Rise time of both SDA and SCL signals	$t_r$	—	—	300	ns
Fall time of both SDA and SCL signals	$t_f$	—	—	300	ns
Set-up time (Stop Condition)	$t_{SU:STO}$	0.6	—	—	$\mu\text{s}$
Bus free time between a STOP and START Condition	$t_{BUF}$	1.3	—	—	$\mu\text{s}$



## Power-on Sequence

1. Turn On the power supplies so that the power supplies rise in order of 1.2 V power supply ( $DV_{DD}$ ) → 1.8 V power supply ( $OV_{DD}$ ) → 2.9 V power supply ( $AV_{DD}$ ). In addition, all power supplies should finish rising within 200 ms.
2. Start master clock (INCK) input after turning On the power supplies.
3. The register values are undefined immediately after power-on, so the system must be cleared. Hold XCLR at Low level for 500 ns or more after all the power supplies have finished rising. (The register values after a system clear are the default values.) In addition, hold XCE to High level during this period. Rise XCE after 1.8 V power supply ( $OV_{DD}$ ).
4. The system clear is applied by setting XCLR to High level. However, the maser clock needs to stabilize before setting the XCLR pin to High level.
5. Make the sensor setting by register communication after the system clear. A period of 20  $\mu$ s or more should be provided after setting XCLR High before inputting the communication enable signal XCE. In  $I^2C$  communication, XCE is fixed to High.

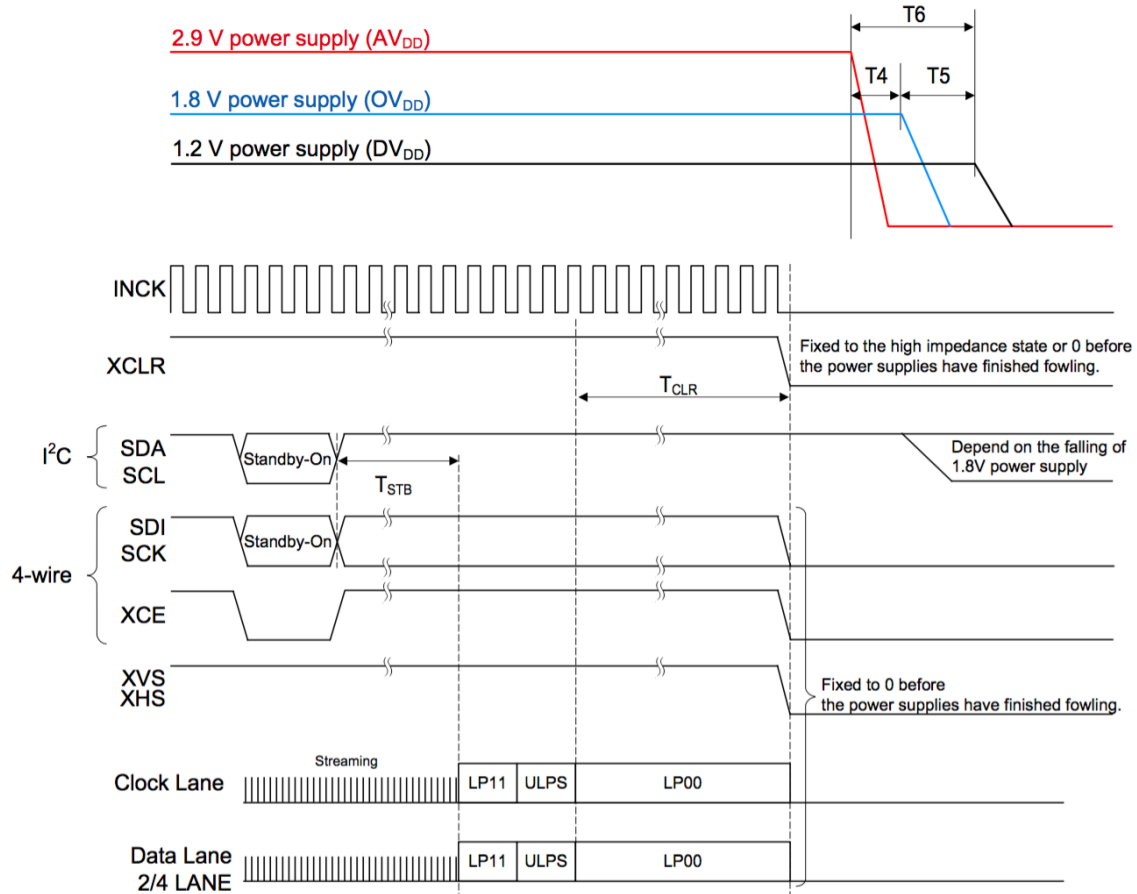


Item	Symbol	Min.	Max.	Unit
1.2 V power supply rising → 1.8 V power supply rising	T0	0	—	ns
1.8 V power supply rising → 2.9 V power supply rising	T1	0	—	ns
Rising time of all power supply	T2	—	200	ms
INCK active → Clear OFF	T <sub>LOW</sub>	500	—	ns
Clear OFF → Communication start	T <sub>XCE</sub>	20	—	$\mu$ s
Standby OFF (communication) → External input XHS,XVS (slave mode only)	T <sub>SYNC</sub>	20	—	ms



## Power-off Sequence

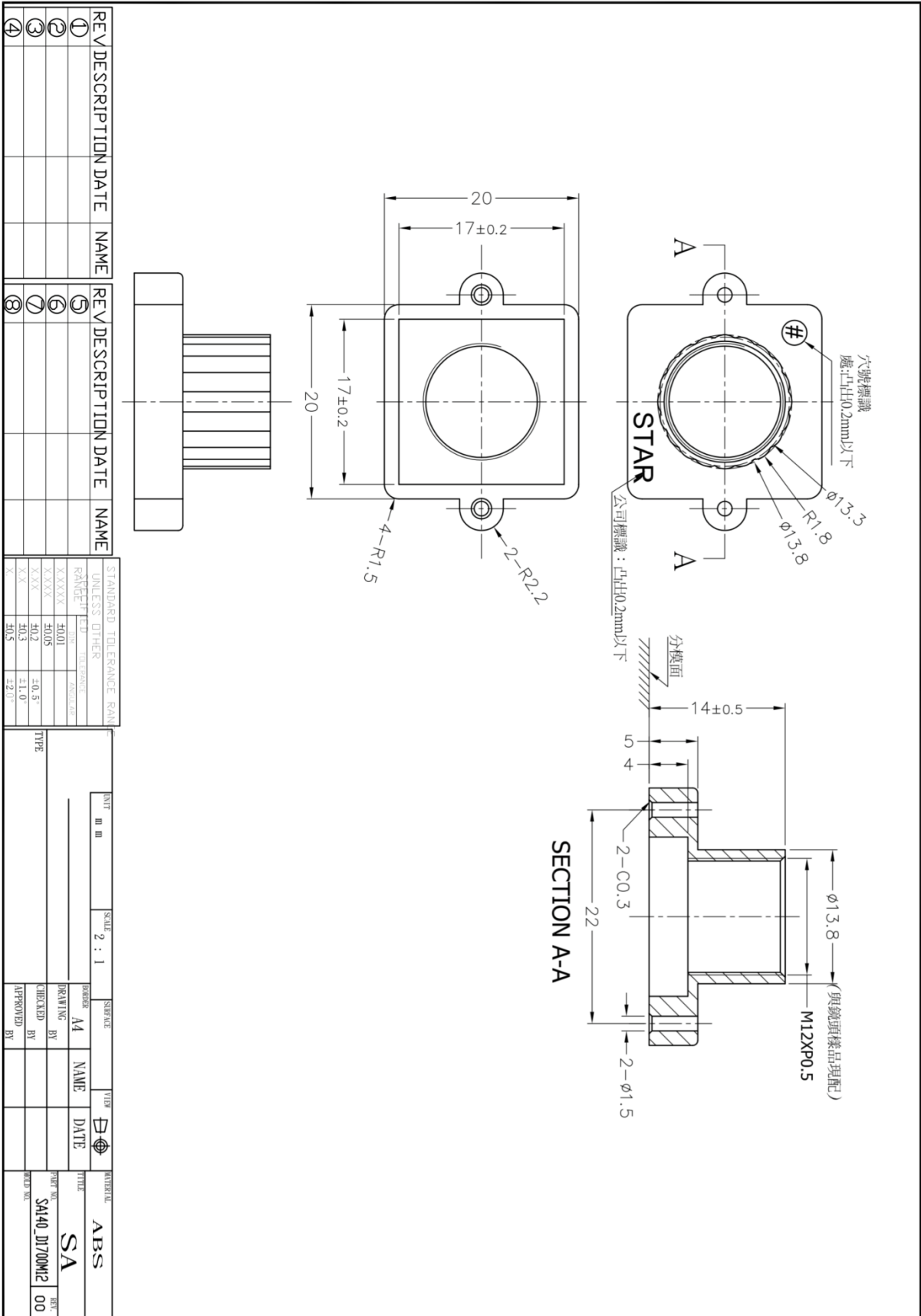
Turn Off the power supplies so that the power supplies fall in order of 2.9 V power supply ( $AV_{DD}$ ) → 1.8 V power supply ( $OV_{DD}$ ) → 1.2 V power supply ( $DV_{DD}$ ). In addition, all power supplies should falling within 200 ms. Set each digital input pin (INCK, XCE, SCK, SDI, XCLR, XMASTER, OMODE, XVS, XHS) to 0 V before the 1.8 V power supply ( $OV_{DD}$ ) falls.



Item	Symbol	Min.	Max.	Unit
Standby ON (communication) → LP11 mode start	$T_{STB}$	Until FE		—
LP00 → XCLR falling	$T_{CLR}$	128	—	cycle
2.9 V power shut down → 1.8 V power shut down	T4	0	—	ns
1.8 V power shut down → 1.2 V power shut down	T5	0	—	ns
Shut down time of all power supply	T6	—	200	ms



# SA140 lens mount Spec



Leopard Imaging Inc.

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 Phone: +1-408-263-0988  
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 Website: www.leopardimaging.com

## Revision History

Revision	Description	Release Date
1.0	First Release	11. May. 2019



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## Данный компонент на территории Российской Федерации

### Вы можете приобрести в компании MosChip.

Для оперативного оформления запроса Вам необходимо перейти по данной ссылке:

<http://moschip.ru/get-element>

Вы можете разместить у нас заказ для любого Вашего проекта, будь то серийное производство или разработка единичного прибора.

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Нашей специализацией является поставка электронной компонентной базы двойного назначения, продукции таких производителей как XILINX, Intel (ex.ALTERA), Vicor, Microchip, Texas Instruments, Analog Devices, Mini-Circuits, Amphenol, Glenair.

Сотрудничество с глобальными дистрибьюторами электронных компонентов, предоставляет возможность заказывать и получать с международных складов практически любой перечень компонентов в оптимальные для Вас сроки.

На всех этапах разработки и производства наши партнеры могут получить квалифицированную поддержку опытных инженеров.

Система менеджмента качества компании отвечает требованиям в соответствии с ГОСТ Р ИСО 9001, ГОСТ РВ 0015-002 и ЭС РД 009

### Офис по работе с юридическими лицами:

105318, г.Москва, ул.Щербаковская д.3, офис 1107, 1118, ДЦ «Щербаковский»

Телефон: +7 495 668-12-70 (многоканальный)

Факс: +7 495 668-12-70 (доб.304)

E-mail: [info@moschip.ru](mailto:info@moschip.ru)

Skype отдела продаж:

moschip.ru

moschip.ru\_4

moschip.ru\_6

moschip.ru\_9