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# MOS FIELD EFFECT TRANSISTOR 2SK3793

## SWITCHING N-CHANNEL POWER MOS FET

#### DESCRIPTION

The 2SK3793 is N-channel MOS Field Effect Transistor designed for high current switching applications.

#### FEATURES

Super low on-state resistance

 $R_{DS(on)1}$  = 125 m $\Omega$  MAX. (V<sub>GS</sub> = 10 V, I<sub>D</sub> = 6 A)

- $R_{DS(on)2}$  = 148 m $\Omega$  MAX. (V<sub>GS</sub> = 4.5 V, I<sub>D</sub> = 6 A)
- Low Ciss: Ciss = 900 pF TYP.
- Built-in gate protection diode

#### ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (VGS = 0 V)	VDSS	100	V
Gate to Source Voltage (VDS = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25°C)	D(DC)	±12	Α
Drain Current (pulse) <sup>Note1</sup>	D(pulse)	±22	Α
Total Power Dissipation (Tc = 25°C)	<b>P</b> T1	20	W
Total Power Dissipation (T <sub>A</sub> = 25°C)	Рт2	2.0	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to +150	°C
Single Avalanche Current Note2	las	10	Α
Single Avalanche Energy <sup>Note2</sup>	Eas	10	mJ

#### **Notes 1.** PW $\leq$ 10 $\mu$ s, Duty Cycle $\leq$ 1%

**2.** Starting T<sub>ch</sub> = 25°C, V<sub>DD</sub> = 50 V, R<sub>G</sub> = 25  $\Omega$ , V<sub>GS</sub> = 20  $\rightarrow$  0 V

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Document No. D16777EJ1V0DS00 (1st edition) Date Published March 2004 NS CP(K) Printed in Japan

#### ORDERING INFORMATION

PART NUMBER	PACKAGE
2SK3793	Isolated TO-220



(Isolated TO-220)

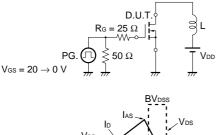
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	loss	V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V			10	μA
Gate Leakage Current	lgss	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0 V			±10	μA
Gate Cut-off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.5	2.0	2.5	V
Forward Transfer Admittance Note	y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 6 A	5.0	10.3		S
Drain to Source On-state Resistance Note	RDS(on)1	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 6 A		89	125	mΩ
	RDS(on)2	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 6 A		96	148	mΩ
Input Capacitance	Ciss	V <sub>DS</sub> = 10 V		900		pF
Output Capacitance	Coss	V <sub>GS</sub> = 0 V		110		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		50		pF
Turn-on Delay Time	td(on)	V <sub>DD</sub> = 50 V, I <sub>D</sub> = 6 A		9		ns
Rise Time	tr	V <sub>GS</sub> = 10 V		5		ns
Turn-off Delay Time	td(off)	R <sub>G</sub> = 0 Ω		30		ns
Fall Time	tr			4		ns
Total Gate Charge	QG	V <sub>DD</sub> = 80 V		21		nC
Gate to Source Charge	Q <sub>GS</sub>	V <sub>GS</sub> = 10 V		3.0		nC
Gate to Drain Charge	Qgd	ID = 12 A		6.2		nC
Body Diode Forward Voltage Note	VF(S-D)	IF = 12 A, VGS = 0 V		0.89	1.5	V
Reverse Recovery Time	trr	IF = 12 A, VGS = 0 V		52		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/ <i>µ</i> s		94		nC

### ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C)

Note Pulsed

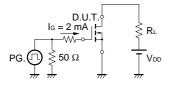
#### TEST CIRCUIT 1 AVALANCHE CAPABILITY

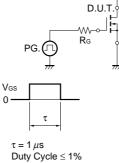
#### **TEST CIRCUIT 2 SWITCHING TIME**

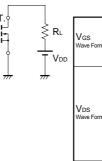


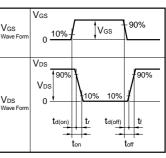


#### TEST CIRCUIT 3 GATE CHARGE

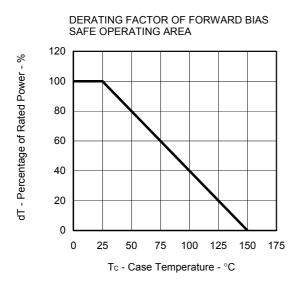




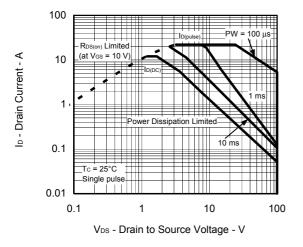


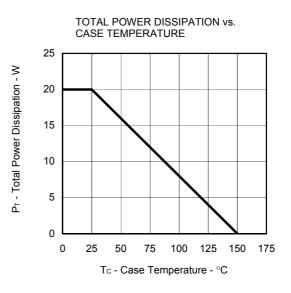


#### TYPICAL CHARACTERISTICS (TA = 25°C)

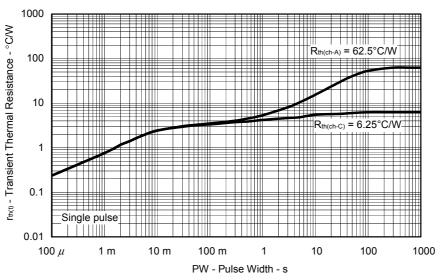




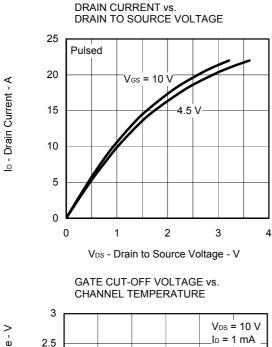


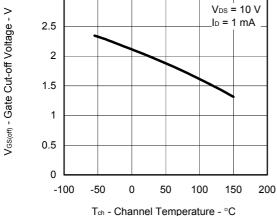




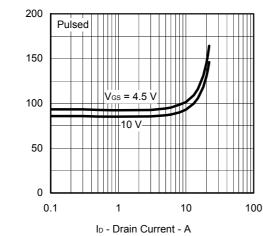


Data Sheet D16777EJ1V0DS

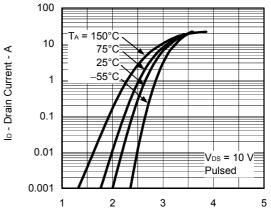




DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

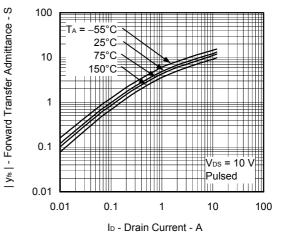


FORWARD TRANSFER CHARACTERISTICS

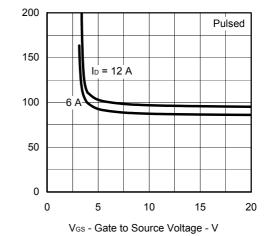


VGS - Gate to Source Voltage - V

FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT

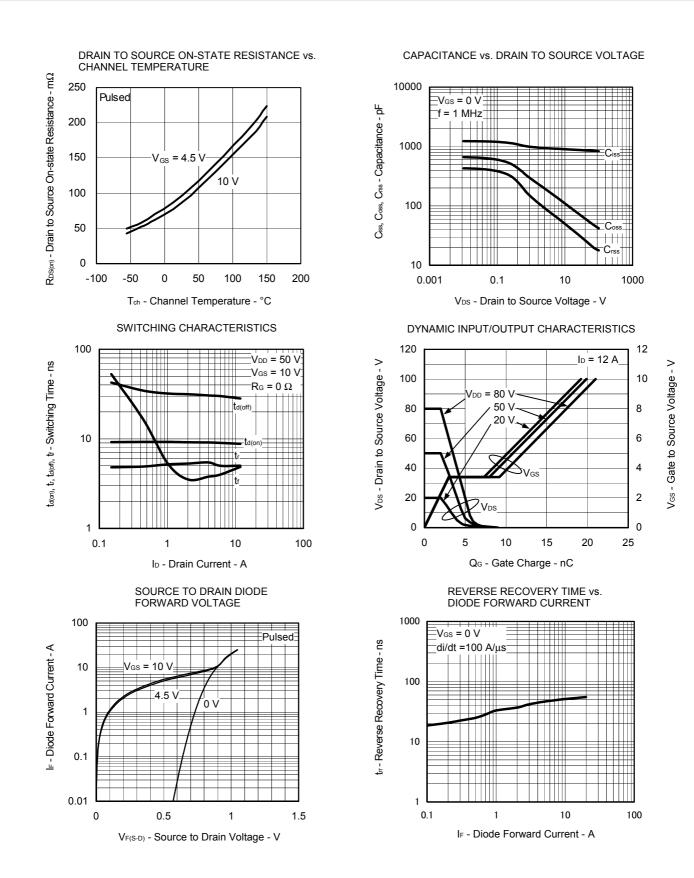


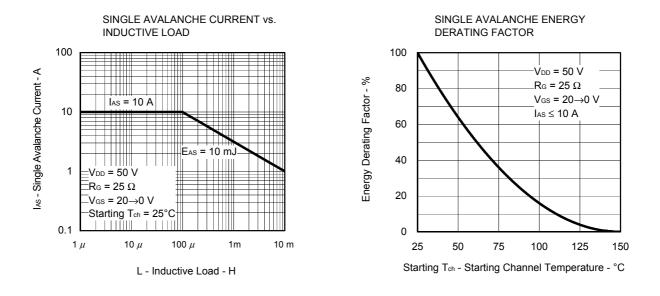
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



 $R_{DS(cn)}$  - Drain to Source On-state Resistance -  $m\Omega$ 

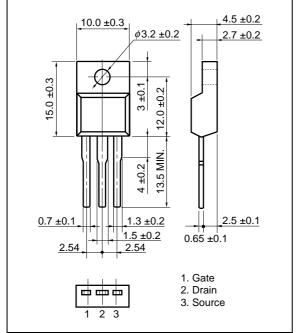
 $R_{DS(m)}$  - Drain to Source On-state Resistance -  $m\Omega$ 



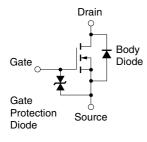


### PACKAGE DRAWING (Unit: mm)

#### Isolated TO-220 (MP-45F)



#### EQUIVALENT CIRCUIT



Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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