## Nch 600V 4A Power MOSFET

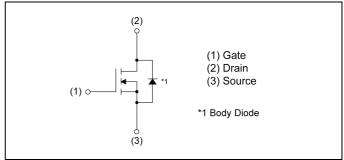
V <sub>DSS</sub>	600V
R <sub>DS(on)</sub> (Max.)	0.98Ω
I <sub>D</sub>	±4.0A
$P_D$	40W

# Outline TO-220FM

#### Features

- 1) Low on-resistance.
- 2) Ultra fast switching speed.
- 3) Parallel use is easy.
- 4) Pb-free lead plating; RoHS compliant

## •Inner circuit



Packaging specifications

- r dokaging opcomoducine				
	Packing	Bulk		
	Reel size (mm)	-		
T. m.o	Tape width (mm)	-		
Type	Basic ordering unit (pcs)	500		
	Taping code	-		
	Marking	R6004KNX		

# Application

Switching

## ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain - Source voltage		$V_{DSS}$	600	V
Continuous drain current (T <sub>c</sub> = 25	5°C)	I <sub>D</sub> *1	±4.0	Α
Pulsed drain current	l <sub>DP</sub> *2	±12	Α	
Gate - Source voltage	static	V <sub>GSS</sub>	±20	V
	AC(f>1Hz)		±30	V
Avalanche current, single pulse	·	I <sub>AS</sub>	0.8	Α
Avalanche energy, single pulse		E <sub>AS</sub> *3	46	mJ
Power dissipation (T <sub>c</sub> = 25°C)		P <sub>D</sub>	40	W
Junction temperature		T <sub>j</sub>	150	°C
Operating junction and storage te	mperature range	T <sub>stg</sub>	-55 to +150	°C

#### ●Thermal resistance

Downwortow	Cymah al	Values			l lesit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R <sub>thJC</sub> *4	-	-	3.13	°C/W
Thermal resistance, junction - ambient	R <sub>thJA</sub>	-	-	70	°C/W
Soldering temperature, wavesoldering for 10s	T <sub>sold</sub>	-	-	265	°C

# • Electrical characteristics $(T_a = 25^{\circ}C)$

Parameter	Symbol	Conditions	Values			Unit
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V$ , $I_D = 1mA$	600	-	1	V
		V <sub>DS</sub> = 600V, V <sub>GS</sub> = 0V				
Zero gate voltage drain current	I <sub>DSS</sub>	$T_j = 25^{\circ}C$	-	-	100	μΑ
aram canoni		$T_j = 125^{\circ}C$	-	-	1000	
Gate - Source leakage current	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V	-	-	±100	nA
Gate threshold voltage	$V_{GS(th)}$	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA	3	-	5	V
		V <sub>GS</sub> = 10V, I <sub>D</sub> = 1.5A				
Static drain - source on - state resistance	R <sub>DS(on)</sub> *5	$T_j = 25^{\circ}C$	-	0.90	0.98	Ω
		$T_j = 125^{\circ}C$	-	1.36	-	
Gate resistance	$R_{G}$	f = 1MHz, open drain	-	3.3	-	Ω

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Darramatar	Cymah al	Conditions	Values			Lloit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Transfer Admittance	Y <sub>fs</sub>  *5	V <sub>DS</sub> = 10V, I <sub>D</sub> = 2A	1.5	3.0	-	S
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	280	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 25V	-	280	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	16	-	
Turn - on delay time	t <sub>d(on)</sub> *5	$V_{DD} \simeq 300V$ , $V_{GS} = 10V$	-	15	-	
Rise time	t <sub>r</sub> *5	I <sub>D</sub> = 2A	-	10	-	20
Turn - off delay time	t <sub>d(off)</sub> *5	R <sub>L</sub> ≃ 150Ω	-	30	-	ns
Fall time	t <sub>f</sub> *5	$R_G = 10\Omega$	-	25	-	

# ● Gate charge characteristics (T<sub>a</sub> = 25°C)

Davarratar	Comple al	Conditions	Values			1.114
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	$Q_g^{*5}$	V <sub>DD</sub> ≈ 300V	-	10.2	-	
Gate - Source charge	Q <sub>gs</sub> *5	I <sub>D</sub> = 4A	-	2.5	-	nC
Gate - Drain charge	Q <sub>gd</sub> *5	V <sub>GS</sub> = 10V	-	4.8	-	
Gate plateau voltage	V <sub>(plateau)</sub>	V <sub>DD</sub> ≈ 300V, I <sub>D</sub> = 4A	-	5.8	-	V

<sup>\*1</sup> Limited only by maximum channel temperature allowed.

<sup>\*2</sup> Pw ≤ 10µs, Duty cycle ≤ 1%

<sup>\*3</sup> L  $\stackrel{=}{=}$  100mH, V<sub>DD</sub>=50V, R<sub>G</sub>=25 $\Omega$ , STARTING T<sub>j</sub>=25 $^{\circ}$ C

<sup>\*4</sup> T<sub>C</sub>=25°C

<sup>\*5</sup> Pulsed

# ●Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions		Unit		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offit
Continuous forward current	I <sub>S</sub> *1	T - 25°C	-	1	4.0	А
Pulse forward current	I <sub>SP</sub> *2	T <sub>C</sub> = 25°C	-	-	12	А
Forward voltage	V <sub>SD</sub> *5	V <sub>GS</sub> = 0V, I <sub>S</sub> = 4A	-	-	1.5	V
Reverse recovery time	t <sub>rr</sub> *5		-	230	-	ns
Reverse recovery charge	Q <sub>rr</sub> *5	$I_S = 4A$ di/dt = 100A/µs	-	1.5	-	μC
Peak reverse recovery current	I <sub>rrm</sub> *5	- α, ατ 100/ γμο	-	12	-	Α

# Typical transient thermal characteristics

Symbol	Value	Unit
R <sub>th1</sub>	0.557	
R <sub>th2</sub>	1.61	K/W
R <sub>th3</sub>	2.24	

Symbol	Value	Unit
C <sub>th1</sub>	0.00102	
C <sub>th2</sub>	0.00898	Ws/K
C <sub>th3</sub>	0.440	

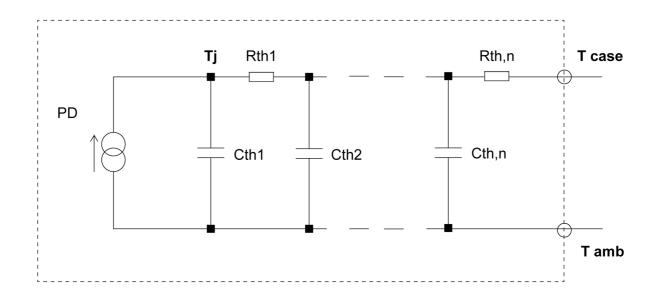


Fig.1 Power Dissipation Derating Curve

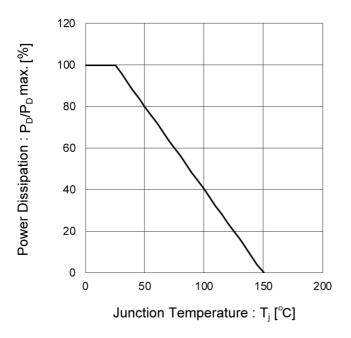


Fig.2 Maximum Safe Operating Area

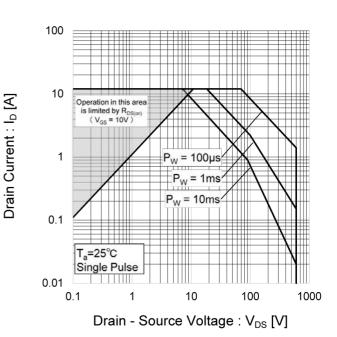


Fig.3 Avalanche Energy Derating Curve vs Junction temprature

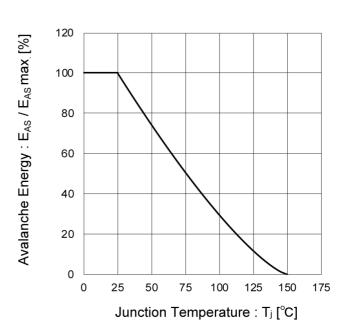


Fig.4 Typical Output Characteristics(I)

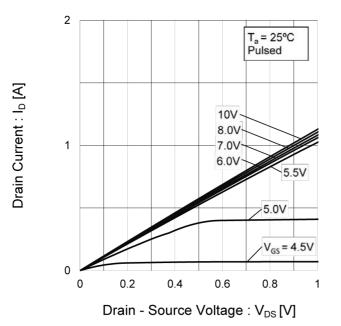
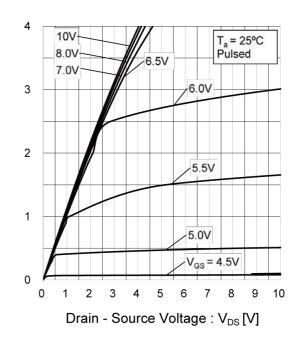


Fig.5 Typical Output Characteristics(II)



Drain Current : I<sub>D</sub> [A]

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Fig. 6 Breakdown Voltage vs.
Junction Temperature

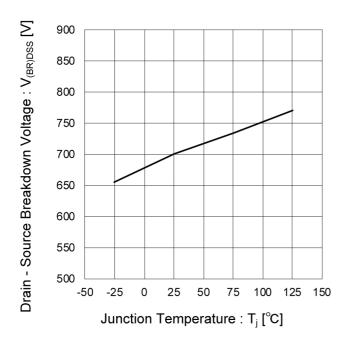


Fig.7 Typical Transfer Characteristics

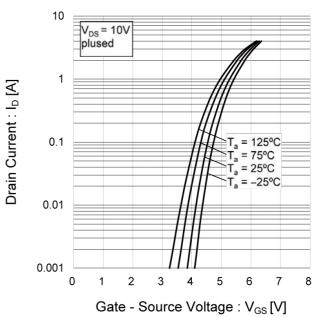


Fig.8 Gate Threshold Voltage vs.
Junction Temperature

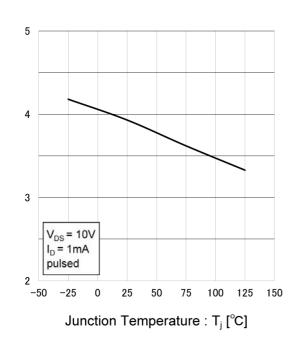
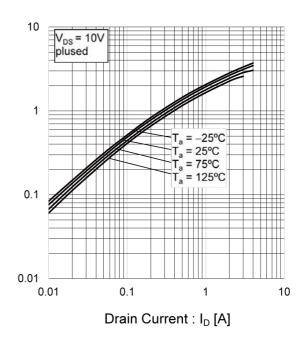


Fig.9 Forward Transfer Admittance vs.

Drain Current



Gate Threshold Voltage: VGS(th) [V]

Transconductance: gfs [S]

Fig.10 Static Drain - Source On - State Resistance vs. Gate Source Voltage

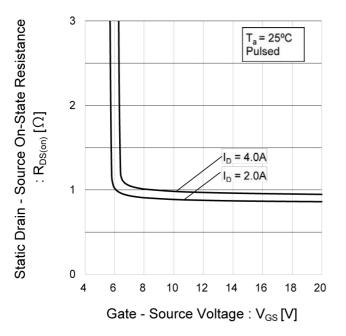


Fig.11 Static Drain - Source On - State Resistance vs. Junction Temperature

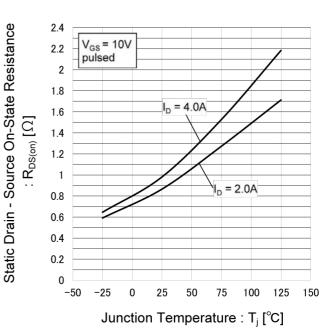


Fig.12 Static Drain - Source On - State Resistance vs. Drain Current(I)

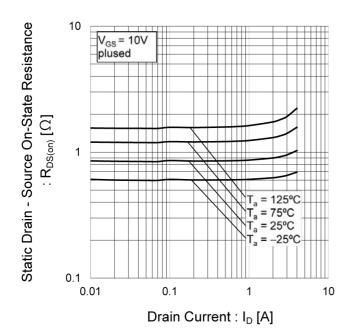
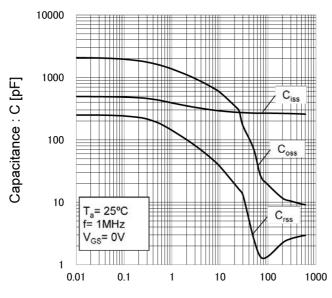


Fig.13 Typical Capacitance vs. Drain - Source Voltage



Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.14 Switching Characteristics

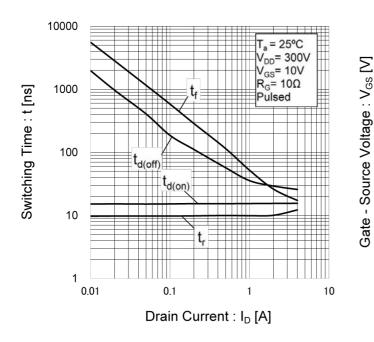
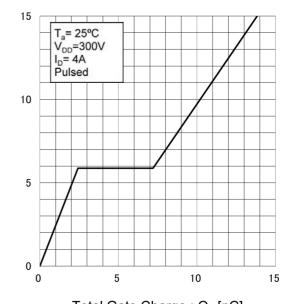


Fig.15 Dynamic Input Characteristics

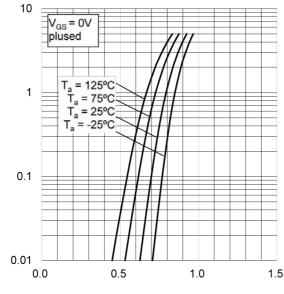


Total Gate Charge : Q<sub>g</sub> [nC]

Inverse Diode Forward Current : Is [A]

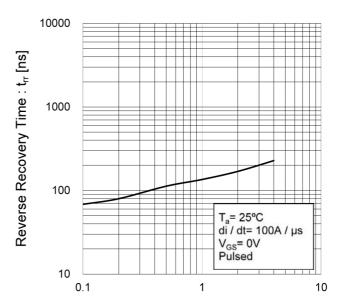
## • Electrical characteristic curves

Fig.16 Inverse Diode Forward Current vs. Source - Drain Voltage



Source - Drain Voltage : V<sub>SD</sub> [V]

Fig.17 Reverse Recovery Time vs.
Inverse Diode Forward Current



Inverse Diode Forward Current : I<sub>S</sub> [A]

#### Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

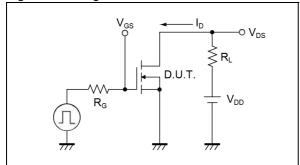


Fig.2-1 Gate Charge Measurement Circuit

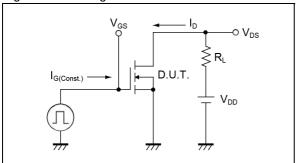


Fig.3-1 Avalanche Measurement Circuit

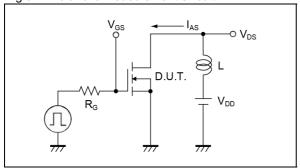


Fig.4-1 dv/dt Measurement Circuit

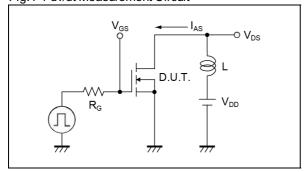


Fig.5-1 dv/dt Measurement Circuit

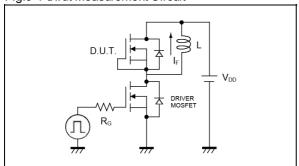


Fig.1-2 Switching Waveforms

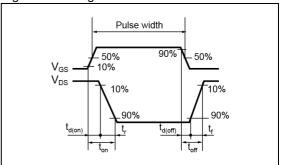


Fig.2-2 Gate Charge Waveform

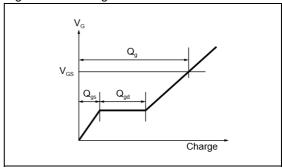


Fig.3-2 Avalanche Waveform

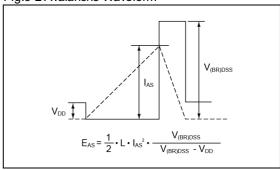


Fig.4-2 dv/dt Waveform

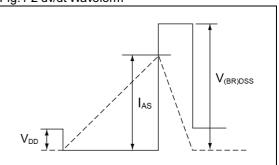
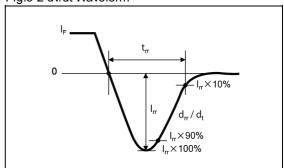
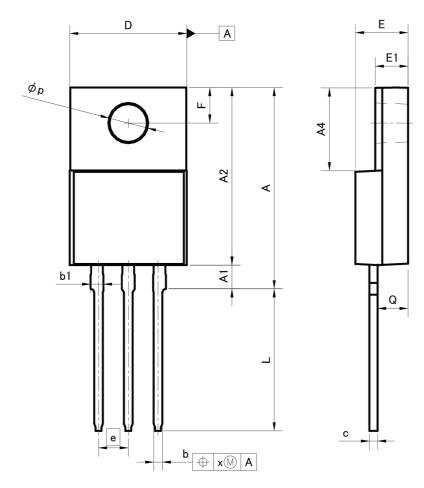


Fig.5-2 dv/dt Waveform



## Dimensions

TO-220FM



DIM	MILIM	ETERS	INCH	HES
DIM	MIN	MAX	MIN	MAX
Α	16.60	17.60	0.654	0.693
A1	1.80	2.20	0.071	0.087
A2	14.80	15.40	0.583	0.606
A4	6.80	7.20	0.268	0.283
b	0.70	0.85	0.028	0.033
b1	1.10	1.50	0.043	0.059
С	0.70	0.85	0.028	0.033
D	9.90	10.30	0.390	0.406
E	4.40	4.80	0.173	0.189
е	2.	54	0.10	00
E1	2.70	3.00	0.106	0.118
F	2.80	3.20	0.110	0.126
L	11.50	12.50	0.453	0.492
р	3.00	3.40	0.118	0.134
Q	2.10	3.10	0.083	0.122
х	-	0.38		0.015

Dimension in mm/inches



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