

FCD600N65S3R0

Power MOSFET, N-Channel, SUPERFET[®] III, Easy Drive, 650 V, 6 A, 600 mΩ

Description

SUPERFET III MOSFET is ON Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This advanced technology is tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate.

Consequently, SUPERFET III MOSFET Easy drive series helps manage EMI issues and allows for easier design implementation.

Features

- 700 V @ $T_J = 150^{\circ}\text{C}$
- Typ. $R_{DS(on)} = 493\text{ m}\Omega$
- Ultra Low Gate Charge (Typ. $Q_g = 11\text{ nC}$)
- Low Effective Output Capacitance (Typ. $C_{oss(eff.)} = 127\text{ pF}$)
- 100% Avalanche Tested
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

Applications

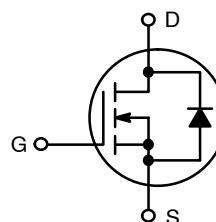
- Computing / Display Power Supplies
- Telecom / Server Power Supplies
- Industrial Power Supplies
- Lighting / Charger / Adapter



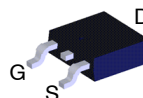
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V_{DS}	$R_{DS(on)}\text{ MAX}$	$I_D\text{ MAX}$
650 V	600 mΩ @ 10 V	6 A

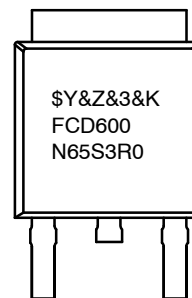


N-Channel MOSFET



D-PAK
(DPAK3 (TO-252 3LD))
CASE 369AS

MARKING DIAGRAM



\$Y	= ON Semiconductor Logo
&Z	= Assembly Plant Code
&3	= Numeric Date Code
&K	= Lot Code
FCD600N65S3R0	= Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

FCD600N65S3R0

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$, Unless otherwise specified)

Symbol	Parameter		Value	Unit
V_{DSS}	Drain to Source Voltage		650	V
V_{GSS}	Gate to Source Voltage	DC	± 30	V
		AC ($f > 1\text{ Hz}$)	± 30	V
I_D	Drain Current	Continuous ($T_C = 25^\circ\text{C}$)	6	A
		Continuous ($T_C = 100^\circ\text{C}$)	3.8	
I_{DM}	Drain Current	Pulsed (Note 1)	15	A
E_{AS}	Single Pulsed Avalanche Energy (Note 2)		24	mJ
I_{AS}	Avalanche Current (Note 2)		1.6	A
E_{AR}	Repetitive Avalanche Energy (Note 1)		0.54	mJ
dv/dt	MOSFET dv/dt		100	V/ns
	Peak Diode Recovery dv/dt (Note 3)		20	
P_D	Power Dissipation	($T_C = 25^\circ\text{C}$)	54	W
		Derate Above 25°C	0.43	W/ $^\circ\text{C}$
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +150	$^\circ\text{C}$
T_L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 s		300	$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Repetitive rating: pulse-width limited by maximum junction temperature.

2. $I_{AS} = 1.6\text{ A}$, $R_G = 25\ \Omega$, starting $T_J = 25^\circ\text{C}$.

3. $I_{SD} \leq 3\text{ A}$, $di/dt \leq 200\text{ A}/\mu\text{s}$, $V_{DD} \leq 400\text{ V}$, starting $T_J = 25^\circ\text{C}$.

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	2.3	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max. (Note 4)	52	

4. Device on 1 in² pad 2 oz copper pad on 1.5 x 1.5 in. board of FR-4 material.

PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Reel Size	Tape Width	Shipping (Qty / Packing) [†]
FCD600N65S3R0	FCD600N65S3R0	D-PAK (DPAK3 (TO-252 3LD)) (Pb-Free / Halogen Free)	330 mm	16 mm	2500 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

FCD600N65S3R0

ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

BV _{DSS}	Drain to Source Breakdown Voltage	V _{GS} = 0 V, I _D = 1 mA, T _J = 25°C	650	–	–	V
		V _{GS} = 0 V, I _D = 1 mA, T _J = 150°C	700	–	–	V
ΔBV _{DSS} /ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 1 mA, Referenced to 25°C	–	0.66	–	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 650 V, V _{GS} = 0 V	–	–	1	μA
		V _{DS} = 520 V, T _C = 125°C	–	0.3	–	
I _{GSS}	Gate to Body Leakage Current	V _{GS} = ±30 V, V _{DS} = 0 V	–	–	±100	nA

ON CHARACTERISTICS

V _{GS(th)}	Gate Threshold Voltage	V _{GS} = V _{DS} , I _D = 0.6 mA	2.5	–	4.5	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 3 A	–	493	600	mΩ
g _{FS}	Forward Transconductance	V _{DS} = 20 V, I _D = 3 A	–	3.6	–	S

DYNAMIC CHARACTERISTICS

C _{iss}	Input Capacitance	V _{DS} = 400 V, V _{GS} = 0 V, f = 1 MHz	–	465	–	pF
C _{oss}	Output Capacitance		–	10	–	pF
C _{oss(eff.)}	Effective Output Capacitance	V _{DS} = 0 V to 400 V, V _{GS} = 0 V	–	127	–	pF
C _{oss(er.)}	Energy Related Output Capacitance	V _{DS} = 0 V to 400 V, V _{GS} = 0 V	–	17	–	pF
Q _{g(tot)}	Total Gate Charge at 10 V	V _{DS} = 400 V, I _D = 3 A, V _{GS} = 10 V (Note 5)	–	11	–	nC
Q _{gs}	Gate to Source Gate Charge		–	3	–	nC
Q _{gd}	Gate to Drain “Miller” Charge		–	4.9	–	nC
ESR	Equivalent Series Resistance	f = 1 MHz	–	0.9	–	Ω

SWITCHING CHARACTERISTICS

t _{d(on)}	Turn-On Delay Time	V _{DD} = 400 V, I _D = 3 A, V _{GS} = 10 V, R _g = 4.7 Ω (Note 5)	–	11	–	ns
t _r	Turn-On Rise Time		–	9	–	ns
t _{d(off)}	Turn-Off Delay Time		–	29	–	ns
t _f	Turn-Off Fall Time		–	14	–	ns

SOURCE-DRAIN DIODE CHARACTERISTICS

I _S	Maximum Continuous Source to Drain Diode Forward Current		–	–	6	A
I _{SM}	Maximum Pulsed Source to Drain Diode Forward Current		–	–	15	A
V _{SD}	Source to Drain Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 3 A	–	–	1.2	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _{SD} = 3 A, dI _F /dt = 100 A/μs	–	198	–	ns
Q _{rr}	Reverse Recovery Charge		–	1.6	–	μC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

5. Essentially independent of operating temperature typical characteristics.

TYPICAL PERFORMANCE CHARACTERISTICS

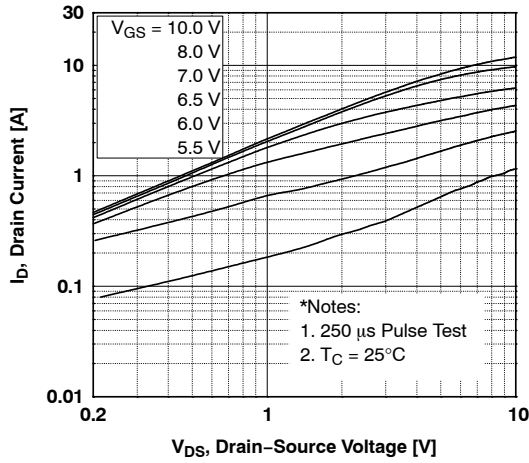


Figure 1. On-Region Characteristics

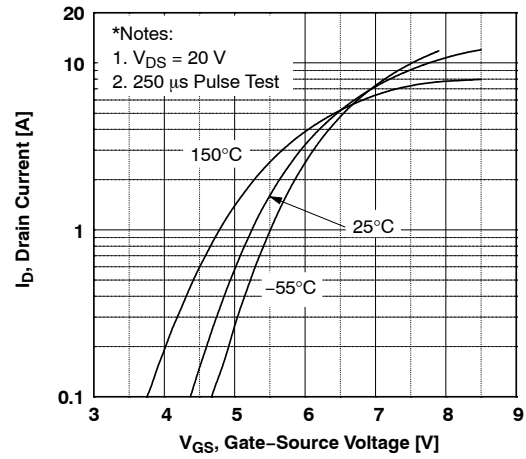


Figure 2. Transfer Characteristics

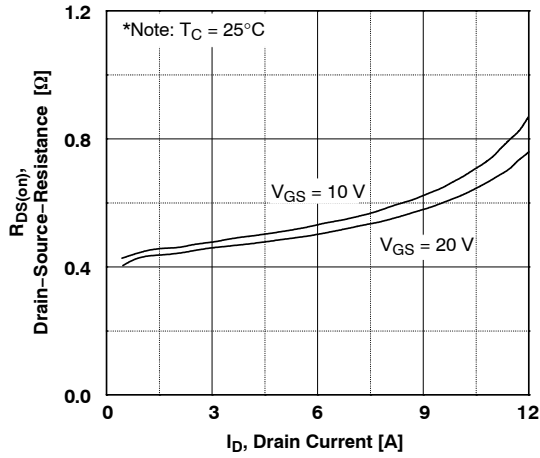


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

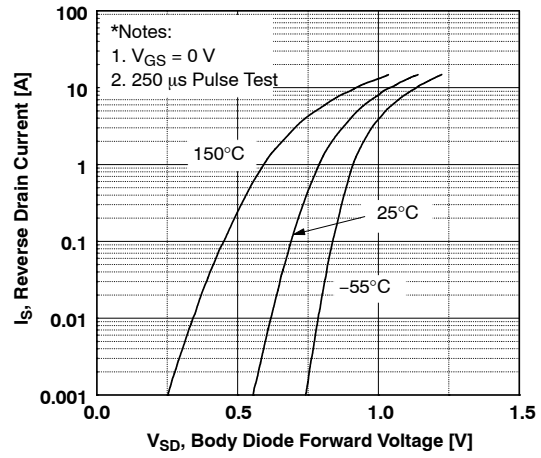


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

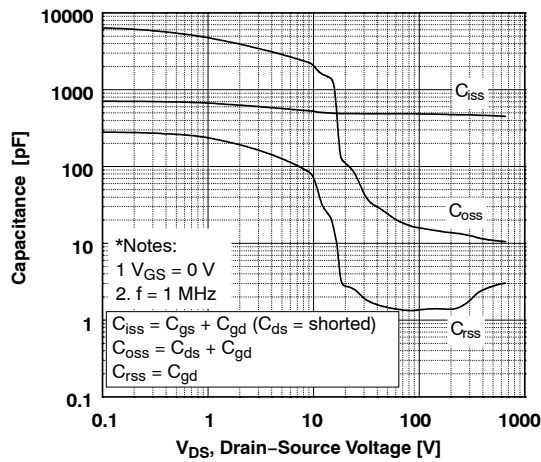


Figure 5. Capacitance Characteristics

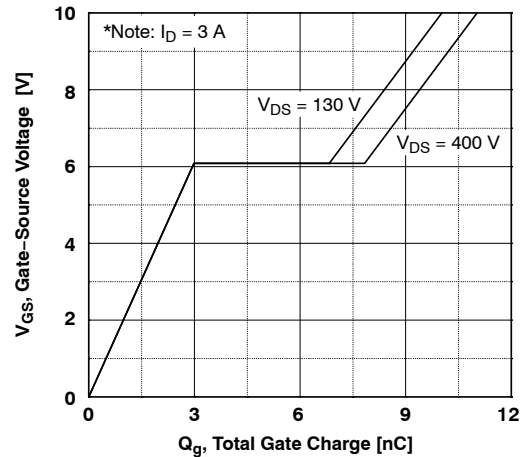


Figure 6. Gate Charge Characteristics

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

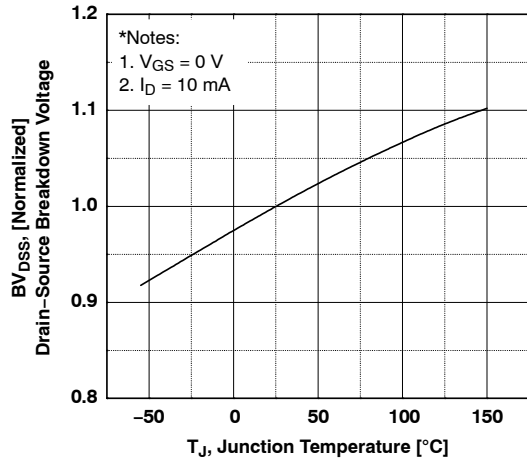


Figure 7. Breakdown Voltage Variation vs. Temperature

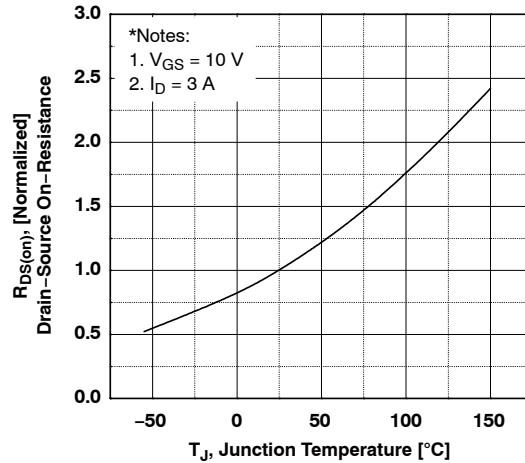


Figure 8. On-Resistance Variant vs. Temperature

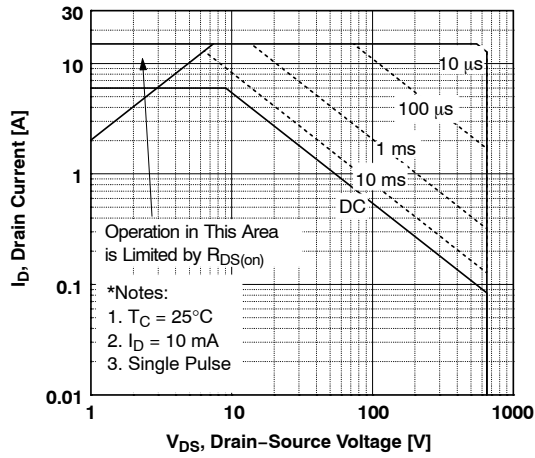


Figure 9. Maximum Safe Operation Area

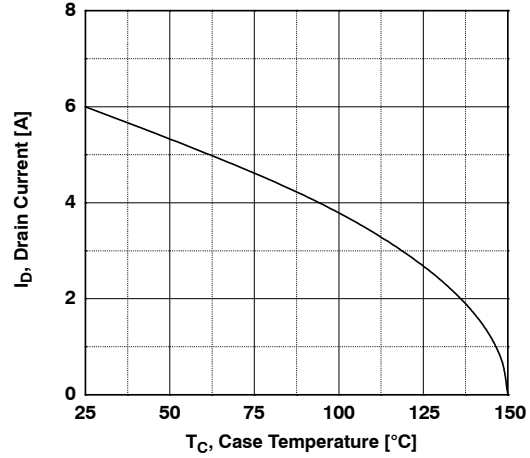


Figure 10. Maximum Drain Current vs. Case Temperature

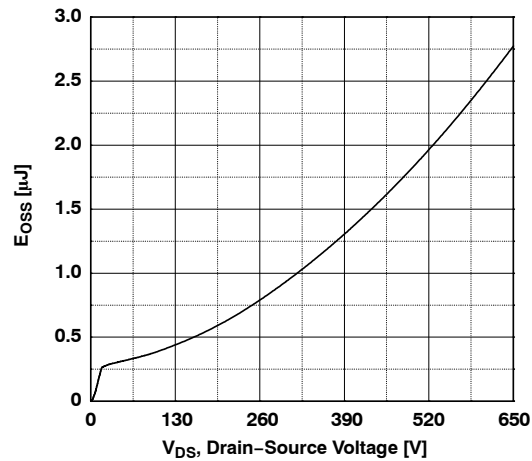


Figure 11. E_{OSS} vs. Drain to Source Voltage

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

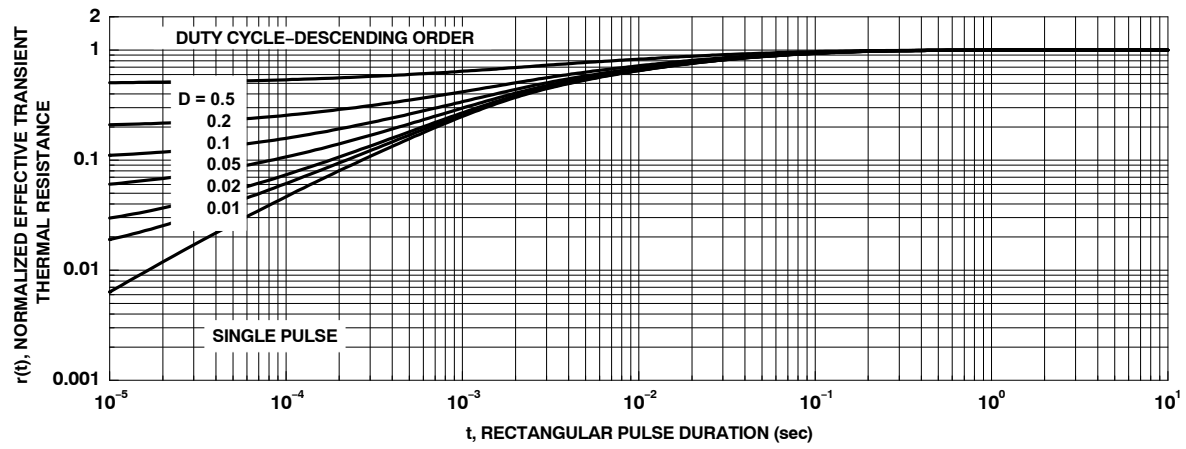


Figure 12. Transient Thermal Response Curve

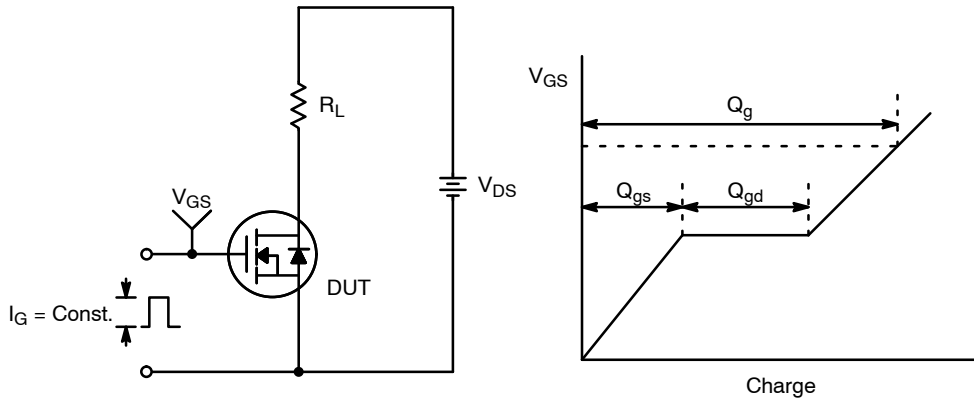


Figure 13. Gate Charge Test Circuit & Waveform

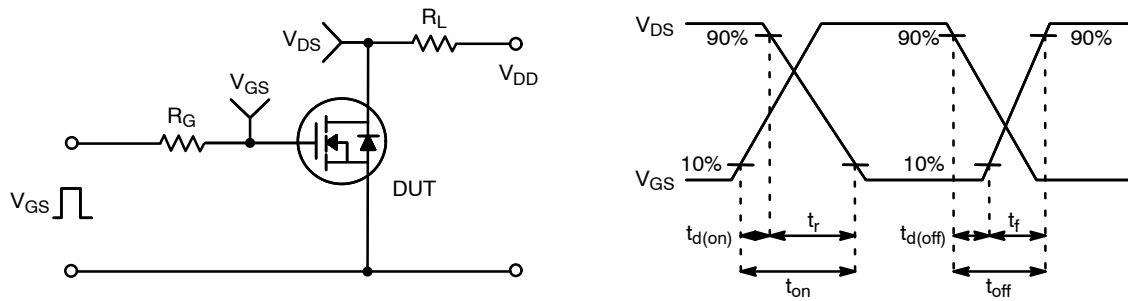


Figure 14. Resistive Switching Test Circuit & Waveforms

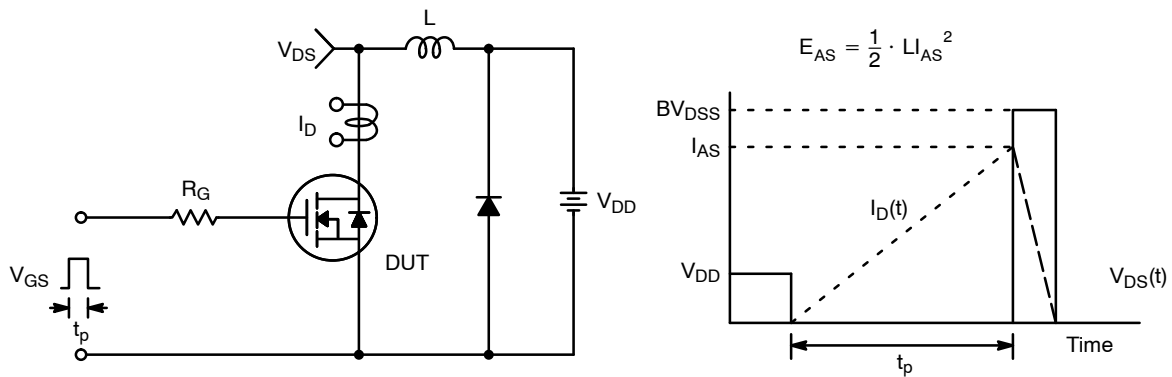


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

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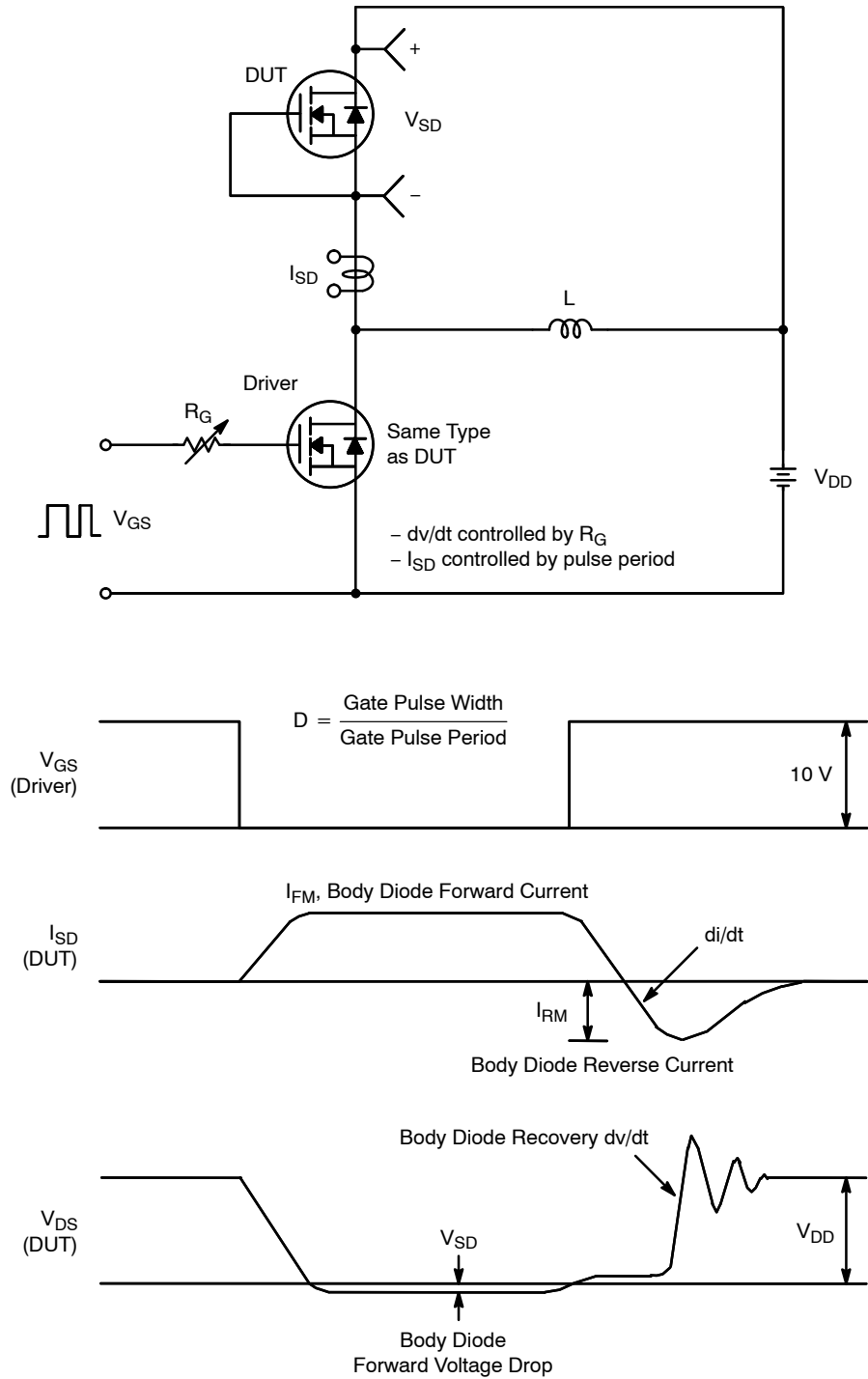


Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

ON

DATE 30 SEP 2016

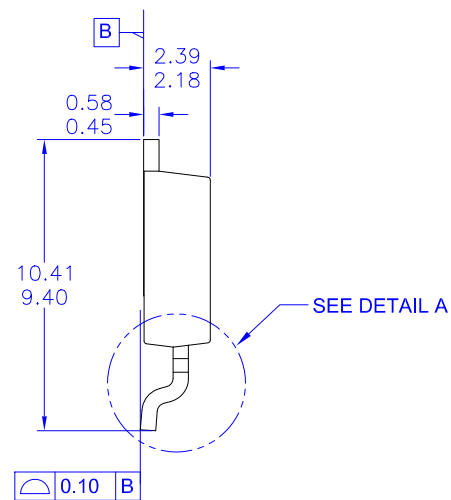
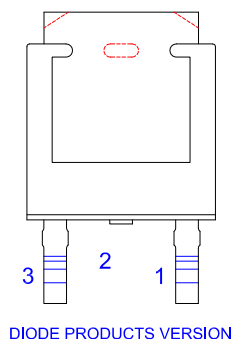
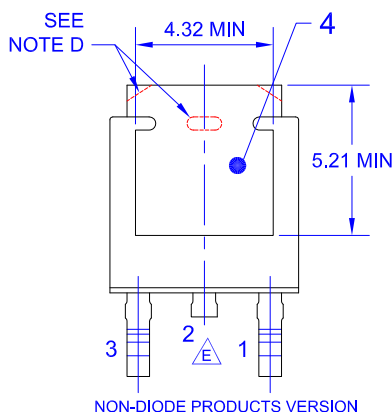
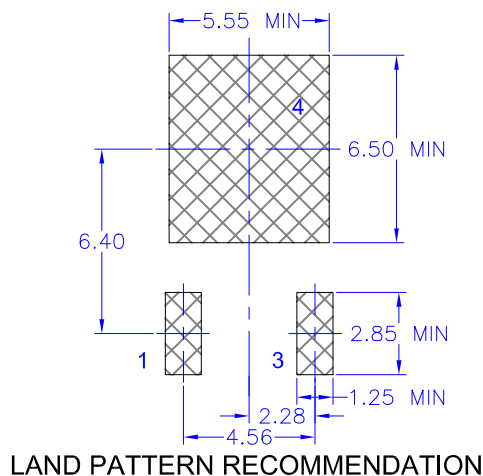


Figure 10 shows a technical drawing of a mechanical part with various dimensions. The part has a base width of 2.90. A gage plane is indicated by a dashed line. Dimensions include 0.61 and 0.45 for the top left section, 1.78 and 1.40 for the middle section, 0.51 for the top right section, 0.127 MAX for the bottom right section, and 1.54 for the bottom right section. A 10° angle is shown on the left side. A seating plane is indicated by a solid line at the bottom.

DETAIL A
(ROTATED -90°)
SCALE: 12X

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DESCRIPTION:	DPAK3 (TO-252 3 LD)	PAGE 1 OF 2

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Нашей специализацией является поставка электронной компонентной базы двойного назначения, продукции таких производителей как XILINX, Intel (ex.ALTERA), Vicor, Microchip, Texas Instruments, Analog Devices, Mini-Circuits, Amphenol, Glenair.

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Офис по работе с юридическими лицами:

105318, г.Москва, ул.Щербаковская д.3, офис 1107, 1118, ДЦ «Щербаковский»

Телефон: +7 495 668-12-70 (многоканальный)

Факс: +7 495 668-12-70 (доб.304)

E-mail: info@moschip.ru

Skype отдела продаж:

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