

## Description

The Atmel® ProChip Designer® v5.0.1 is a fully featured EDA software suite incorporating state-of-the-art synthesis and simulation tools from Mentor Graphics with the Atmel user-friendly interface and design navigator, as well as, powerful fitter technologies. The combination of these tools enables CPLD designers to achieve the highest possible logic utilization from the Atmel ATF15xxAS/ASL/ASV/ASVL/BE<sup>(1)</sup> series CPLDs with Logic Doubling® features. Through all of the software components available within ProChip, the following features are supported:

- Design Entry and Synthesis
  - VHDL
  - Verilog
  - CUPL<sup>(2)</sup>
  - Schematic<sup>(2)</sup>
- Simulation
  - VHDL Functional and Timing Simulation<sup>(3)</sup>
  - Verilog Functional and Timing Simulation<sup>(3)</sup>
  - CUPL Functional Simulation<sup>(2)</sup>
- Device
  - ATF15xxBE:<sup>(1)</sup> 1.8V ultra-low-power CPLD
  - ATF15xxAS/ASL: 5.0V standard and low-power CPLD
  - ATF15xxASV/ASVL: 3.0V standard and low-power CPLD
- Programming
  - JTAG ISP

---

1. The ATF15xxBE is not recommended for new design.  
2. Optional and requires Altium software.  
3. Optional and requires a separate license from Mentor Graphics.

## Features

ProChip Designer seamlessly integrates the following software components into one user-friendly Integrated Development Environment (IDE) for the ATF15xx CPLDs:

ProChip Designer Software Components	
<b>Precision<sup>®</sup> RTL Synthesis</b> (Atmel-OEM version)	VHDL and Verilog <sup>®</sup> synthesis supports from Mentor Graphics <sup>®</sup> .
<b>ModelSim<sup>®</sup></b> ( <i>Optional*</i> ) (Atmel-OEM version)	VHDL and Verilog simulation supports from Mentor Graphics. *Requires a separate Mentor Graphics license.
<b>FIT15xx</b>	Fitting support for the ATF15xx CPLDs from Atmel.
<b>HDL Planner</b>	VHDL and Verilog design entry and text editing supports from Atmel.
<b>ATMISP</b>	JTAG In-System programming (ISP) support from Atmel.

The following optional third-party software from Altium<sup>®</sup> can be installed separately and can be used independently or in conjunction with ProChip Designer for schematic and CUPL<sup>®</sup> designs:

Optional Third-party Software	
<b>Protel<sup>®</sup> Design Explorer 99SE</b> (Atmel Edition)	Schematic and CUPL design entries, synthesis, and functional simulation supports from Altium.

# 1. ProChip Designer Software Suite Components and Licensing Terms

The software components within ProChip Designer are divided into two groups:

- Schematic/CUPL Design Flow
- VHDL/Verilog Design Flow

These two software groups, their contents, licensing terms, and ordering code are described in [Table 1-1](#) below.

**Table 1-1. ProChip Designer Contents and Licensing Terms**

Design Flow	Software Components	Licensing Term
<b>Schematic/CUPL Design Flow</b>	ProChip Designer with HDL Planner, Atmel Fitter, and ATMISP.	Free
	<a href="#">Altium Design Explorer 99SE</a> .	<a href="#">Permanent License [P/N: ATDS1500PC]</a>
<b>VHDL/Verilog Design Flow</b>	ProChip Designer with HDL Planner, Atmel Fitter, and ATMISP.	Free
	<a href="#">Mentor Graphics Precision RTL Synthesis (Atmel-OEM version)</a> .	<a href="#">2-year License [P/N: ATDS15xxKSW1]</a>
	<a href="#">Mentor Graphics ModelSim (Atmel-OEM version)</a> .	<a href="#">Contact Mentor Graphics</a>

Note: 1. The Atmel software tools are supported on Microsoft® Windows® 7/8/10 operating systems. The details of the operating system supports for other vendors' tools can be obtained from the third-party tool vendors' websites or documentation.

## 1.1 Default Installation of Software Components

By default, the installation of ProChip Designer includes the installation of the following software components:

- HDL Planner
- ATF15xx Fitters
- ATMISP
- Precision RTL Synthesis
- ModelSim



Unlike ProChip Designer v4.0, the installation of Altium tools are no longer included in ProChip Designer v5.0.1 installer.

*Optionally*, Protel Design Explorer 99SE can be installed separately, and it can be used independently or in conjunction with ProChip Designer v5.0.1 for schematic and CUPL designs.

## 1.2 Software Component Licensing

### 1.2.1 Mentor Graphics Precision RTL Synthesis

To use Mentor Graphics Precision RTL Synthesis in ProChip Designer for VHDL or Verilog synthesis, a FLEXLM (node locked) license file from Atmel is required. License requests can be submitted to Atmel for evaluation on-line at the following URL:

[www.atmel.com/System/BaseForm.aspx?target=tcm:26-17233](http://www.atmel.com/System/BaseForm.aspx?target=tcm:26-17233)

The 2-year license for Precision RTL Synthesis (Atmel OEM version) can also be purchased from an Atmel distributor.

**Table 1-2. Mentor Graphics Precision RTL Synthesis License**

2-year License	Atmel Ordering Code
Precision RTL Synthesis (Atmel OEM version)	ATDS15xxKSW1

Once a license has been purchased, instructions on how to download ProChip Designer from the Atmel website and how to obtain the license file will be mailed..



Atmel no longer provides licenses for the Mentor Graphics ModelSim software.

Please contact Mentor Graphics for information on how to obtain the appropriate license for ModelSim if VHDL/Verilog simulation is required.

### 1.2.2 Altium Protel Design Explorer 99SE

The 30-day trial version of Altium Protel Design Explorer 99SE is no longer available, and the Licensed Edition of Altium Protel Design Explorer 99SE *must* be ordered separately.

The Atmel Licensed Edition of Altium Protel Design Explorer 99SE can be ordered from an Atmel distributor and the Atmel.

**Table 1-3. Altium Protel Design Explorer 99SE License**

Permanent License	Atmel Ordering Code
Altium Protel Design Explorer 99SE	ATDS1500PC

Once a license has been purchased, instructions on how to obtain ProChip Designer and Protel Design Explorer 99SE along with a 16-digit Access Code (permanent license) will be mailed. Users can then follow the instructions provided to install the Licensed Edition software.

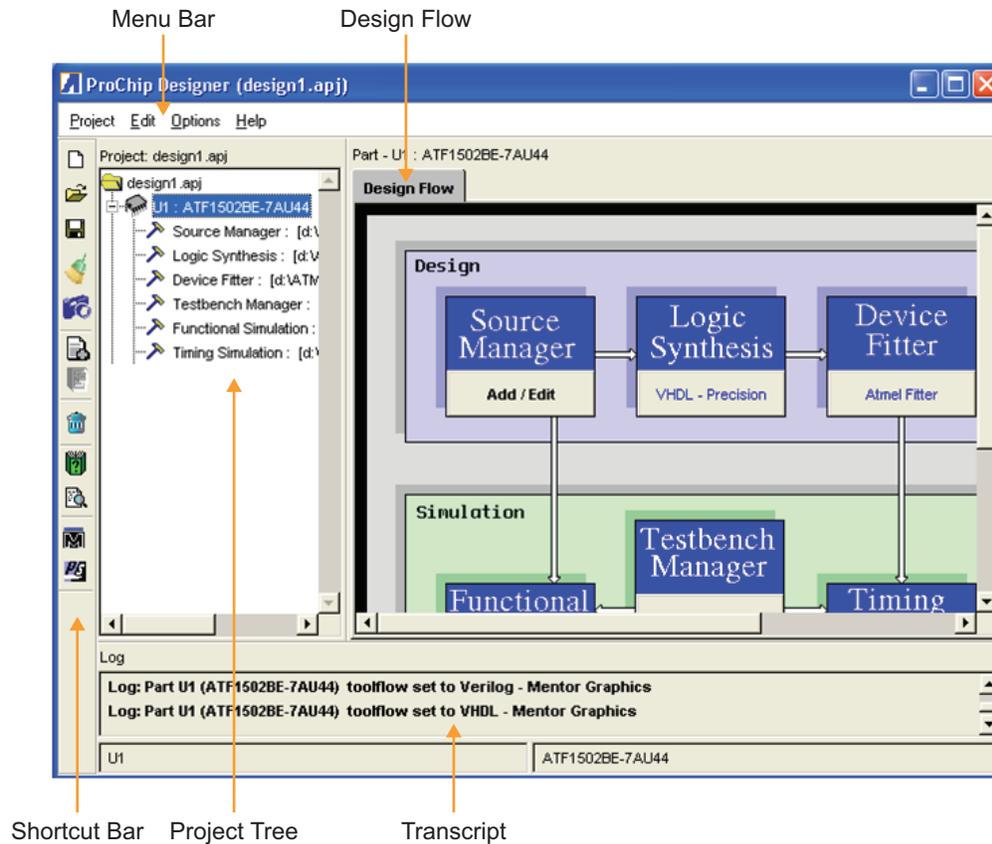
The ProChip Designer v5.0.1 software is available on the Atmel website at the following URL:

[www.atmel.com/tools/PROCHIPDESIGNER.aspx](http://www.atmel.com/tools/PROCHIPDESIGNER.aspx)

## 2. Design Flow

The ProChip Designer IDE is shown in [Figure 2-1](#). The *Design Flow* chart on the right provides push-button access to all stages of a design cycle. Feature descriptions for each design flow stage is described in [Table 2-1](#).

**Figure 2-1. ProChip Designer IDE**



**Table 2-1. Description of Design Flow Stages**

Design Flow Stage	Description
Source Manager	To specify new or existing design file(s). Launches the appropriate design entry tool for editing based on the selected tool-flow.
Logic Synthesis	To synthesize the design file(s) to generate the appropriate netlist file.
Device Fitter	To fit the netlist into the target device with the user selected fitter options.
Testbench Manager	To specify new or existing testbench file(s). Launches the appropriate testbench entry tool for editing based on the selected tool-flow.
Functional Simulation	To perform functional simulation on the design file(s).
Timing Simulation	To perform timing simulation on the output netlist generated by the fitter after fitting.
Atmel-ISP	To program the target ATF15xx CPLD through JTAG-ISP.

### 3. Support and Resources

For details on how to use ProChip Designer, refer to the ProChip Designer help file, tutorials, and online resources. These resources are available under the ProChip **Help** menu.

**Table 3-1. Technical Support**

Request Support By:	
Email	<a href="mailto:pld@atmel.com">pld@atmel.com</a>
Hotline	(+1) (408) 436-4333
Online Support Form	<a href="http://www.atmel.com/design-support/">www.atmel.com/design-support/</a>
Product Information	<a href="http://www.atmel.com/products/programmable-logic/spld-cpld/default.aspx">www.atmel.com/products/programmable-logic/spld-cpld/default.aspx</a>

### 4. Revision History

Rev. Ver.	Date	Description
3628D	05/2016	Update links and software version. ATF15xxBE is not recommended for new design.
3628C	09/2013	General update of the overview document. Update template.



Atmel®, Atmel logo and combinations thereof, Enabling Unlimited Possibilities®, ProChip Designer®, and others are registered trademarks or trademarks of Atmel Corporation in U.S. and other countries. Other terms and product names may be trademarks of others.

DISCLAIMER: The information in this document is provided in connection with Atmel products. No license, express or implied, by estoppel or otherwise, to any intellectual property right is granted by this document or in connection with the sale of Atmel products. EXCEPT AS SET FORTH IN THE ATMEL TERMS AND CONDITIONS OF SALES LOCATED ON THE ATMEL WEBSITE, ATMEL ASSUMES NO LIABILITY WHATSOEVER AND DISCLAIMS ANY EXPRESS, IMPLIED OR STATUTORY WARRANTY RELATING TO ITS PRODUCTS INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTY OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, OR NON-INFRINGEMENT. IN NO EVENT SHALL ATMEL BE LIABLE FOR ANY DIRECT, INDIRECT, CONSEQUENTIAL, PUNITIVE, SPECIAL OR INCIDENTAL DAMAGES (INCLUDING, WITHOUT LIMITATION, DAMAGES FOR LOSS AND PROFITS, BUSINESS INTERRUPTION, OR LOSS OF INFORMATION) ARISING OUT OF THE USE OR INABILITY TO USE THIS DOCUMENT, EVEN IF ATMEL HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES. Atmel makes no representations or warranties with respect to the accuracy or completeness of the contents of this document and reserves the right to make changes to specifications and products descriptions at any time without notice. Atmel does not make any commitment to update the information contained herein. Unless specifically provided otherwise, Atmel products are not suitable for, and shall not be used in, automotive applications. Atmel products are not intended, authorized, or warranted for use as components in applications intended to support or sustain life.

SAFETY-CRITICAL, MILITARY, AND AUTOMOTIVE APPLICATIONS DISCLAIMER: Atmel products are not designed for and will not be used in connection with any applications where the failure of such products would reasonably be expected to result in significant personal injury or death ("Safety-Critical Applications") without an Atmel officer's specific written consent. Safety-Critical Applications include, without limitation, life support devices and systems, equipment or systems for the operation of nuclear facilities and weapons systems. Atmel products are not designed nor intended for use in military or aerospace applications or environments unless specifically designated by Atmel as military-grade. Atmel products are not designed nor intended for use in automotive applications unless specifically designated by Atmel as automotive-grade.

## Данный компонент на территории Российской Федерации

### Вы можете приобрести в компании MosChip.

Для оперативного оформления запроса Вам необходимо перейти по данной ссылке:

<http://moschip.ru/get-element>

Вы можете разместить у нас заказ для любого Вашего проекта, будь то серийное производство или разработка единичного прибора.

В нашем ассортименте представлены ведущие мировые производители активных и пассивных электронных компонентов.

Нашей специализацией является поставка электронной компонентной базы двойного назначения, продукции таких производителей как XILINX, Intel (ex.ALTERA), Vicor, Microchip, Texas Instruments, Analog Devices, Mini-Circuits, Amphenol, Glenair.

Сотрудничество с глобальными дистрибьюторами электронных компонентов, предоставляет возможность заказывать и получать с международных складов практически любой перечень компонентов в оптимальные для Вас сроки.

На всех этапах разработки и производства наши партнеры могут получить квалифицированную поддержку опытных инженеров.

Система менеджмента качества компании отвечает требованиям в соответствии с ГОСТ Р ИСО 9001, ГОСТ РВ 0015-002 и ЭС РД 009

### Офис по работе с юридическими лицами:

105318, г.Москва, ул.Щербаковская д.3, офис 1107, 1118, ДЦ «Щербаковский»

Телефон: +7 495 668-12-70 (многоканальный)

Факс: +7 495 668-12-70 (доб.304)

E-mail: [info@moschip.ru](mailto:info@moschip.ru)

Skype отдела продаж:

moschip.ru

moschip.ru\_4

moschip.ru\_6

moschip.ru\_9