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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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# **HAT2160H**

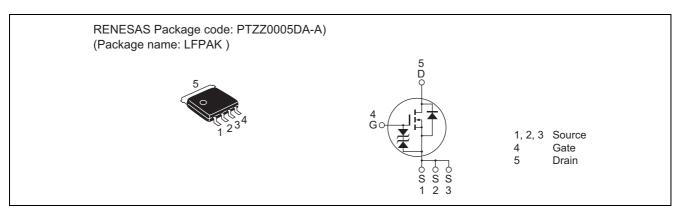
# Silicon N Channel Power MOS FET Power Switching

REJ03G0002-0300 Rev.3.00 Sep 26, 2005

#### **Features**

- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance  $R_{DS(on)} = 2.1 \ m\Omega \ typ. \ (at \ V_{GS} = 10 \ V)$

### **Outline**



## **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	20	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	60	A
Drain peak current	I <sub>D(pulse)</sub> Note1	240	A
Body-drain diode reverse drain current	I <sub>DR</sub>	60	A
Avalanche current	I <sub>AP</sub> Note 3	30	A
Avalanche energy	E <sub>AR</sub> Note 3	90	mJ
Channel dissipation	Pch Note2	30	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. Tc = 25°C

3. Value at Tch = 25°C, Rg  $\geq$  50  $\Omega$ 

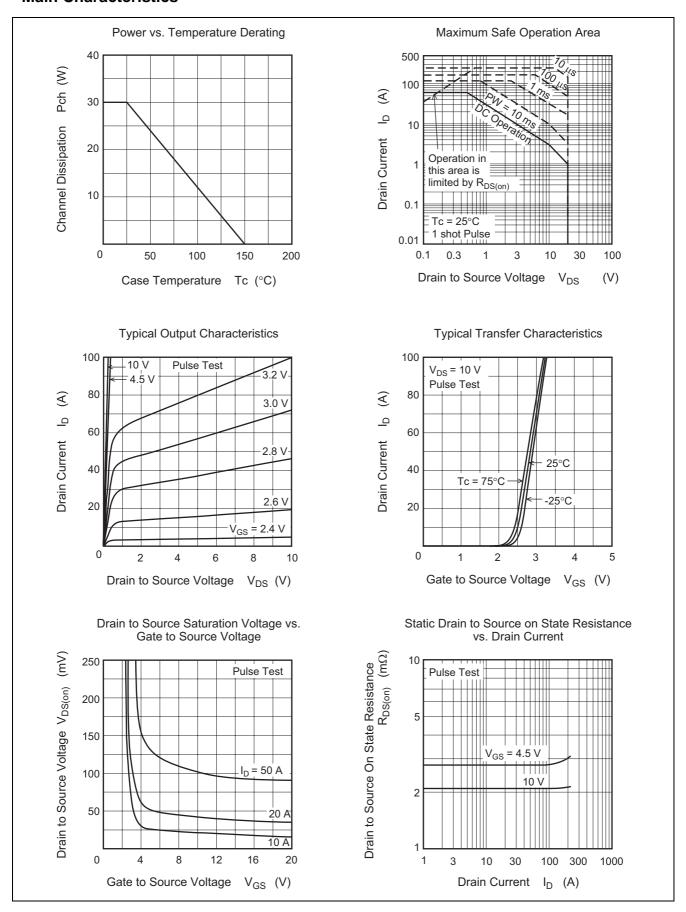
## **Electrical Characteristics**

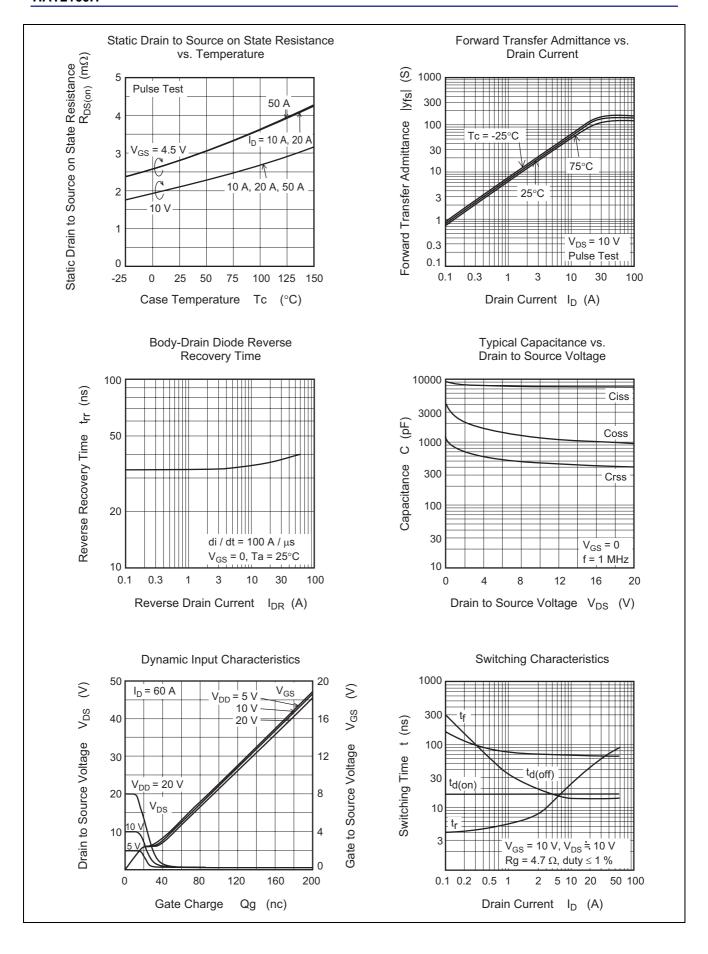
 $(Ta = 25^{\circ}C)$ 

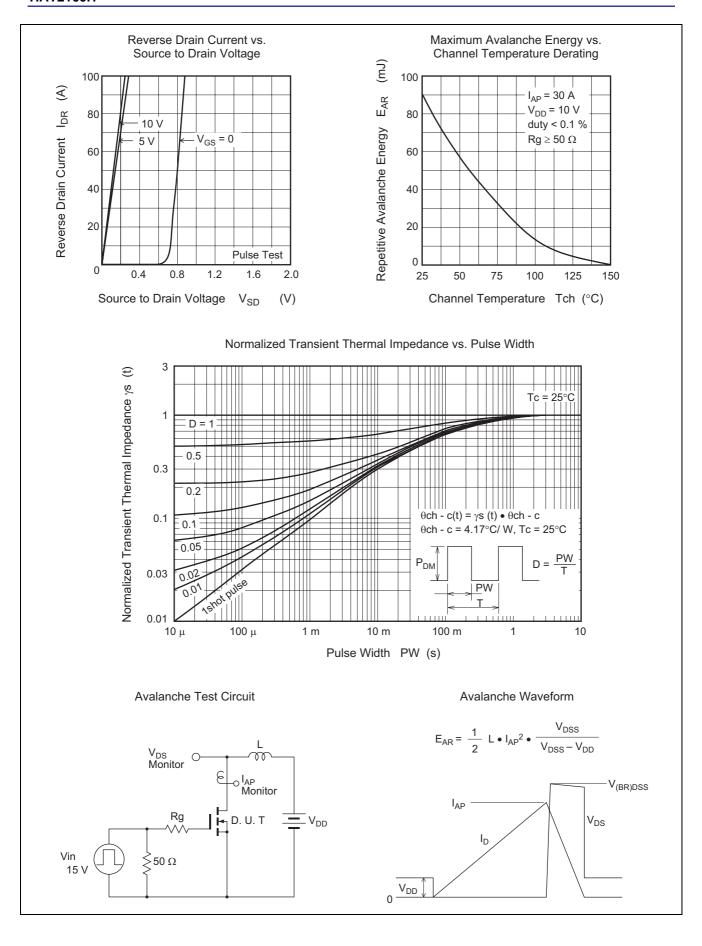
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	20	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	1	μΑ	$V_{DS} = 20 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	0.8	_	2.3	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$
Static drain to source on state	R <sub>DS(on)</sub>	_	2.1	2.6	mΩ	$I_D = 30 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R <sub>DS(on)</sub>	_	2.8	4.1	mΩ	$I_D = 30 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y <sub>fs</sub>	78	130		S	$I_D = 30 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	7750		pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$
Output capacitance	Coss	_	1220	_	pF	
Reverse transfer capacitance	Crss	_	450	_	pF	
Gate resistance	Rg	_	0.5	_	Ω	
Total gate charge	Qg	_	54	_	nC	$V_{DD} = 10 \text{ V}, V_{GS} = 4.5 \text{ V},$
Gate to source charge	Qgs	_	19	_	nC	$I_D = 60 \text{ A}$
Gate to drain charge	Qgd	_	14	_	nC	
Turn-on delay time	t <sub>d(on)</sub>	_	17	_	ns	$V_{GS} = 10 \text{ V}, I_D = 30 \text{ A},$
Rise time	t <sub>r</sub>	_	60	_	ns	$V_{DD} \cong 10 \text{ V}, \text{ R}_L = 0.33 \Omega,$
Turn-off delay time	t <sub>d(off)</sub>	_	65	_	ns	$Rg = 4.7 \Omega$
Fall time	t <sub>f</sub>	_	15	_	ns	
Body-drain diode forward voltage	$V_{DF}$	_	0.82	1.07	V	IF = 60 A, V <sub>GS</sub> = 0 <sup>Note4</sup>
Body-drain diode reverse recovery	t <sub>rr</sub>	_	40	_	ns	IF = 60 A, V <sub>GS</sub> = 0
time						$di_F/dt = 100 A/ \mu s$

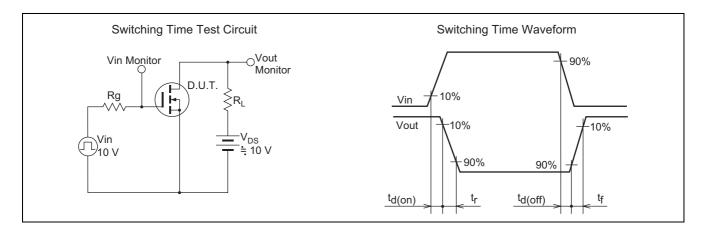
Notes: 4. Pulse test

#### **Main Characteristics**

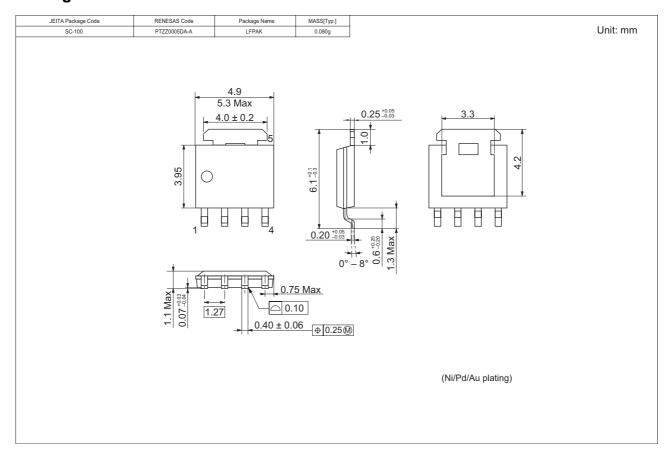








## **Package Dimensions**



## **Ordering Information**

Part Name	Quantity	Shipping Container
HAT2160H-EL-E	2500 pcs	Taping

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