

High Power GaAs DPDT Diversity Switch DC - 4.0 GHz

Rev. V1

Features

- Ideal for high power diversity switch applications including WiMax, WLAN MESH Networks, and Fixed Wireless Access
- Broadband Performance: DC 4.0 GHz
- Low Insertion Loss: 0.8 dB @ 2.5 GHz and

1.2 dB @ 3.5 GHz

- High P1dB Compression: 39.5 dBm @ 5 V
- · Fast Settling for Low Gate Lag Requirements
- Lead-Free 3 mm 12-Lead PQFN Package
- 100% Matte Tin Plating over Copper
- Halogen-Free "Green" Mold Compound
- RoHS* Compliant and 260°C Reflow Compatible

Description

M/A-COM's MASW-007587 is a broadband GaAs PHEMT MMIC diversity switch available in a lead-free 3 mm 12-lead PQFN package. The MASW-007587 is ideally suited for applications where very small size and high linear power are required.

Typical applications include 2.5 & 3.5 GHz WiMax, WLAN MESH networks, fixed wireless access, and other higher power systems. Designed for high power, this DPDT switch maintains high linearity up to 4.0 GHz.

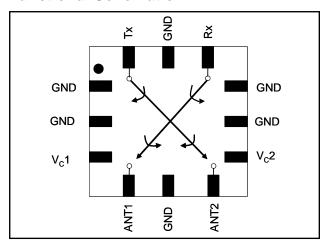
The MASW-007587 is fabricated using a 0.5 micron gate length GaAs PHEMT process. The process features full passivation for performance and reliability.

Ordering Information ¹

Part Number	Package
MASW-007587-TR3000	3000 piece reel
MASW-007587-000SMB	Sample Test Board (Includes 5 Samples)

1. Reference Application Note M513 for reel size information.

Functional Schematic



Pin Configuration

Pin No.	Pin Name	Description		
1	GND	Ground		
2	GND	Ground		
3	V _c 1	Control 1		
4	ANT1	Antenna Port 1		
5	GND	Ground		
6	ANT2	Antenna Port 2		
7	V _C 2	Control 2		
8	GND	Ground		
9	GND	Ground		
10	Rx	Receive Port		
11	GND	Ground		
12	Tx	Transmit Port		
13	Paddle ²	RF and DC Ground		

The exposed pad centered on the package bottom must be connected to RF and DC ground.

^{*} Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

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MASW-007587



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Electrical Specifications: $T_A = 25$ °C, $Z_0 = 50\Omega$, $V_C = 0 \text{ V} / 3 \text{ V}$, 39 pF Capacitor ³

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Insertion Loss ⁴	0.5 - 1 GHz 1 - 2 GHz 2 - 3 GHz 2.45 GHz 3 - 4 GHz	dB dB dB dB dB	11111	0.6 0.7 0.8 0.8 1.2	 1.2
Isolation (on/off or off/on) Iso @ Tx when IL from Ant 2 to Rx Iso @ Rx when IL from Ant 1 to Tx	0.5 - 1 GHz 1 - 2 GHz 2 - 3 GHz 2.45 GHz 3 - 4 GHz		 24 	41.5 35 30 30 27	_ _ _ _
Isolation (on/off or off/on) Iso @ Tx when IL from Ant 1 to Rx Iso @ Rx when IL from Ant 2 to Tx	0.5 - 1 GHz 1 - 2 GHz 2 - 3 GHz 2.45 GHz 3 - 4 GHz	dB dB dB dB dB	 29 	46.5 43 38 38 32	_ _ _ _
Return Loss	0.5 - 1 GHz 1 - 2 GHz 2 - 3 GHz 3 - 4 GHz	dB dB dB dB		14 15 19.5 14	_ _ _ _
IP3	Two Tone, +15 dBm/Tone, 5 MHz Spacing, 2.4 GHz $V_C=3\ V$ $V_C=5\ V$ $V_C=8\ V$			57.5 59 60	_ _ _
Input P1dB	2.4 GHz, $V_C = 3 \text{ V}$ 2.4 GHz, $V_C = 5 \text{ V}$ 2.4 GHz, $V_C = 8 \text{ V}$	dBm dBm dBm	_ _ _	34 39.5 41	
2 nd Harmonic	2.4 GHz, Pin = 15 dBm	dBc	_	-86	_
3 rd Harmonic	2.4 GHz, Pin = 15 dBm	dBc		-91	_
Trise, Tfall	10% to 90% RF 90% to 10% RF		_	64 80	_
Ton, Toff	50% control to 90% RF and 50% control to 10% RF			90	
Transients		mV		5	_
Control Current	<u>-</u>	μΑ	_	5	10

^{3.} For positive voltage control, external DC blocking capacitors are required on all RF ports.

^{4.} Insertion loss can be optimized by varying the DC blocking capacitor value. For use above 2.5 GHz, M/A-COM recommends using smaller capacitor values. For example, use 5 pF for 3.2 GHz.

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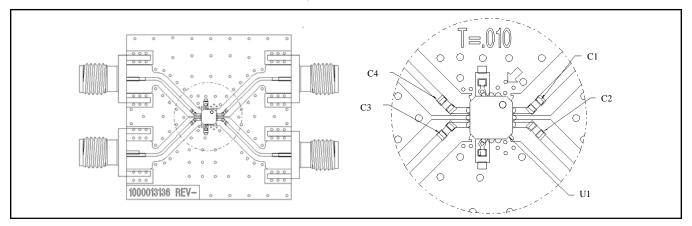
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Evaluation Board for 3 mm 12-Lead PQFN

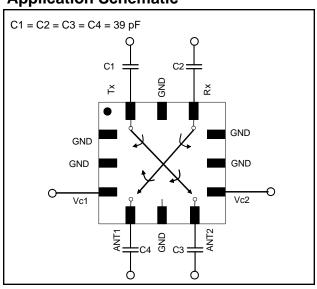


Absolute Maximum Ratings 5,6

Parameter	Absolute Maximum		
Input Power @ 3 V Control	+35 dBm CW		
Input Power @ 5 V Control	+37 dBm CW		
Voltage	≤ 8 volts		
Operating Temperature	-40°C to +85°C		
Storage Temperature	-65°C to +150°C		

- 5. Exceeding any one or combination of these limits may cause permanent damage to this device.
- M/A-COM does not recommend sustained operation near these survivability limits.

Application Schematic



Truth Table 7,8

Control V _c 1	Control V _C 2	ANT 1 - Rx	ANT 1 - Tx	ANT 2 - Tx	ANT 2 - Rx
1	0	On	Off	On	Off
0	1	Off	On	Off	On

- 7. Differential voltage, V (state 1) V (state 0), must be +2.7 V minimum and must not exceed 8.0 V.
- 8. 1 = +2.9 V to +8 V, 0 = 0 V + 0.2 V.

Qualification

Qualified to M/A-COM specification REL-201, Process Flow -2.

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

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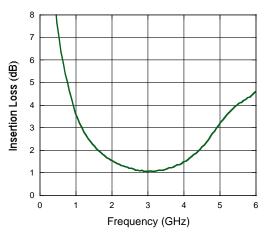


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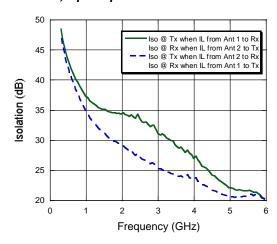
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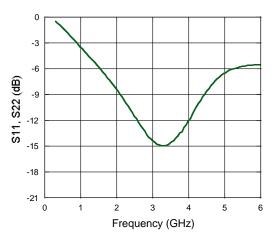
Typical Performance Curves

Insertion Loss, 4 pF Capacitors

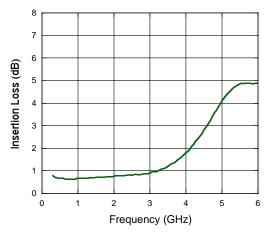


Isolation, 4 pF Capacitors

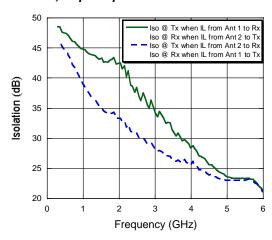




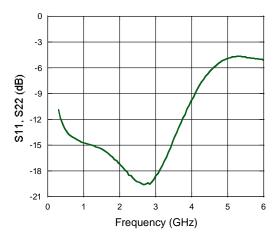
Insertion Loss, 39 pF Capacitors



Isolation, 39 pF Capacitors







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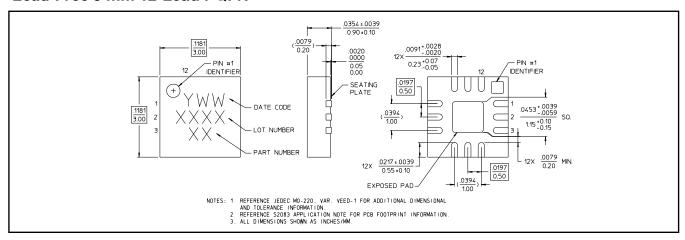
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Lead-Free 3 mm 12-Lead PQFN[†]



 $^{^\}dagger$ Reference Application Note M538 for lead-free solder reflow recommendations.

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ПОСТАВКА ЭЛЕКТРОННЫХ КОМПОНЕНТОВ

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