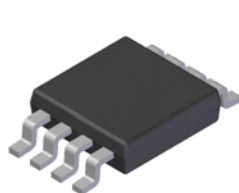


Features

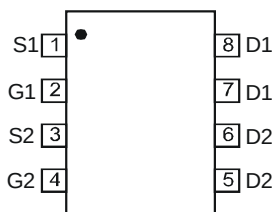
- Dual P-Channel MOSFET
- Low On-Resistance
 - 65mΩ @ $V_{GS} = -10V$
 - 115mΩ @ $V_{GS} = -4.5V$
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Lead Free By Design/RoHS Compliant (Note 2)**
- **"Green" Device (Note 4)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

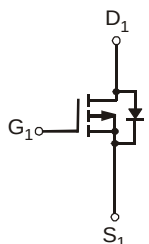
- Case: SOP-8L
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals Connections: See Diagram
- Terminals: Finish - Matte Tin annealed over Copper lead frame. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.072g (approximate)



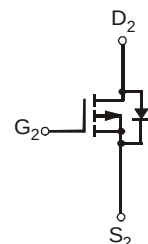
TOP VIEW


 TOP VIEW
Internal Schematic

SOP-8L



P-Channel MOSFET



P-Channel MOSFET

Maximum Ratings @ $T_A = 25^\circ C$ unless otherwise specified

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V_{DSS}	-30	V
Gate-Source Voltage			V_{GSS}	± 20	V
Drain Current (Note 1)	Steady State	$T_A = 25^{\circ}C$	I_D	-4.4	A
		$T_A = 70^{\circ}C$		-3.3	
Pulsed Drain Current (Note 3)			I_{PM}	-15	A

Thermal Characteristics

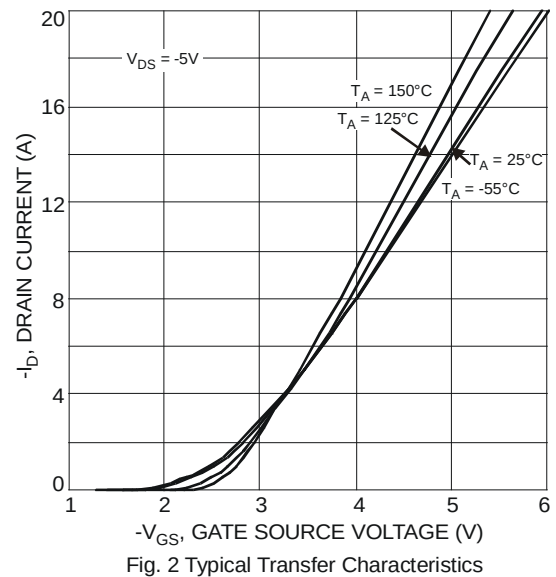
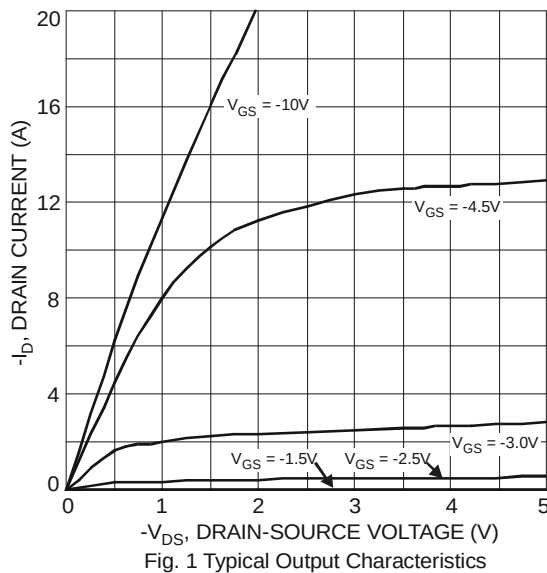
Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 1)	P_D	1.8	W
Thermal Resistance, Junction to Ambient (Note 1)	$R_{\theta JA}$	70	$^\circ C/W$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ C$

- Notes:
1. Device mounted on 2 oz. 1" x 1" Copper pads on 2" x 2" FR-4 PCB.
 2. No purposefully added lead.
 3. Pulse width $\leq 10\mu s$, Duty Cycle $\leq 1\%$.
 4. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 5)						
Drain-Source Breakdown Voltage	BV _{DSS}	-30	—	—	V	V _{GS} = 0V, I _D = -250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	-1	μA	V _{DS} = -30V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 5)						
Gate Threshold Voltage	V _{GS(th)}	-1	1.7	-2.1	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	56 98	65 115	mΩ	V _{GS} = -10V, I _D = -5.0A V _{GS} = -4.5V, I _D = -4.0A
Forward Transconductance	g _{fs}	—	5.2	—	S	V _{DS} = -10V, I _D = -5.0A
Diode Forward Voltage (Note 5)	V _{SD}	-0.5	—	-1.2	V	V _{GS} = 0V, I _S = -2.6A
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{iss}	—	336	—	pF	V _{DS} = -25V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	—	70	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	49	—	pF	
Gate Resistance	R _G	—	4.6	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1.0MHz
SWITCHING CHARACTERISTICS						
Total Gate Charge	Q _g	—	4.0 7.8	—	nC	V _{DS} = -15V, V _{GS} = -4.5V, I _D = -5.0A V _{DS} = -15V, V _{GS} = -10V, I _D = -5.0A V _{DS} = -15V, V _{GS} = -4.5V, I _D = -5.0A
Gate-Source Charge	Q _{gs}	—	1.0	—		V _{DS} = -15V, V _{GS} = -4.5V, I _D = -5.0A
Gate-Drain Charge	Q _{gd}	—	2.5	—		V _{DS} = -15V, V _{GS} = -4.5V, I _D = -5.0A
Turn-On Delay Time	t _{d(on)}	—	6.0	—	ns	V _{DS} = -15V, V _{GS} = -10V, I _D = -1A, R _G = 6.0Ω
Rise Time	t _r	—	5.0	—		
Turn-Off Delay Time	t _{d(off)}	—	17.6	—		
Fall Time	t _f	—	9.5	—		

Notes: 5. Short duration pulse test used to minimize self-heating effect.



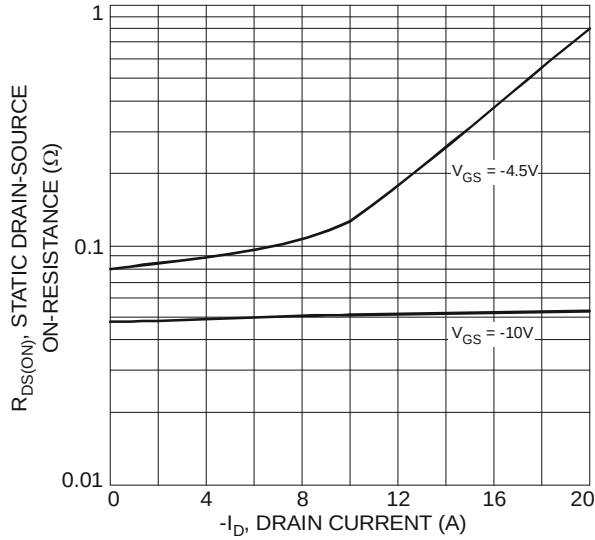


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

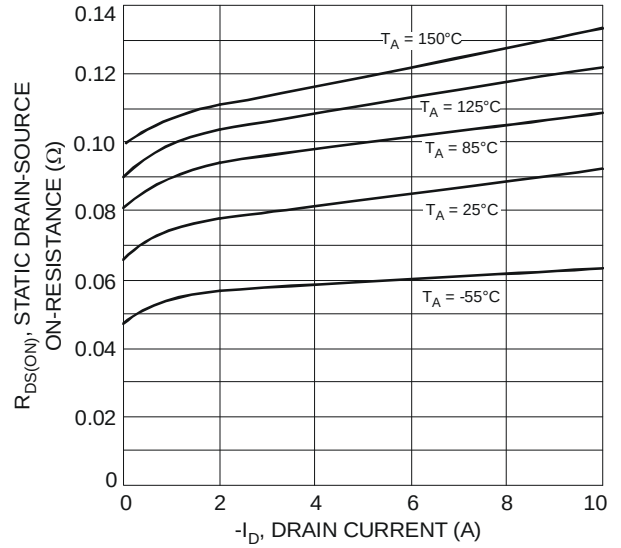


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

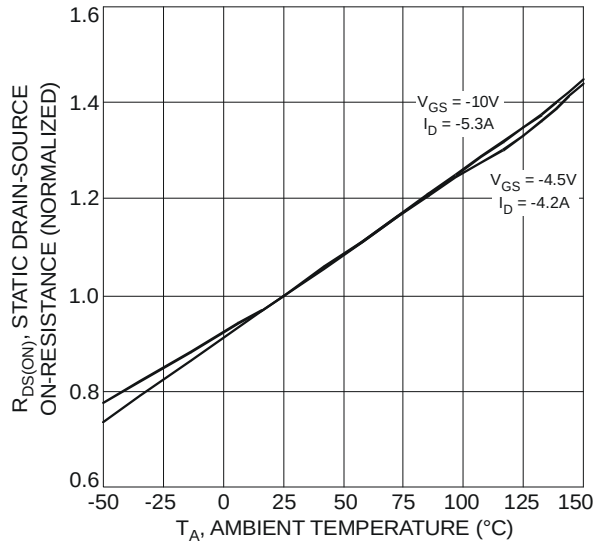


Fig. 5 On-Resistance Variation with Temperature

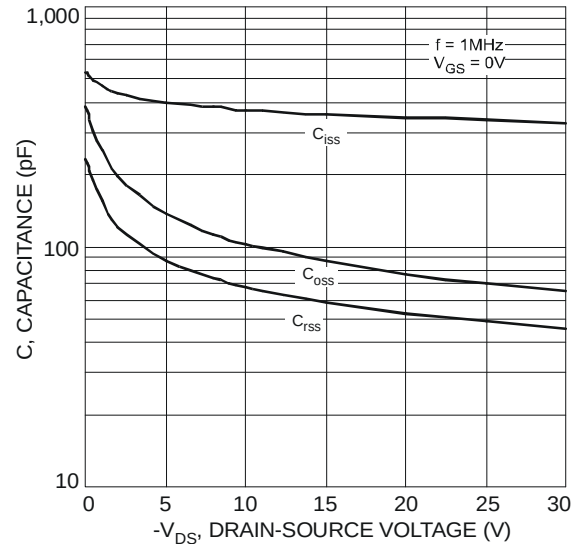


Fig. 6 Typical Capacitance

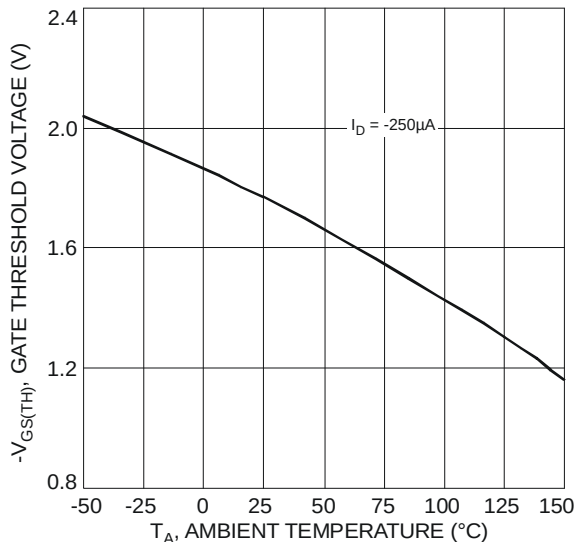


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

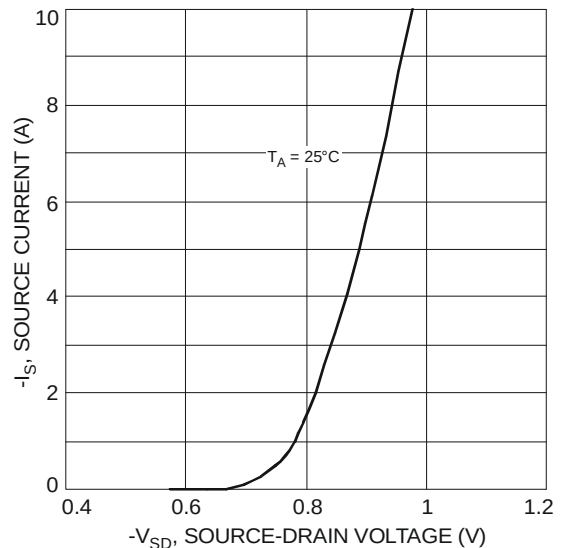


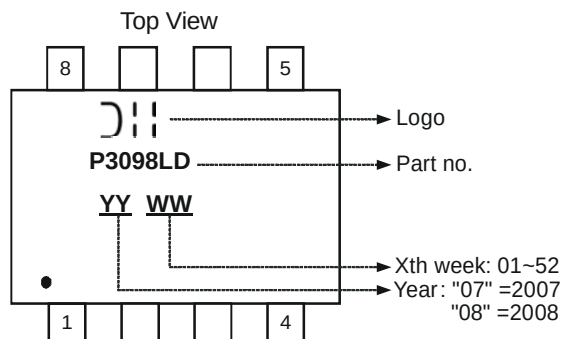
Fig. 8 Diode Forward Voltage vs. Current

Ordering Information (Note 6)

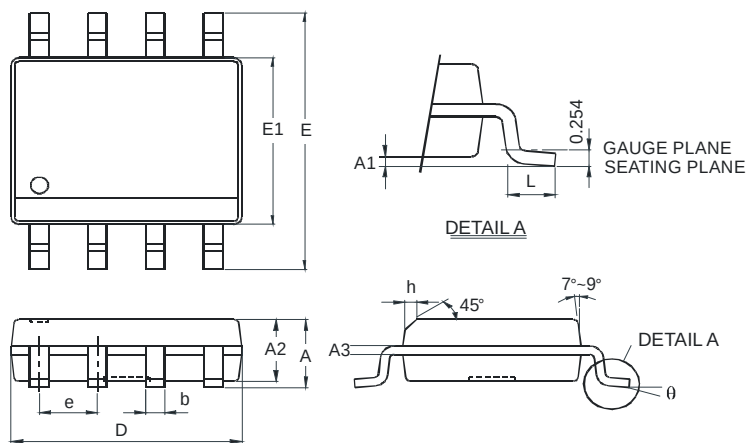
Part Number	Case	Packaging
DMP3098LSD-13	SOP-8L	2500/Tape & Reel

Notes: 6. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

Marking Information

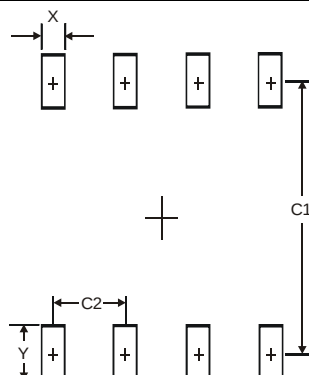


Package Outline Dimensions



SOP-8L		
Dim	Min	Max
A	-	1.75
A1	0.08	0.25
A2	1.30	1.50
A3	0.20 Typ.	
b	0.3	0.5
D	4.80	5.30
E	5.79	6.20
E1	3.70	4.10
e	1.27 Typ.	
h	-	0.35
L	0.38	1.27
θ	0°	8°
All Dimensions in mm		

Suggested Pad Layout



Dimensions	Value (in mm)
X	0.60
Y	1.55
C1	5.4
C2	1.27

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<http://moschip.ru/get-element>

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