Product data sheet

1. General description

Planar passivated very sensitive gate four quadrant triac in a SOT54 plastic package intended for interfacing with low power drivers including microcontrollers.

2. Features and benefits

- Direct interfacing to logic level ICs
- · Direct interfacing with low power gate drivers and microcontrollers
- · High blocking voltage capability
- · Planar passivated for voltage ruggedness and reliability
- Triggering in all four quadrants
- · Very sensitive gate

3. Applications

- Air conditioner indoor fan
- General purpose low power motor control
- · General purpose switching and phase control

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{DRM}	repetitive peak off- state voltage		-	-	800	V
I _{T(RMS)}	RMS on-state current	full sine wave; T _{lead} ≤ 51 °C; <u>Fig. 1</u> ; <u>Fig. 2</u> ; <u>Fig. 3</u>	-	-	1	Α
I _{TSM}	non-repetitive peak on- state current	full sine wave; $T_{j(init)}$ = 25 °C; t_p = 20 ms; Fig. 4; Fig. 5	-	-	12.5	Α
		full sine wave; $T_{j(init)} = 25 \text{ °C}$; $t_p = 16.7 \text{ ms}$	-	-	13.7	Α
T _j	junction temperature		-	-	125	°C
Static chara	acteristics			•		
I _{GT}	gate trigger current	$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; } T2+ G+;$ $T_j = 25 \text{ °C; } Fig. 7$	-	0.4	3	mA
		$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; } T2 + G-;$ $T_j = 25 \text{ °C; } Fig. 7$	-	1.3	3	mA
		$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; } T2-\text{ G-;}$ $T_j = 25 \text{ °C; } \underline{Fig. 7}$	-	1.4	3	mA

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
		$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; T2- G+;}$ $T_j = 25 \text{ °C; } \frac{\text{Fig. 7}}{}$	-	3.8	7	mA
I _H	holding current	V _D = 12 V; T _j = 25 °C; <u>Fig. 9</u>	-	1.3	5	mA
V _T	on-state voltage	I _T = 1.4 A; T _j = 25 °C; <u>Fig. 10</u>	-	1.2	1.5	V
Dynamic cha	racteristics					
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 536 V; T_j = 125 °C; $(V_{DM}$ = 67% of V_{DRM}); exponential waveform; $R_{GT1(ext)}$ = 1 k Ω ; Fig. 12	10	20	-	V/µs
dV _{com} /dt	rate of change of commutating voltage	$V_D = 400 \text{ V}; T_j = 125 \text{ °C}; \text{ dI}_{com}/$ dt = 0.5 A/ms; I _T = 1 A	2	-	-	V/µs

5. Ordering information

Table 2. Ordering information

Type number	Package					
	Name	Description	Version			
BT131-800	TO-92	plastic single-ended leaded (through hole) package; 3 leads	SOT54			

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6. Limiting values

Table 3. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DRM}	repetitive peak off-state voltage		-	800	V
I _{T(RMS)}	RMS on-state current	full sine wave; T _{lead} ≤ 51 °C; <u>Fig. 1</u> ; <u>Fig. 2</u> ; <u>Fig. 3</u>	-	1	Α
I _{TSM}	non-repetitive peak on- state current	full sine wave; $T_{j(init)}$ = 25 °C; t_p = 20 ms; Fig. 4; Fig. 5	-	12.5	Α
		full sine wave; T _{j(init)} = 25 °C; t _p = 16.7 ms	-	13.7	Α
l ² t	I ² t for fusing	t _p = 10 ms; sine-wave pulse	-	0.78	A²s
dl _T /dt	rate of rise of on-state current	I _G = 6 mA	-	50	A/µs
			-	50	A/µs
		I _G = 14 mA	-	10	A/µs
		I _G = 6 mA	-	50	A/µs
I _{GM}	peak gate current		-	2	Α
P_{GM}	peak gate power		-	5	W
P _{G(AV)}	average gate power	over any 20 ms period	-	0.1	W
T _{stg}	storage temperature		-40	150	°C
Tj	junction temperature		-	125	°C

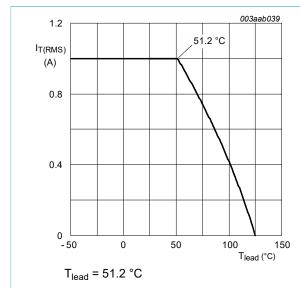


Fig. 1. RMS on-state current as a function of lead temperature; maximum values

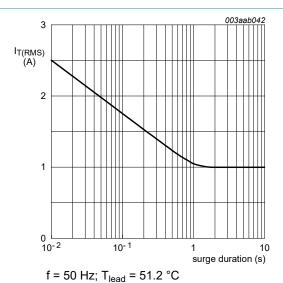


Fig. 2. RMS on-state current as a function of surge duration; maximum values

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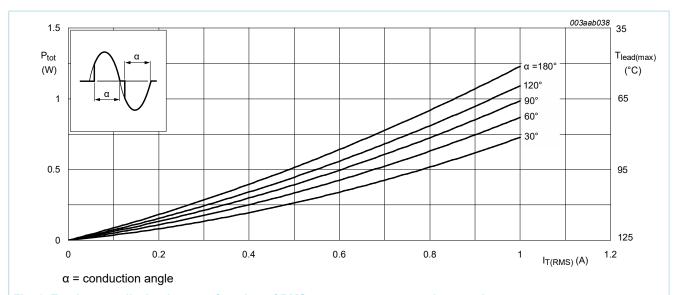


Fig. 3. Total power dissipation as a function of RMS on-state current; maximum values

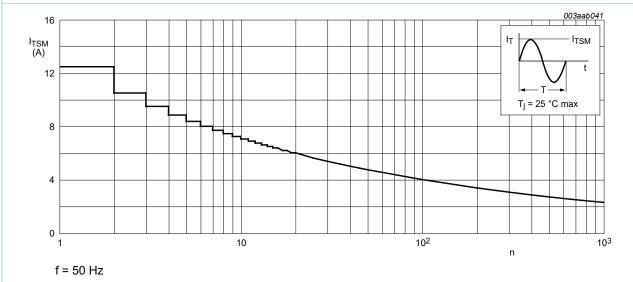
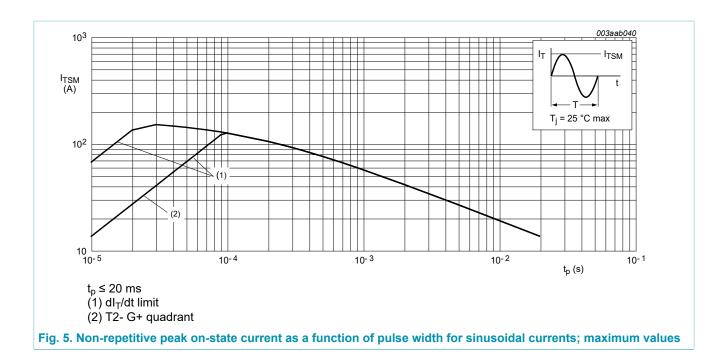


Fig. 4. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum

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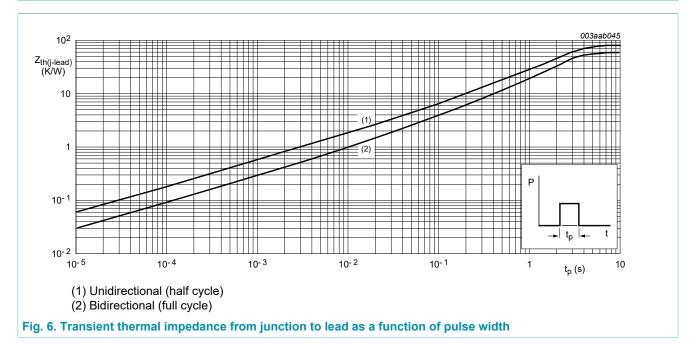


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7. Thermal characteristics

Table 4. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-lead)}	thermal resistance	full cycle; Fig. 6	-	-	60	K/W
	from junction to lead	half cycle; Fig. 6	-	-	80	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient free air	printed circuit board mounted: lead length = 4 mm	-	150	-	K/W



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8. Characteristics

Table 5. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics		,			
I _{GT}	gate trigger current	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T2+ G+;$ $T_j = 25 \text{ °C}; Fig. 7$	-	0.4	3	mA
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T2+ \text{ G-};$ $T_j = 25 \text{ °C}; \frac{\text{Fig. 7}}{}$	-	1.3	3	mA
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; \text{ T2- G-};$ $T_j = 25 \text{ °C}; \frac{\text{Fig. 7}}{}$	-	1.4	3	mA
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; \text{ T2- G+};$ $T_j = 25 \text{ °C}; \frac{\text{Fig. 7}}{}$	-	3.8	7	mA
IL	latching current	$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; T2+ G+;$ $T_j = 25 \text{ °C}; Fig. 8$	-	1.2	5	mA
		$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; T2+ G-;$ $T_j = 25 \text{ °C}; \frac{\text{Fig. 8}}{2}$	-	4	8	mA
		$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; \text{ T2- G-};$ $T_j = 25 \text{ °C}; \frac{\text{Fig. 8}}{\text{C}}$	-	1	5	mA
		$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; \text{ T2- G+};$ $T_j = 25 \text{ °C}; \frac{\text{Fig. 8}}{}$	-	2.5	8	mA
I _H	holding current	V _D = 12 V; T _j = 25 °C; <u>Fig. 9</u>	-	1.3	5	mA
V_{T}	on-state voltage	I _T = 1.4 A; T _j = 25 °C; <u>Fig. 10</u>	-	1.2	1.5	V
V_{GT}	gate trigger voltage	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T_j = 25 \text{ °C};$ Fig. 11	-	0.7	1	V
		$V_D = 400 \text{ V}; I_T = 0.1 \text{ A}; T_j = 125 \text{ °C};$ Fig. 11	0.2	0.3	-	V
I _D	off-state current	V _D = 800 V; T _j = 125 °C	-	0.1	0.5	mA
Dynamic ch	naracteristics					
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 536 V; T_j = 125 °C; (V_{DM} = 67% of V_{DRM}); exponential waveform; $R_{GT1(ext)}$ = 1 k Ω ; Fig. 12	10	20	-	V/µs
dV _{com} /dt	rate of change of commutating voltage	$V_D = 400 \text{ V}; T_j = 125 \text{ °C}; \text{ dI}_{com}/$ dt = 0.5 A/ms; I _T = 1 A	2	-	-	V/µs
t _{gt}	gate-controlled turn-on time	I_{TM} = 1.5 A; V_D = 800 V; I_G = 0.1 A; dI_G/dt = 5 A/ μ s	-	2	-	μs

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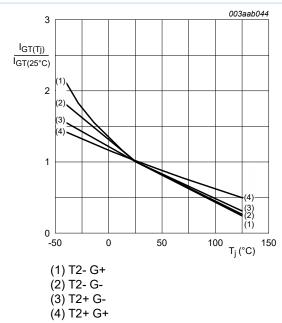


Fig. 8. Normalized latching current as a function of junction temperature

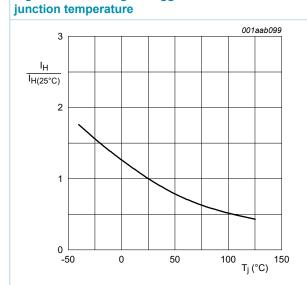
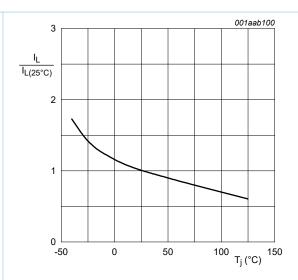


Fig. 7. Normalized gate trigger current as a function of

Fig. 9. Normalized holding current as a function of junction temperature



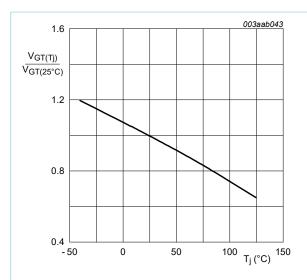
2 I_T (A) 1.6 1.2 8.0 (2) 0.4 0 0.4 8.0 $^{1.6}$ $_{V_{T}(V)}$ 2

 V_o = 0.92 V; R_s = 0.4 Ω (1) T_j = 125 °C; typical values (2) T_j = 125 °C; maximum values

(3) T_i = 25 °C; maximum values

Fig. 10. On-state current as a function of on-state voltage

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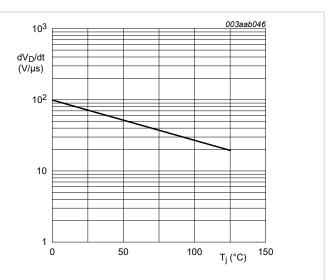
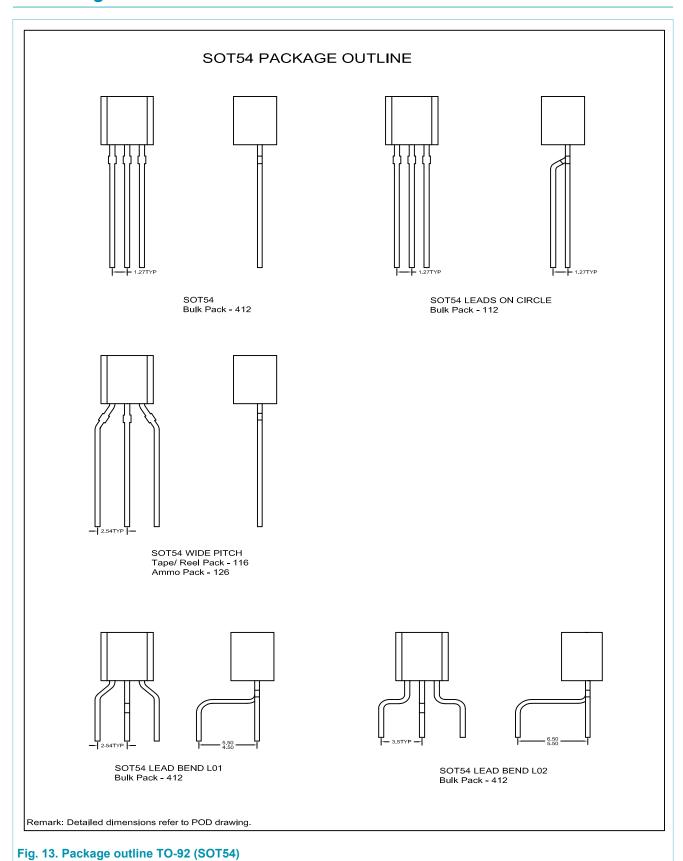


Fig. 12. Rate of rise of off-state voltage as a function of junction temperature; typical values

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9. Package outline



BT131-80

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10. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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