

PNP PRE-BIASED SMALL SIGNAL SOT-26 DUAL SURFACE MOUNT TRANSISTOR

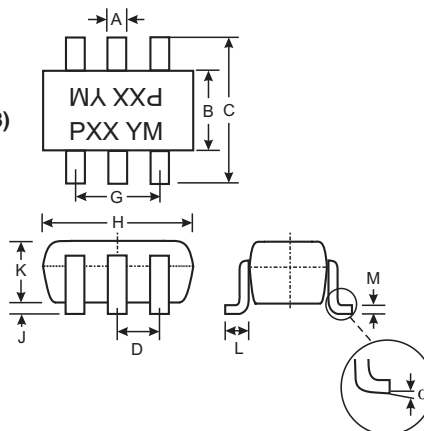
Features

- Epitaxial Planar Die Construction
- Complementary NPN Types Available (DDC)
- Built-In Biasing Resistors
- Available in Lead Free/RoHS Compliant Version (Note 3)

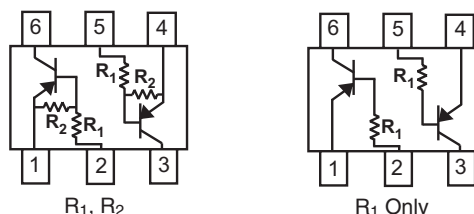
Mechanical Data

- Case: SOT-26
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminal Connections: See Diagram
- Terminals: Solderable per MIL-STD-202, Method 208
- Also Available in Lead Free Plating (Matte Tin Finish annealed over Copper leadframe). Please see Ordering Information, Note 5, on Page 2
- Marking: Date Code and Marking Code (See Diagrams & Page 2)
- Ordering Information (See Page 2)
- Weight: 0.015 grams (approximate)

P/N	R1	R2	MARKING
DDA124EK	22K Ω	22K Ω	P17
DDA144EK	47K Ω	47K Ω	P20
DDA114YK	10K Ω	47K Ω	P14
DDA123JK	2.2K Ω	47K Ω	P06
DDA114EK	10K Ω	10K Ω	P13
DDA143TK	4.7K Ω	-	P07
DDA114TK	10K Ω	-	P12



SOT-26			
Dim	Min	Max	Typ
A	0.35	0.50	0.38
B	1.50	1.70	1.60
C	2.70	3.00	2.80
D	0.95		
G	1.90		
H	2.90	3.10	3.00
J	0.013	0.10	0.05
K	1.00	1.30	1.10
L	0.35	0.55	0.40
M	0.10	0.20	0.15
α	0°	8°	—
All Dimensions in mm			



SCHEMATIC DIAGRAM

Maximum Ratings @ T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Supply Voltage, (1) to (6) and (4) to (3)	V _{CC}	50	V
Input Voltage, (2) to (1) and (5) to (4)	V _{IN}	+10 to -40 +10 to -40 +6 to -40 +5 to -12 +10 to -40 +5 V _{max} +5 V _{max}	V
Output Current	I _O	-30 -30 -70 -100 -50 -100 -100	mA
Output Current All	I _C (Max)	-100	mA
Power Dissipation (Total)	P _d	300	mW
Thermal Resistance, Junction to Ambient Air (Note 1)	R _{θJA}	416.7	°C/W
Operating and Storage and Temperature Range	T _j , T _{STG}	-55 to +150	°C

- Note: 1. Mounted on FR4 PC Board with recommended pad layout at <http://www.diodes.com/datasheets/ap02001.pdf>.
2. 200mW per element must not be exceeded.
3. No purposefully added lead.

Electrical Characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic (DDA143TK & DDA114TK only)	Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV_{CBO}	-50	—	—	V	$I_C = -50\mu\text{A}$
Collector-Emitter Breakdown Voltage	BV_{CEO}	-50	—	—	V	$I_C = -1\text{mA}$
Emitter-Base Breakdown Voltage	BV_{EBO}	-5	—	—	V	$I_E = -50\mu\text{A}$
Collector Cutoff Current	I_{CBO}	—	—	-0.5	μA	$V_{CB} = -50\text{V}$
Emitter Cutoff Current	I_{EBO}	—	—	-0.5	μA	$V_{EB} = -4\text{V}$
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	—	—	-0.3	V	$I_C/I_B = -2.5\text{mA} / -0.25\text{mA}$ DDA143TK $I_C/I_B = -1\text{mA} / -0.1\text{mA}$ DDA114TK
DC Current Transfer Ratio	h_{FE}	100	250	600	—	$I_C = -1\text{mA}$, $V_{CE} = -5\text{V}$
Input Resistor (R_1) Tolerance	ΔR_1	-30	—	+30	%	—
Gain-Bandwidth Product*	f_T	—	250	—	MHz	$V_{CE} = -10\text{V}$, $I_E = 5\text{mA}$, $f = 100\text{MHz}$

Characteristic		Symbol	Min	Typ	Max	Unit	Test Condition
Input Voltage	DDA124EK DDA144EK DDA114YK DDA123JK DDA114EK	$V_{I(off)}$	-0.5 -0.5 -0.3 -0.5 -0.5	-1.1 -1.1 — — -1.1	—	V	$V_{CC} = -5\text{V}$, $I_O = -100\mu\text{A}$
	DDA124EK DDA144EK DDA114YK DDA123JK DDA114EK	$V_{I(on)}$	—	-1.9 -1.9 — — -1.9	-3.0 -3.0 -1.4 -1.1 -3.0		$V_O = -0.3$, $I_O = -5\text{mA}$ $V_O = -0.3$, $I_O = -2\text{mA}$ $V_O = -0.3$, $I_O = -1\text{mA}$ $V_O = -0.3$, $I_O = -5\text{mA}$ $V_O = -0.3$, $I_O = -10\text{mA}$
Output Voltage	DDA124EK DDA144EK DDA114YK DDA123JK DDA114EK	$V_{O(on)}$	—	-0.1	-0.3	V	$I_O/I_I = -10\text{mA} / -0.5\text{mA}$ $I_O/I_I = -10\text{mA} / -0.5\text{mA}$ $I_O/I_I = -5\text{mA} / -0.25\text{mA}$ $I_O/I_I = -5\text{mA} / -0.25\text{mA}$ $I_O/I_I = -10\text{mA} / -0.5\text{mA}$
Input Current	DDA124EK DDA144EK DDA114YK DDA123JK DDA114EK	I_I	—	—	-0.36 -0.18 -0.88 -3.6 -0.88	mA	$V_I = -5\text{V}$
Output Current		$I_{O(off)}$	—	—	-0.5	μA	$V_{CC} = 50\text{V}$, $V_I = 0\text{V}$
DC Current Gain	DDA124EK DDA144EK DDA114YK DDA123JK DDA114EK	G_I	56 68 68 80 30	—	—	—	$V_O = -5\text{V}$, $I_O = -5\text{mA}$ $V_O = -5\text{V}$, $I_O = -5\text{mA}$ $V_O = -5\text{V}$, $I_O = -10\text{mA}$ $V_O = -5\text{V}$, $I_O = -10\text{mA}$ $V_O = -5\text{V}$, $I_O = -5\text{mA}$
Input Resistor (R_1) Tolerance		ΔR_1	-30	—	+30	%	—
Resistance Ratio Tolerance		R_2/R_1	-20	—	+20	%	—
Gain-Bandwidth Product*		f_T	—	250	—	MHz	$V_{CE} = -10\text{V}$, $I_E = -5\text{mA}$, $f = 100\text{MHz}$

* Transistor - For Reference Only

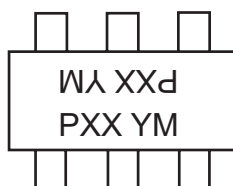
Ordering Information (Note 4)

Device	Packaging	Shipping
DDA124EK-7	SOT-26	3000/Tape & Reel
DDA144EK-7	SOT-26	3000/Tape & Reel
DDA114YK-7	SOT-26	3000/Tape & Reel
DDA123JK-7	SOT-26	3000/Tape & Reel
DDA114EK-7	SOT-26	3000/Tape & Reel
DDA143TK-7	SOT-26	3000/Tape & Reel
DDA114TK-7	SOT-26	3000/Tape & Reel

 Notes: 4. For Packaging Details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

5. For Lead Free/RoHS Compliant version part numbers, please add "-F" suffix to the part numbers above. Example: DDA114TK-7-F.

Marking Information



PXX = Product Type Marking Code
 See Sheet 1 Diagrams
 YM = Date Code Marking
 Y = Year ex: T = 2006
 M = Month ex: 9 = September

Date Code Key

Year	2006	2007	2008	2009	2010	2011	2012
Code	T	U	V	W	X	Y	Z

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

TYPICAL CURVES - DDA123JK

ONE SECTION

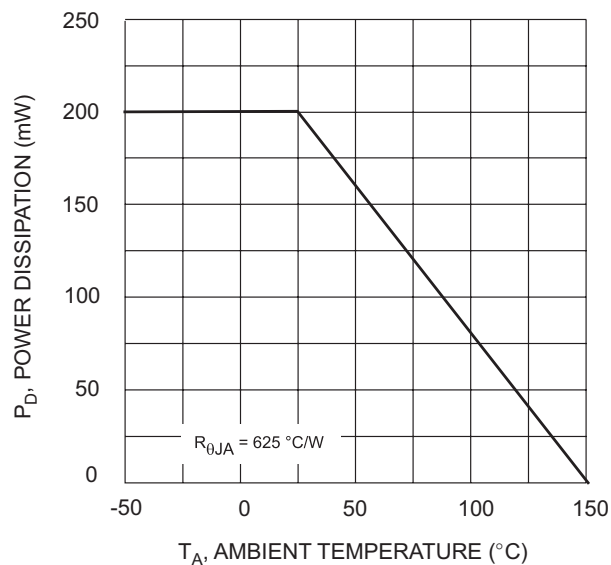


Fig. 1 Derating Curve

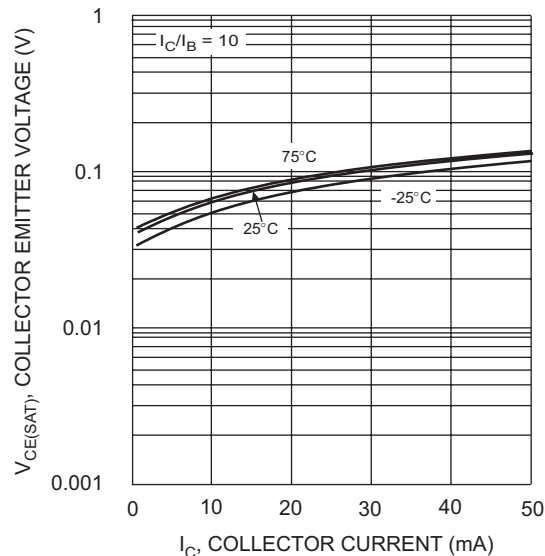


Fig. 2 $V_{CE(SAT)}$ vs. I_C

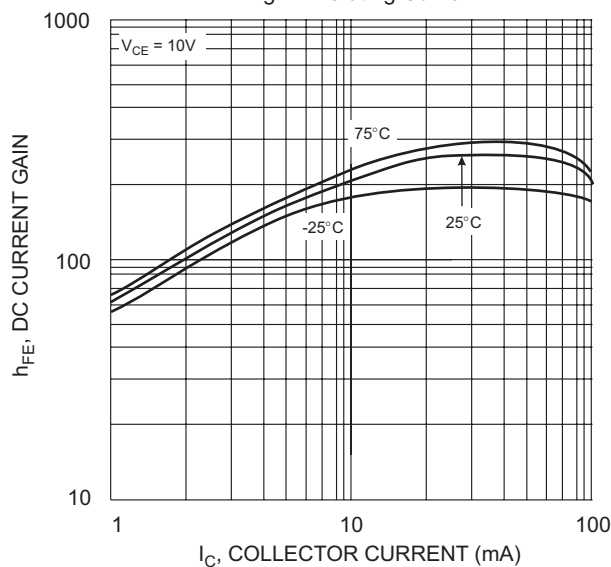


Fig. 3 DC Current Gain

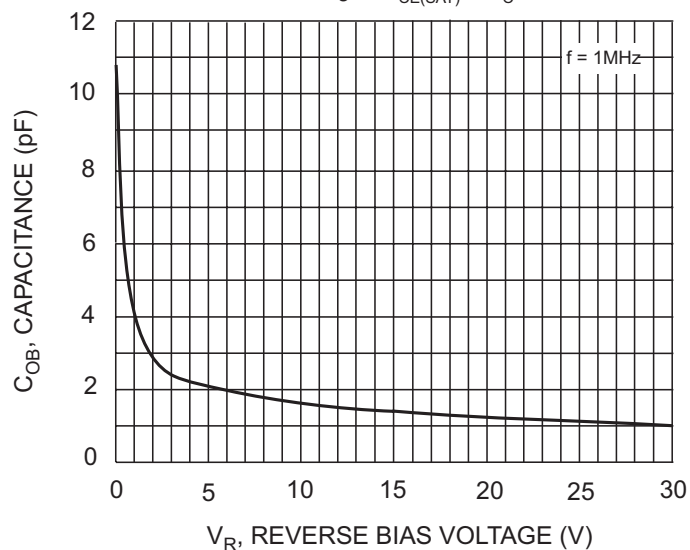


Fig. 4 Output Capacitance

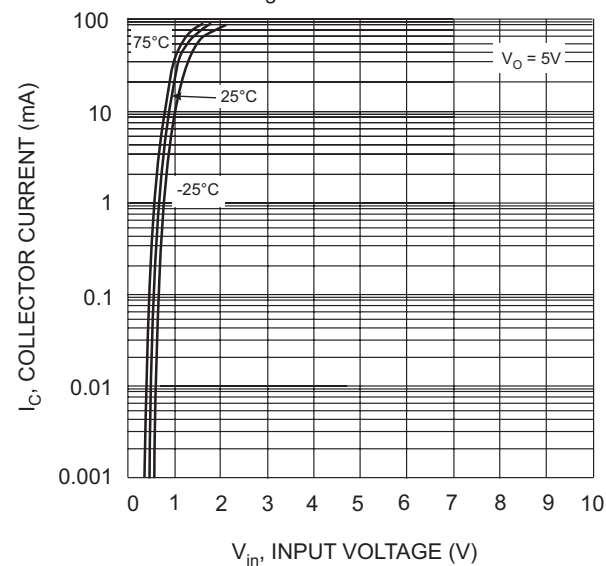


Fig. 5 Collector Current Vs. Input Voltage

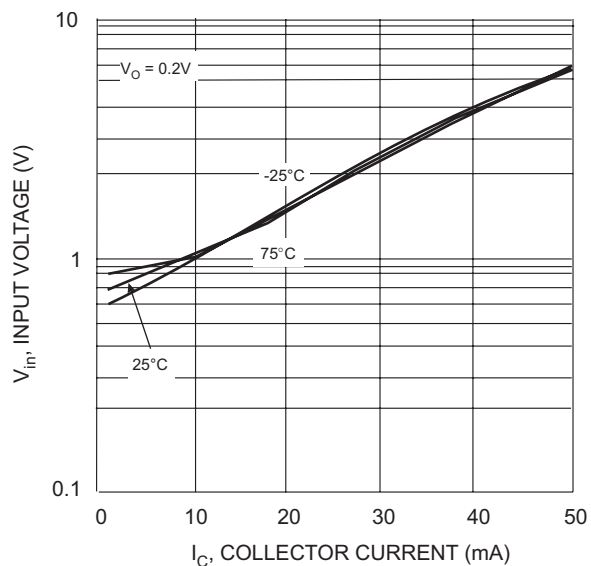


Fig. 6 Input Voltage vs. Collector Current

TYPICAL CURVES - DDA114TK

ONE SECTION

NEW PRODUCT

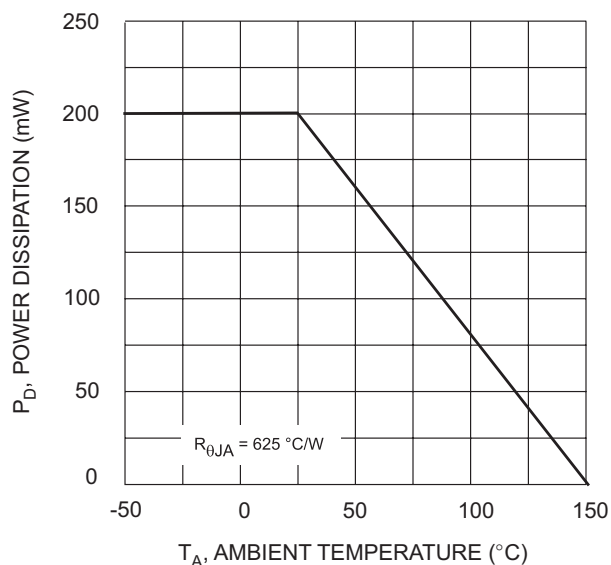


Fig. 1 Derating Curve

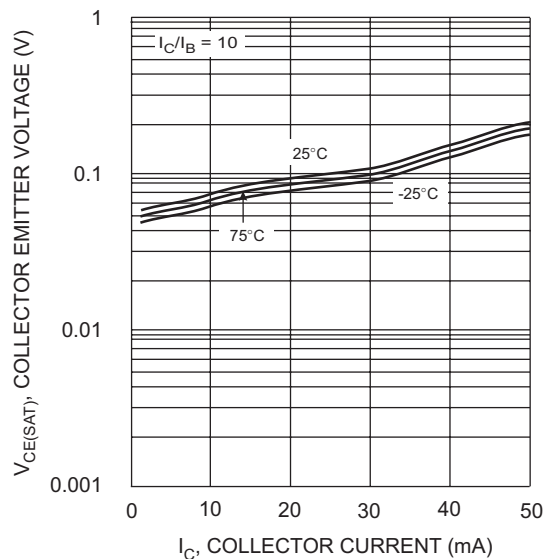


Fig. 2 $V_{CE(SAT)}$ vs. I_C

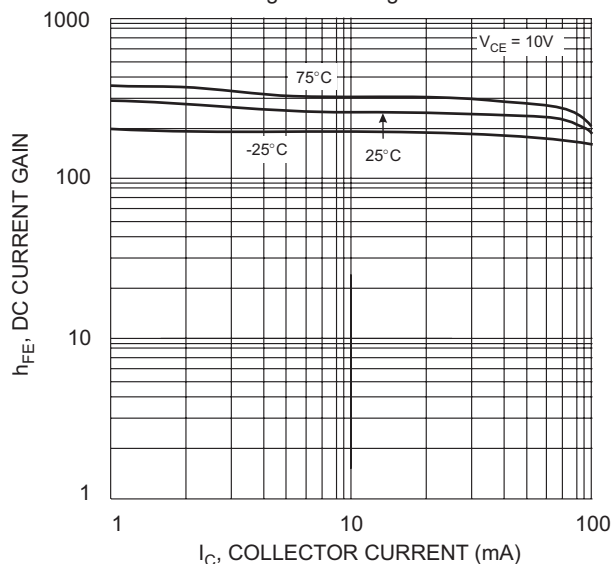


Fig. 3 DC Current Gain

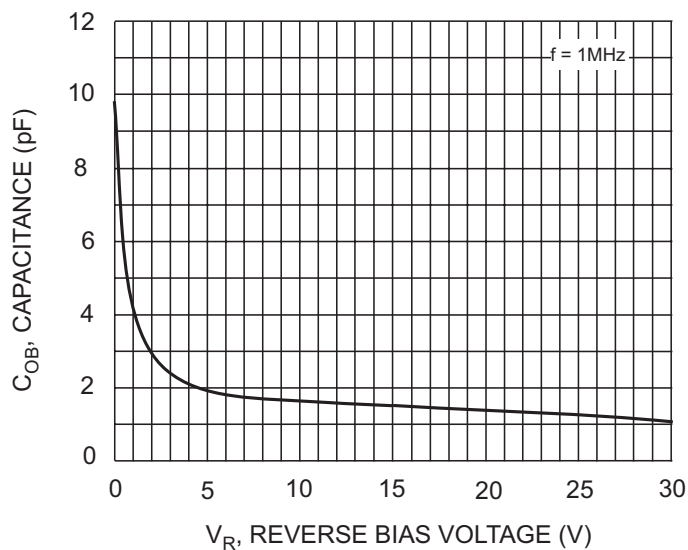


Fig. 4 Output Capacitance

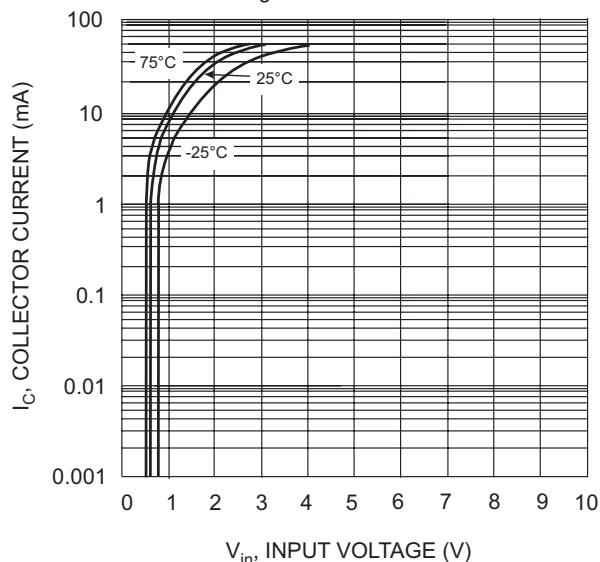


Fig. 5 Collector Current Vs. Input Voltage

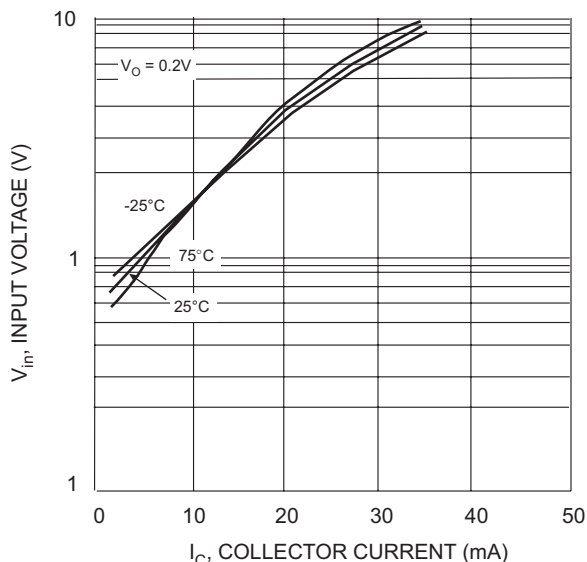


Fig. 6 Input Voltage vs. Collector Current

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