

# HumPRC<sup>™</sup> Series Evaluation Module Data Guide

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All RF products are susceptible to RF interference that can prevent <u>communication</u>. RF products without frequency agility or hopping implemented are more subject to interference. This module does have a frequency hopping protocol built in, but the developer should still be aware of the risk of interference.

**Do not use any Linx product over the limits in this data guide.** Excessive voltage or extended operation at the maximum voltage could cause product failure. Exceeding the reflow temperature profile could cause product failure which is not immediately evident.

**Do not make any physical or electrical modifications to any Linx product.** This will void the warranty and regulatory and UL certifications and may cause product failure which is not immediately evident.

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 $\textbf{HumPRC}^{\text{TM}} \text{ Series Evaluation Module}$ 



### Data Guide



Figure 1: HumPRC<sup>™</sup> Series Evaluation Modules; from left: EVM-\*\*\*-PRC, EVM-\*\*\*-PRC-CAS, EVM-\*\*\*-PRC-UFL

#### Description

The HumPRC<sup>™</sup> Series is the most complete system to integrate bi-directional remote control into many different applications. No programming is required and both module and finished hardware options are available, making it the easiest solution to implement.

The module provides long-range transmission at 868MHz and 900MHz for multi-region operation, utilizing frequency hopping and industry-standard encryption for secure and robust communications. The HumPRC<sup>™</sup> Series interoperates with Linx's HumPRO<sup>™</sup> family, making it the only remote control solution that simultaneously supports data applications for seamless integration with sensor and control IoT applications.

The evaluation module contains the surface mount HumPRC<sup>™</sup> Series transceiver module on a board with through-hole headers. This small board simplifies prototyping with the HumPRC<sup>™</sup> Series module, allowing it to be integrated into a host PCB without requiring any soldering.

There are three versions of the evaluation module, populated with the three versions of the HumPRC<sup>™</sup> Series module. The standard version has the standard module and an MMCX connector for use with the Master Development System. The "-CAS" version has an edge-mount RP-SMA connector for the antenna. The "-UFL" version uses the module's on-board U.FL connector for the antenna.

#### **Ordering Information**

Ordering Information				
Part Number	Description			
EVM-***-PRC	HumPRC <sup>™</sup> Series Carrier Board, Not Certified, Through-Hole Pir Interface, MMCX Connector for the Development System			
EVM-***-PRC-CAS	HumPRC <sup>™</sup> Series Carrier Board, Certified, Through-Hole Pin Interface, RP-SMA Connector			
EVM-***-PRC-UFL	HumPRC <sup>™</sup> Series Carrier Board, Certified, Through-Hole Pin Interface, U.FL Connector			
HUM-***-PRC	HumPRC <sup>™</sup> Series Remote Control Transceiver, Castellation Interface, External Antenna Connection			
HUM-***-PRC-CAS	HumPRC <sup>™</sup> Series Remote Control Transceiver, Certified, Castellation Interface, External Antenna Connection			
HUM-***-PRC-UFL	HumPRC <sup>™</sup> Series Remote Control Transceiver, Certified, Castellation Interface, U.FL Connector			
MDEV-***-PRC	HumPRC <sup>™</sup> Series Master Development System			
MDEV-PGDOCK	Development System Programming Dock			
MDEV-PROTO	Development System Prototype Board			
CON-SOC-EVM	EVM Module Socket Kit			
*** = Frequency; 868,	900MHz			

Figure 2: Ordering Information

#### Absolute Maximum Ratings

Absolute Maximum Ratings				
Supply Voltage $V_{cc}$	-0.3	to	+3.9	VDC
Any Input or Output Pin	-0.3	to	V <sub>cc</sub> + 0.3	VDC
RF Input		0		dBm
Operating Temperature	-40	to	+85	°C
Storage Temperature	-40	to	+85	°C

Exceeding any of the limits of this section may lead to permanent damage to the device. Furthermore, extended operation at these maximum ratings may reduce the life of this device.

Figure 3: Absolute Maximum Ratings

Warning: This product incorporates numerous static-sensitive components. Always wear an ESD wrist strap and observe proper ESD handling procedures when working with this device. Failure to observe this precaution may result in module damage or failure.

#### **Electrical Specifications**

Parameter	Symbol	Min.	Тур.	Max.	Units	Notes
Power Supply						
Operating Voltage	V <sub>cc</sub>	2.0		3.6	VDC	
TX Supply Current	Ісстх					
at +10dBm			40.5	41.5	mA	1,2
at 0dBm			22	24	mA	1,2
RX Supply Current	I <sub>CCRX</sub>		23.5	24.5	mA	1,2,3
Power-Down Current	PDN		0.7	1.4	μA	1,2
RF Section						
Operating Frequency Band	F <sub>c</sub>				MHz	
EVM-900-PRC		902		928	MHz	
EVM-868-PRC		863		870	MHz	
RF Data Rate						
EVM-900-PRC		19.2		152.34	kbps	
EVM-868-PRC			38.4		kbps	
Serial Data Rate		9.6		115.2	kbps	
Receiver Sensitivity						5
EVM-900-PRC @min rate		-98	-101		dBm	5
EVM-900-PRC @max rate		-91	-94		dBm	5
EVM-868-PRC		-97	-100		dBm	5
Output Power	Po					
EVM-900-PRC		+8.5	+9.5		dBm	6
EVM-868-PRC		+8.5	+10.6		dBm	6
Antenna Port						
RF Impedance	R <sub>IN</sub>		50		Ω	4
Environmental						
Operating Temp. Range		-40		+85	°C	4
<ol> <li>Measured at 3.3V V<sub>cc</sub></li> <li>Measured at 25°C</li> <li>Input power &lt; -60dBm</li> </ol>		4. 5. 6.	Characteriz PER = 5% Into a 50-ol		tested	

Figure 4: Electrical Specifications

Please see the HumPRC<sup>™</sup> Series Transceiver module data guide for full electrical specifications.

#### **Pin Assignments**



Figure 5: EVM-fff-PRC Pin Assignments

#### **Pin Descriptions**

Pin Descriptions					
Pin Number	Name	I/O	Description		
1	ANTENNA	-	50-ohm RF Antenna Port		
2, 3, 4, 5, 6	GND	-	Ground		
7	MODE_IND	0	Mode Indicator. This line indicates module activity. It can source enough current to drive a small LED, causing it to flash. The duration of the flashes indicates the module's current state.		
8	RESET <sup>2</sup>	I	This line resets the module when pulled low. It should be pulled high for normal operation.		
9	CMD_DATA_IN	I	Command Data In. Input line for the serial interface commands. If serial control is not used, this line should be tied to supply to minimize current consumption.		
10	POWER_DOWN	I	Power Down. Pulling this line low places the module into a low-power state. The module is not functional in this state. Pull high for normal operation. Do not leave floating.		
11	LATCH_EN	I	If this line is high, then the status line outputs are latched (a received command to activate a status line toggles the output state). If low, then the output lines are momentary (active for as long as a valid signal is received).		

Pin Number	Name	I/O	Description
12, 18, 22–37, 47-56	NC	-	No Electrical Connection. Do not connec any traces to these lines.
13	ACK_EN	I	Pull this line high to enable the module to send an acknowledgement message after valid control message has been received.
14	PAIR <sup>1</sup>	I	A high on this line initiates the Pair process which causes two units to accept each other's transmissions. It is also used with a special sequence to reset the module to factory default configuration.
15	CMD_DATA_OUT	0	Command Data Out. Output line for data and serial commands
16	LNA_EN	0	Low Noise Amplifier Enable. This line is driven high when receiving. It is intended activate an optional external LNA.
17	VCC	-	Supply Voltage
19	CO	I	This line sets the input/output direction for status lines S0-S3. When low, the lines are outputs; when high they are inputs.
20	PA_EN	0	Power Amplifier Enable. This line is driver high when transmitting. It is intended to activate an optional external power ampli
21	C1	I	This line sets the input/output direction for status lines S4-S7. When low, the lines an outputs; when high they are inputs.
38–45	S0-S71	I/O	Status Lines. Each line can be configured as either an input to register button or contact closures or as an output to contr application circuitry.
46	ACK_OUT	0	This line goes high when the module receives an acknowledgement message from another module after sending a con message.

2. These lines have an internal  $10k\Omega$  pull-up resistor

Figure 6: EVM-fff-PRC Pin Descriptions

### Schematic

Figure 7 shows the schematic diagram for the evaluation module.



### Pad Layout

Figure 8 shows the recommended PCB layout for the evaluation module.



Figure 8: EVM-fff-PRC PCB Layout Dimensions

#### **Power Supply Requirements**

The module does not have an internal voltage regulator, therefore it requires a clean, well-regulated power source. The power supply noise should be less than 20mV. Power supply noise can significantly affect the module's performance, so providing a clean power supply for the module should be a high priority during design.



Figure 9: Supply Filter

A 10 $\Omega$  resistor in series with the supply followed by a 10 $\mu$ F tantalum capacitor from V<sub>cc</sub> to ground helps in cases where the quality of supply power is poor. This filter should be placed close to the module's supply lines. These values may need to be adjusted depending on the noise present on the supply line.

Figure 7: EVM-fff-PRC Schematic

#### Dimensions

The figures below show the dimensions for the three variants of the module.





7.54 mm (0.297 in) 5.98 mm 4.24 mm (0.235 in) (0.167 in) -Ø6.35 mm (0.25 in) Ø7.93 mm (0.312 in) . 2 37.43 mm . Ō Ţ. (1.473 in) . 29.72 mm (1.170 in) . aaaaaaa . . . . . . . <u>tennnnnn</u> 26.64 mm 6.53 mm (0.970 in) (0.257 in) 9.83 mm (0.387 in)

11.51 mm (0.453 in)

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Figure 10: EVM-fff-PRC Dimensions





Figure 12: EVM-fff-PRC-CAS Dimensions

Figure 11: EVM-fff-PRC-UFL Dimensions

Notes



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