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FDMS8023S

October 2014

N-Channel PowerTrench[®] SyncFETTM 30 V, 49 A, 2.4 m Ω

Features

- Max $r_{DS(on)} = 2.4 \text{ m}\Omega$ at $V_{GS} = 10 \text{ V}$, $I_D = 26 \text{ A}$
- Max $r_{DS(on)} = 3.0 \text{ m}\Omega$ at $V_{GS} = 4.5 \text{ V}$, $I_D = 23 \text{ A}$
- Advanced package and silicon combination for low r_{DS(on)} and high efficiency
- SyncFET Schottky Body Diode
- MSL1 robust package design
- 100% UIL tested
- RoHS Compliant

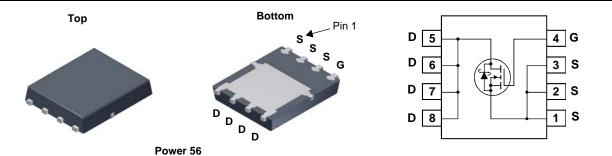


General Description

The FDMS8023S has been designed to minimize losses in power conversion application. Advancements in both silicon and package technologies have been combined to offer the lowest $r_{\text{DS}(\text{on})}$ while maintaining excellent switching performance.This device has the added benefit of an efficient monolithic Schottky body diode.

Applications

- Synchronous Rectifier for DC/DC Converters
- Notebook Vcore/GPU low side switch
- Networking Point of Load low side switch
- Telecom secondary side rectification



MOSFET Maximum Ratings T_C = 25°C unless otherwise noted

| Symbol | Parameter | | | Ratings | Units |
|-----------------------------------|--|-----------------------|-----------|-------------|-------|
| V_{DS} | Drain to Source Voltage | | | 30 | V |
| V_{GS} | Gate to Source Voltage | | (Note 4) | ±20 | V |
| | Drain Current -Continuous (Package limited) | T _C = 25°C | | 49 | |
| | -Continuous (Silicon limited) | T _C = 25°C | | 128 | A |
| ID | -Continuous | T _A = 25°C | (Note 1a) | 26 | A |
| | -Pulsed | | | 100 | |
| E _{AS} | Single Pulse Avalanche Energy | | (Note 3) | 86 | mJ |
| P _D | Power Dissipation | T _C = 25°C | | 59 | W |
| | Power Dissipation | T _A = 25°C | (Note 1a) | 2.5 | VV |
| T _J , T _{STG} | Operating and Storage Junction Temperature Range | | | -55 to +150 | °C |

Thermal Characteristics

| $R_{\theta JC}$ | Thermal Resistance, Junction to Case | 2.1 | °C/W |
|-----------------|---|-----|------|
| $R_{\theta JA}$ | Thermal Resistance, Junction to Ambient (Note 1a) | 50 | C/VV |

Package Marking and Ordering Information

| Device Marking | Device | Package | Reel Size | Tape Width | Quantity |
|----------------|-----------|----------|-----------|------------|------------|
| FDMS8023S | FDMS8023S | Power 56 | 13 " | 12 mm | 3000 units |

Electrical Characteristics T_J = 25°C unless otherwise noted

| Symbol | Parameter | Test Conditions | Min | Тур | Max | Units | | |
|--------------------------------------|--|---|-----|-----|-----|-------|--|--|
| Off Characteristics | | | | | | | | |
| BV_{DSS} | Drain to Source Breakdown Voltage | $I_D = 1$ mA, $V_{GS} = 0$ V | 30 | | | V | | |
| $\frac{\Delta BV_{DSS}}{\Delta T_J}$ | Breakdown Voltage Temperature Coefficient | I _D = 10 mA, referenced to 25°C | | 16 | | mV/°C | | |
| I _{DSS} | Zero Gate Voltage Drain Current | V _{DS} = 24 V, V _{GS} = 0 V | | | 500 | μΑ | | |
| I _{GSS} | Gate to Source Leakage Current, Forward | V _{GS} = 20 V, V _{DS} = 0 V | | | 100 | nA | | |

On Characteristics

| V _{GS(th)} | Gate to Source Threshold Voltage | $V_{GS} = V_{DS}$, $I_D = 1$ mA | 1.2 | 1.7 | 3.0 | V |
|--|---|---|-----|-----|-----|-------|
| $\frac{\Delta V_{GS(th)}}{\Delta T_J}$ | Gate to Source Threshold Voltage Temperature Coefficient | I _D = 10 mA, referenced to 25°C | | -5 | | mV/°C |
| r _{DS(on)} | Static Drain to Source On Resistance | V _{GS} = 10 V, I _D = 26 A | | 2.0 | 2.4 | |
| | | $V_{GS} = 4.5 \text{ V}, I_D = 23 \text{ A}$ | | 2.6 | 3.0 | mΩ |
| | | V _{GS} = 10 V, I _D = 26 A, T _J = 125°C | | 2.7 | 3.3 | |
| g _{FS} | Forward Transconductance | V _{DS} = 5 V, I _D = 26 A | | 168 | | S |

Dynamic Characteristics

| C _{iss} | Input Capacitance | V _{DS} = 15 V, V _{GS} = 0 V, f = 1MHz | 2670 | 3550 | pF |
|------------------|------------------------------|--|------|------|----|
| C _{oss} | Output Capacitance | | 975 | 1300 | pF |
| C _{rss} | Reverse Transfer Capacitance | | 95 | 140 | pF |
| R_g | Gate Resistance | | 0.7 | 2.5 | Ω |

Switching Characteristics

| t _{d(on)} | Turn-On Delay Time | | 12 | 21 | ns |
|---------------------|-------------------------------|---|-----|----|----|
| t _r | Rise Time | V _{DD} = 15 V, I _D = 26 A, | 5 | 10 | ns |
| t _{d(off)} | Turn-Off Delay Time | V_{GS} = 10 V, R_{GEN} = 6 Ω | 32 | 52 | ns |
| t _f | Fall Time | | 4 | 10 | ns |
| Q_g | Total Gate Charge | V _{GS} = 0 V to 10 V | 41 | 57 | nC |
| Qg | Total Gate Charge | $V_{GS} = 0 \ V \text{ to } 4.5 \ V_{DD} = 15 \ V,$ | 20 | 28 | nC |
| Q _{gs} | Gate to Source Charge | I _D = 26 A | 7.0 | | nC |
| Q _{gd} | Gate to Drain "Miller" Charge | | 5.4 | | nC |

Drain-Source Diode Characteristics

| V _{SD} | Source-Drain Dioge Forward Voltage | $V_{GS} = 0 \ V, I_S = 2 \ A$ (Note 2) | 0.63 | 0.8 | V |
|-----------------|------------------------------------|---|------|-----|----|
| | | $V_{GS} = 0 \text{ V}, I_S = 26 \text{ A}$ (Note 2) | 0.8 | 1.2 | |
| t _{rr} | Reverse Recovery Time | I _F = 26 A, di/dt = 300 A/μs | 29 | 46 | ns |
| Q _{rr} | Reverse Recovery Charge | 1F = 26 A, α/αι = 300 A/μs | 32 | 51 | nC |

Notes:

^{1.} R_{0JA} is determined with the device mounted on a 1 in² pad 2 oz copper pad on a 1.5 x 1.5 in. board of FR-4 material. R_{0JC} is guaranteed by design while R_{0CA} is determined by the user's board design.



a. 50 °C/W when mounted on a
 1 in² pad of 2 oz copper.



b. 125 °C/W when mounted on a minimum pad of 2 oz copper.

- 2. Pulse Test: Pulse Width < 300 $\mu\text{s},$ Duty cycle < 2.0%.
- 3. E_{AS} of 86 mJ is based on starting T_J = 25 °C, L = 0.3 mH, I_{AS} = 24 A, V_{DD} = 27 V, V_{GS} = 10 V.
- 4. As an N-ch device, the negative Vgs rating is for low duty cycle pulse occurrence only. No continuous rating is implied.

Typical Characteristics $T_J = 25^{\circ}C$ unless otherwise noted

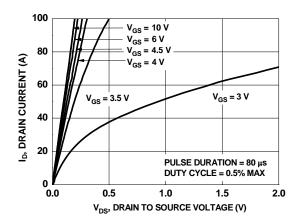


Figure 1. On Region Characteristics

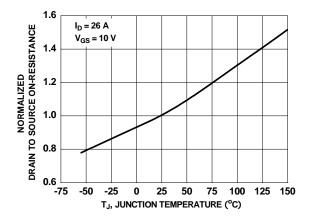


Figure 3. Normalized On Resistance vs Junction Temperature

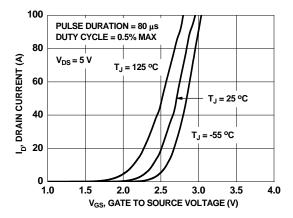


Figure 5. Transfer Characteristics

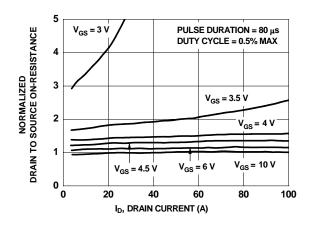


Figure 2. Normalized On-Resistance vs Drain Current and Gate Voltage

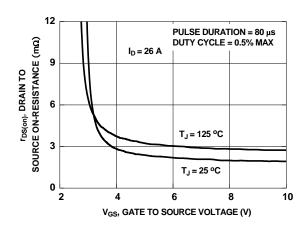


Figure 4. On-Resistance vs Gate to Source Voltage

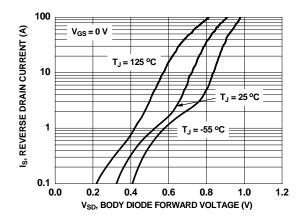


Figure 6. Source to Drain Diode Forward Voltage vs Source Current

Typical Characteristics T_J = 25°C unless otherwise noted

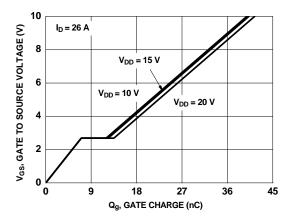


Figure 7. Gate Charge Characteristics

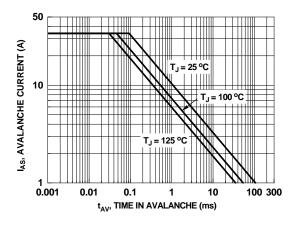


Figure 9. Unclamped Inductive Switching Capability

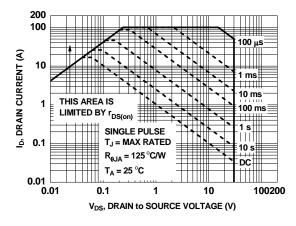


Figure 11. Forward Bias Safe Operating Area

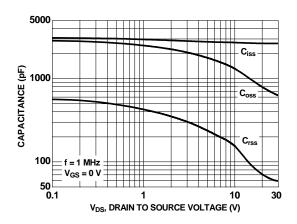


Figure 8. Capacitance vs Drain to Source Voltage

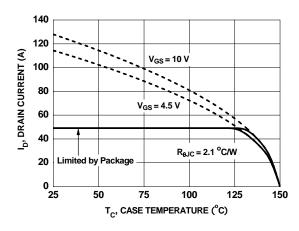


Figure 10. Maximum Continuous Drain Current vs Case Temperature

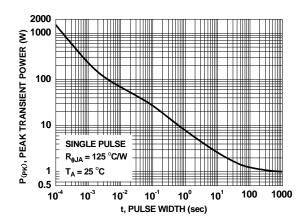


Figure 12. Single Pulse Maximum Power Dissipation

Typical Characteristics T_J = 25°C unless otherwise noted

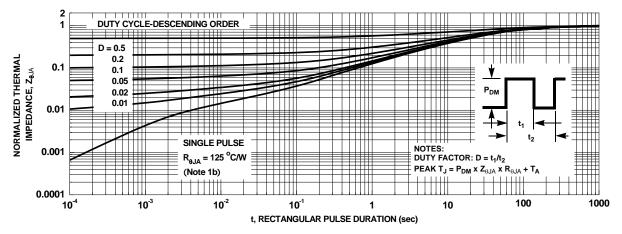


Figure 13. Junction-to-Ambient Transient Thermal Response Curve

Typical Characteristics (continued)

SyncFET Schottky body diode Characteristics

Fairchild's SyncFET process embeds a Schottky diode in parallel with PowerTrench MOSFET. This diode exhibits similar characteristics to a discrete external Schottky diode in parallel with a MOSFET. Figure 14 shows the reverse recovery characteristic of the FDMS8023S.

Schottky barrier diodes exhibit significant leakage at high temperature and high reverse voltage. This will increase the power in the device.

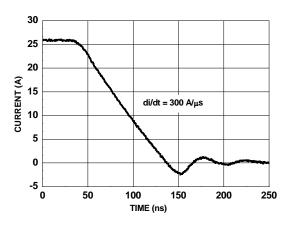


Figure 14. FDMS8023S SyncFET body diode reverse recovery characteristic

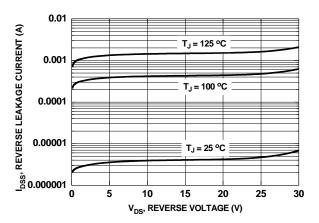


Figure 15. SyncFET body diode reverse leakage versus drain-source voltage



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