



DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	I _D max T _A = +25°C
	$3\Omega @ V_{GS} = 4.5V$	250 mA
30V	5Ω @ V _{GS} = 4.0V	200 mA
	7Ω @ V _{GS} = 2.5V	100 mA

Description

This MOSFET has been designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Motor Control
- Power Management Functions
- DC-DC Converters
- Backlighting





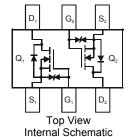
Top View

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- ESD Protected Gate to 2kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 standards for High Reliability

Mechanical Data

- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See diagram
- Terminals: Finish Matte Tin annealed over Alloy42 leadframe.
 Solderable per MIL-STD-202, Method 208 (a)
- Weight: 0.006 grams (approximate)



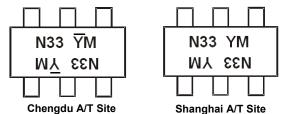
Ordering Information (Note 4)

Part Number	Case	Packaging
DMN33D8LDW-7	SOT363	3K/Tape & Reel
DMN33D8LDW-13	SOT363	10K/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds
- 4. For packaging details, go to our website at http"//www.diodes.com/products/packages.html

Marking Information



N33 = Product Type Marking Code

YM = Date Code Marking for SAT (Shanghai Assembly/ Test site) \overline{Y} M = Date Code Marking for CAT (Chengdu Assembly/ Test site)

Y or \overline{Y} = Year (ex: A = 2013) M = Month (ex: 9 = September)

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Date Code Key

Year	201	1	2012		2013	20	14	2015		2016	2	2017
Code	Υ		Z		Α	E	3	С		D		Е
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units	
Drain-Source Voltage	V_{DSS}	30	V		
Gate-Source Voltage	V _{GSS}	±20	V		
		T _A = +25°C T _A = +70°C	I _D	250 200	mA
Maximum Continuous Body Diode Forward Current	(Note 5)	Is	0.5	Α	
Pulsed Drain Current (10µs pulse, duty cycle=1%)		I _{DM}	0.8	А	

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

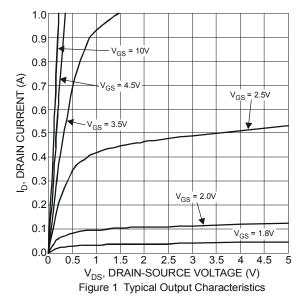
Characteristic		Symbol	Value	Units
Total Dawer Dissipation (Note 5)	T _A = +25°C	D-	0.35	W
Total Power Dissipation (Note 5)	T _A = +70°C	P_{D}	0.22	VV
Thermal Resistance, Junction to Ambient (Note 5) Steady State		$R_{\theta JA}$	360	°C/W
Thermal Resistance, Junction to Case	$R_{ heta JC}$	126	C/VV	
Operating and Storage Temperature Range	$T_{J,}T_{STG}$	-55 to 150	°C	

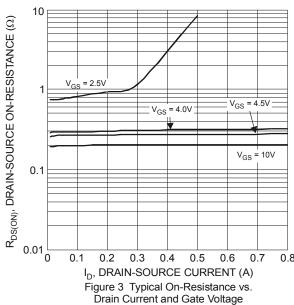
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

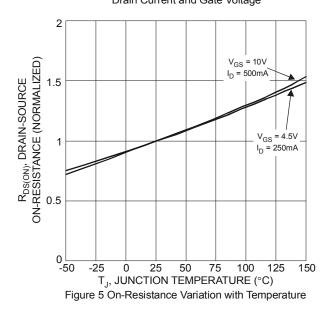
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BV _{DSS}	30	_	_	V	$V_{GS} = 0V$, $I_D = 1mA$
Zero Gate Voltage Drain Current @T _C = +25°C	I _{DSS}	_	_	1	μΑ	V _{DS} = 30V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	_	_	±10	μΑ	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	V _{GS(th)}	0.8	_	1.5	V	$V_{DS} = 3V, I_{D} = 100\mu A$
		_	_	2.4		$V_{GS} = 10V, I_D = 250mA$
		_	_	3.0		$V_{GS} = 4.5V, I_D = 250mA$
Static Drain-Source On-Resistance	R _{DS(ON)}	_	_	5.0	Ω	$V_{GS} = 4.0V, I_D = 10mA$
		_	_	7.0		$V_{GS} = 2.5V, I_D = 5mA$
		_	_	20		$V_{GS} = 1.8V, I_D = 5mA$
Forward Transfer Admittance	Y _{fs}	10	_	-	mS	$V_{DS} = 3V, I_{D} = 10mA$
Diode Forward Voltage	V_{SD}	_	_	1.2	V	V _{GS} = 0V, I _S = 115mA
DYNAMIC CHARACTERISTICS (Note 7)						
Input Capacitance	C _{iss}	_	48	_	pF	., 5),,,,
Output Capacitance	Coss	_	11	_	pF	$V_{DS} = 5V, V_{GS} = 0V,$ -f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	8	_	pF	1 = 1:0lvii iz
Gate Resistance	R_g	_	57	_	Ω	f=1MHz , Vgs=0V, Vds=0V
Total Gate Charge (V _{GS} = 4.5V)	Q_g	_	0.55	_	nC	
Total Gate Charge (V _{GS} = 10V)	Qg	_	1.23	_	nC	$V_{GS} = 10V, V_{DS} = 10V,$
Gate-Source Charge	Q_{gs}	_	0.14	_	nC	I _D = 250mA
Gate-Drain Charge	Q_{gd}	_	0.14	_	nC	
Turn-On Delay Time	t _{D(on)}	_	2.9	_	ns	
Turn-On Rise Time		_	2.6	_	ns	V _{DD} = 30V, V _{GS} = 10V,
Turn-Off Delay Time		_	18.2	_	ns	$R_G = 25\Omega$, $I_D = 200mA$
Turn-Off Fall Time		_	13.6	_	ns	

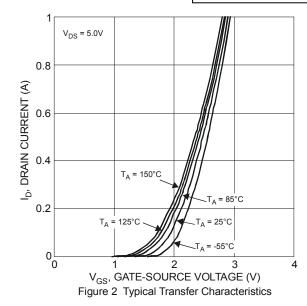
5. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
6. Short duration pulse test used to minimize self-heating effect.
7. Guaranteed by design. Not subject to product testing.











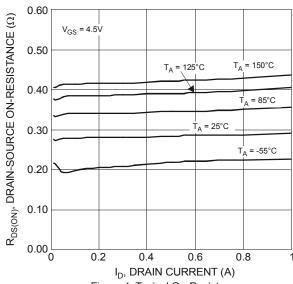


Figure 4 Typical On-Resistance vs. Drain Current and Temperature

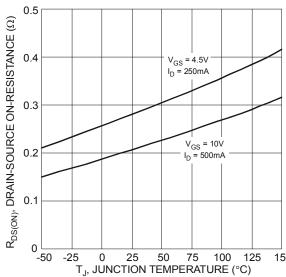


Figure 6 On-Resistance Variation with Temperature



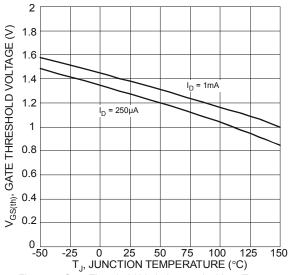
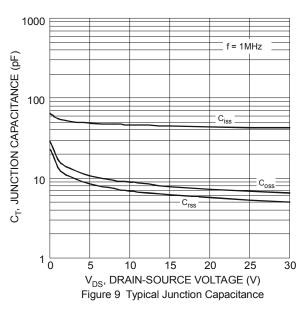
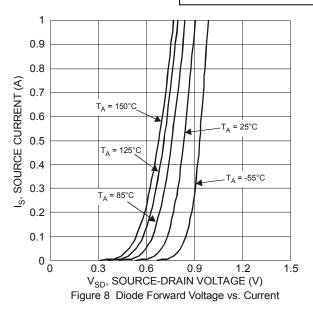
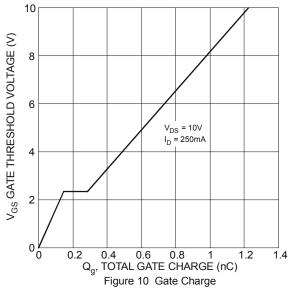
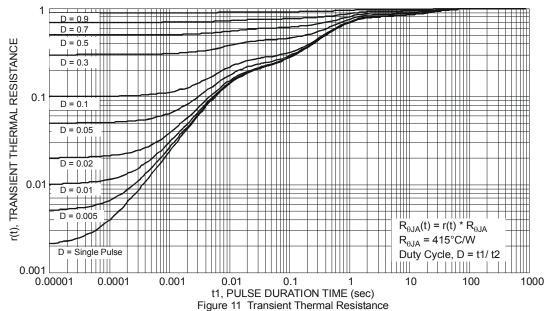


Figure 7 Gate Threshold Variation vs. Ambient Temperature





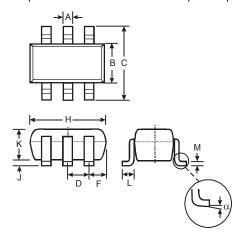






Package Outline Dimensions

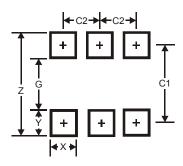
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



	SOT363							
Dim	Min	Тур						
Α	0.10	0.30	0.25					
В	1.15	1.35	1.30					
С	2.00	2.00 2.20						
D	0.65 Typ							
F	0.40	0.45	0.425					
Н	1.80	2.20	2.15					
J	0	0.10	0.05					
K	0.90	1.00	1.00					
L	0.25	0.40	0.30					
М	0.10	0.22	0.11					
α	0°	8°	-					
All	All Dimensions in mm							

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.5
G	1.3
X	0.42
Y	0.6
C1	1.9
C2	0.65



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многоканальный

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Офис по работе с юридическими лицами:

105318, г. Москва, ул. Щербаковская д. 3, офис 1107, 1118, ДЦ «Щербаковский»

Телефон: +7 495 668-12-70 (многоканальный)

Факс: +7 495 668-12-70 (доб.304)

E-mail: info@moschip.ru

Skype отдела продаж:

moschip.ru moschip.ru_6 moschip.ru 4 moschip.ru 9