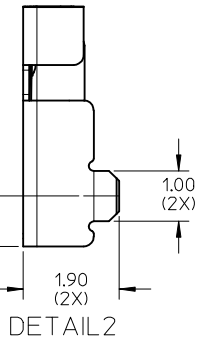
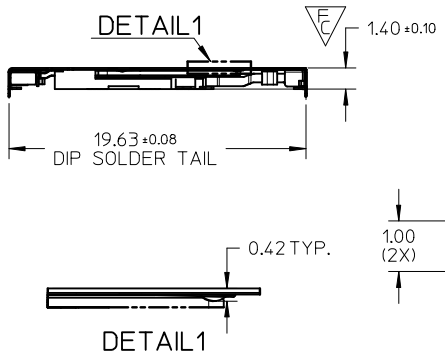
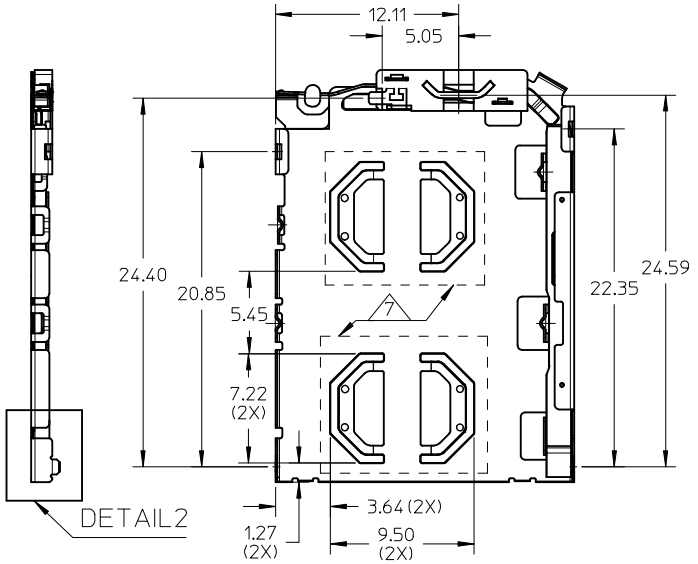
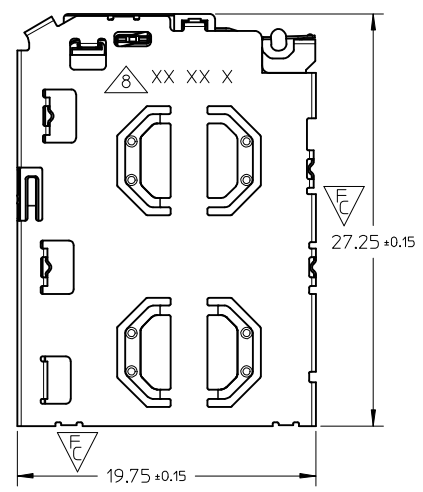
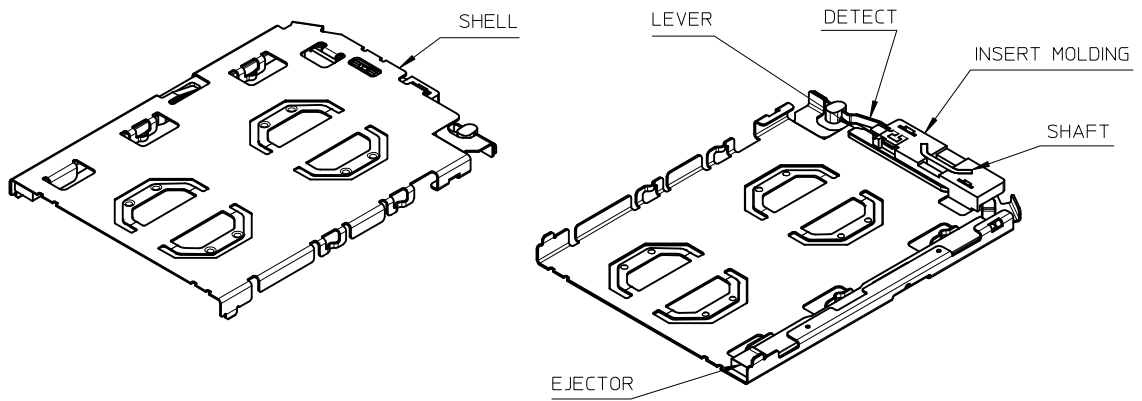


10 9 8 7 6 5 4 3 2 1

THIS DESIGN IS BASED ON DESIGN OBJECTIVES AND IS STRICTLY TENTATIVE. IT MAY CHANGE BASED ON RESULTS OF ADDITIONAL DESIGN REVIEWS & VERIFICATIONS.

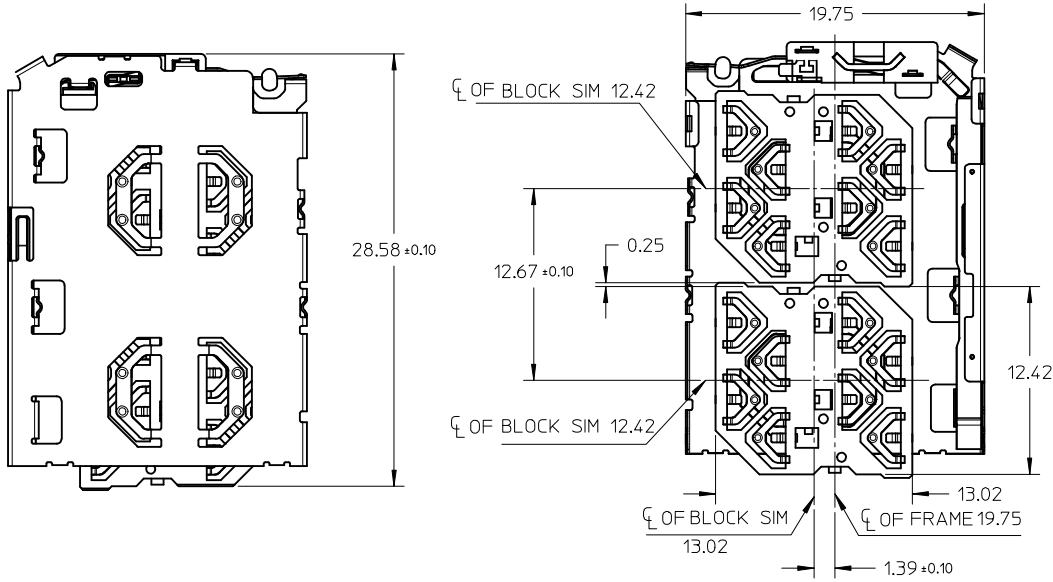


NOTES:  
 1. MATERIALS:  
 INSERT MOLD HOUSING: LCP, UL94V-0;  
 LEVER, SHAFT, EJECTOR, SHELL: STAINLESS STEEL;  
 DETECT SPRING: COPPER ALLOY;  
 2. FINISHES:  
 DETECT SPRING:  
 1.27um MIN. NICKEL UNDERPLATING OVERALL;  
 0.127um MIN. GOLD PLATING ON CONTACT AREA;  
 1.27 um MIN. TIN PLATING ON SOLDERING TAIL;  
 SHELL:  
 1.27um MIN NICKEL UNDERPLATING OVERALL;  
 0.025um MIN GOLD PLATING ON CONTACT AREA AND SOLDERING AREA;  
 SHAFT: 1.27um MIN TIN ON SOLDERING TAIL;  
 3. PRODUCT SPECIFICATION: PS-151031-001;  
 4. PACKAGING SPECIFICATION: PK-151031-001;PK-151032-001  
 5. SOLDER TAIL COPLANARITY: 0.10 MM MAX BEFORE REFLOW  
 6. THIS PART IS A FRAME ONLY, IT SHOULD BE USED TOGETHER WITH 0.35MM BLOCK SIM 151032 FOR AN ENTIRE SIM POP OUT SYSTEM;  
 7. 0.10 MINIMUM KEEP OUT ZONE FROM TOP SURFACE OF SHELL DURING INSERTION AND WITHDRAWAL OF TRAY (WITH SIM CARD)  
 8. DATE CODE PRINTED: XX XX X  
 DAY  
 WEEK  
 YEAR

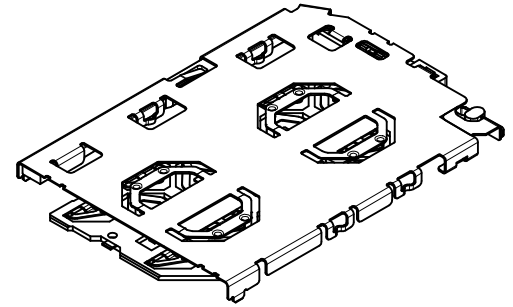
CHANGE BLOCK SIM AND TRAY EC NO: S2014-0434 DRWN: JZENG 2013/11/04 CHKD: JTAN02 2014/01/02 APPR: KHLIM 2014/01/27	QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION	
	$F_A=0$	mm	INCH	DRAWN BY	DATE	TITLE			
	$F_C=4$	4 PLACES ± --- ± ---	JZENG 2013/11/04	DUAL MICRO SIM FRAME 1.40 H					
	$F_B=0$	3 PLACES ± --- ± ---	CHECKED BY	DATE	MATERIAL NO. 1510310001				
	2 PLACES ± 0.20 ± ---	JTAN02 2013/12/05	APPROVED BY		DATE	DOCUMENT NO. SD-151031-0001			
	1 PLACE ± 0.20 ± ---	KHLIM 2014/01/27	MATERIAL NO.		DATE	SHEET NO. 1 OF 5			
	0 PLACE ± --- ± ---		SIZE A3		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION				
	ANGULAR ± 3 °								
	DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS								

9 8 7 6 5 4 3 2 1

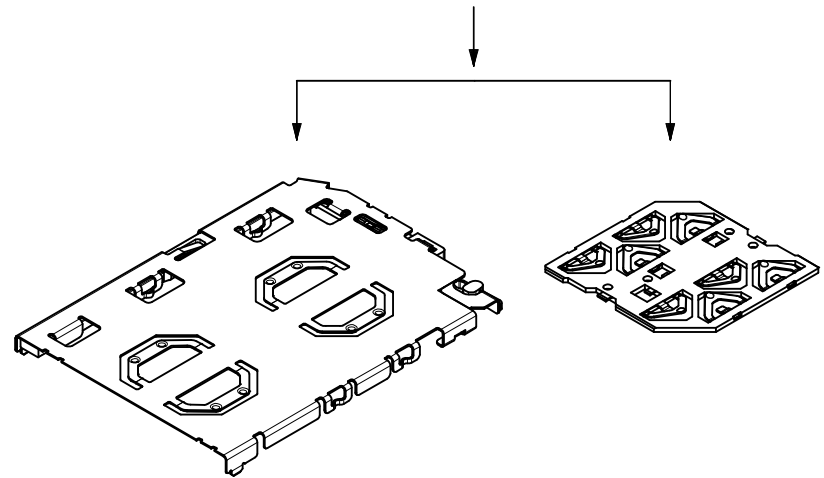
SIM CONNECTOR  
(WITH 151032 BLOCK SIM CONNECTOR)



SIM CONNECTOR BOM



FRAME + BLOCK SIM



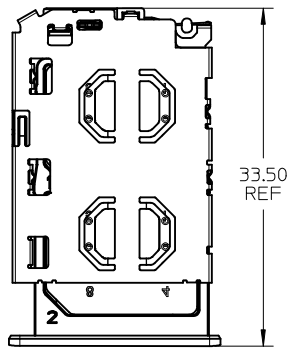
151031 SERIES

151032 SERIES

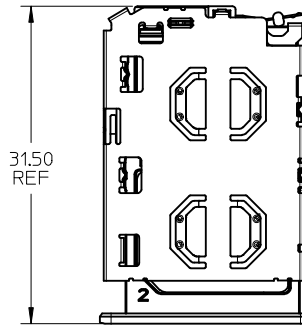
THIS DESIGN IS BASED ON DESIGN OBJECTIVES AND IS STRICTLY TENTATIVE. IT MAY CHANGE BASED ON RESULTS OF ADDITIONAL DESIGN REVIEWS & VERIFICATIONS.

SEE SHEET 1 EC NO: S2014-0434 DRWN: JZENG CHKD: JTAN02 APPR: KHL IM	2013/11/04 2014/01/02 2014/01/27	QUALITY SYMBOLS $F_A=0$ $F_B=0$ $F_C=0$	GENERAL TOLERANCES (UNLESS SPECIFIED) mm INCH 4 PLACES ± --- ± --- 3 PLACES ± --- ± --- 2 PLACES ± 0.20 ± --- 1 PLACE ± 0.20 ± --- 0 PLACE ± --- ± --- ANGULAR ± 3 °	DIMENSION STYLE MM ONLY DRAWN BY: JZENG CHECKED BY: JTAN02 APPROVED BY: KHL IM MATERIAL NO. 1510310001	SCALE NTS DESIGN UNITS METRIC THIRD ANGLE PROJECTION	DATE: 2013/11/04 DATE: 2013/12/05 DATE: 2014/01/27	TITLE DUAL MICRO SIM FRAME 1.40 H	DOCUMENT NO. SD-151031-0001	SHEET NO. 2 OF 5
	DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS	SIZE A3	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION						

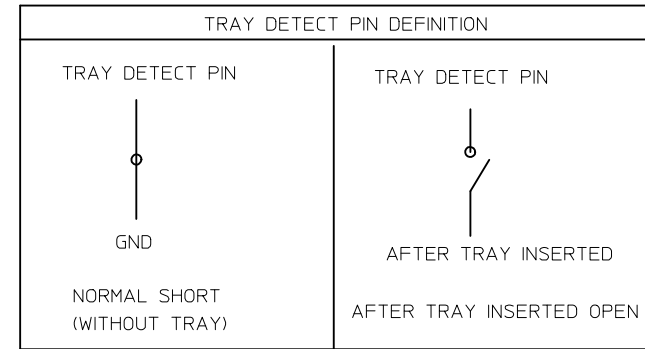
SIM CONNECTOR FRAME AND TRAY



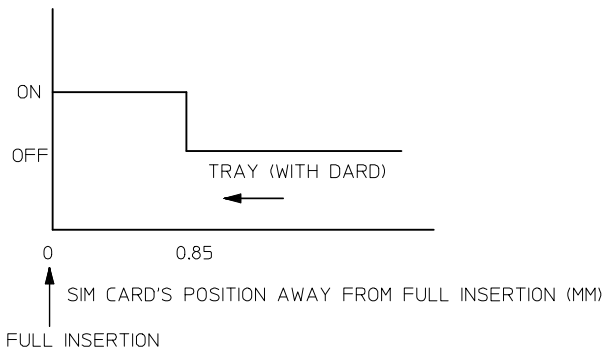
TRAY EJECTED POSITION



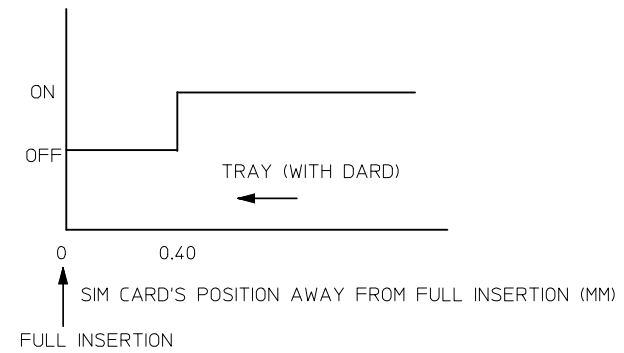
TRAY INSERTION POSITION



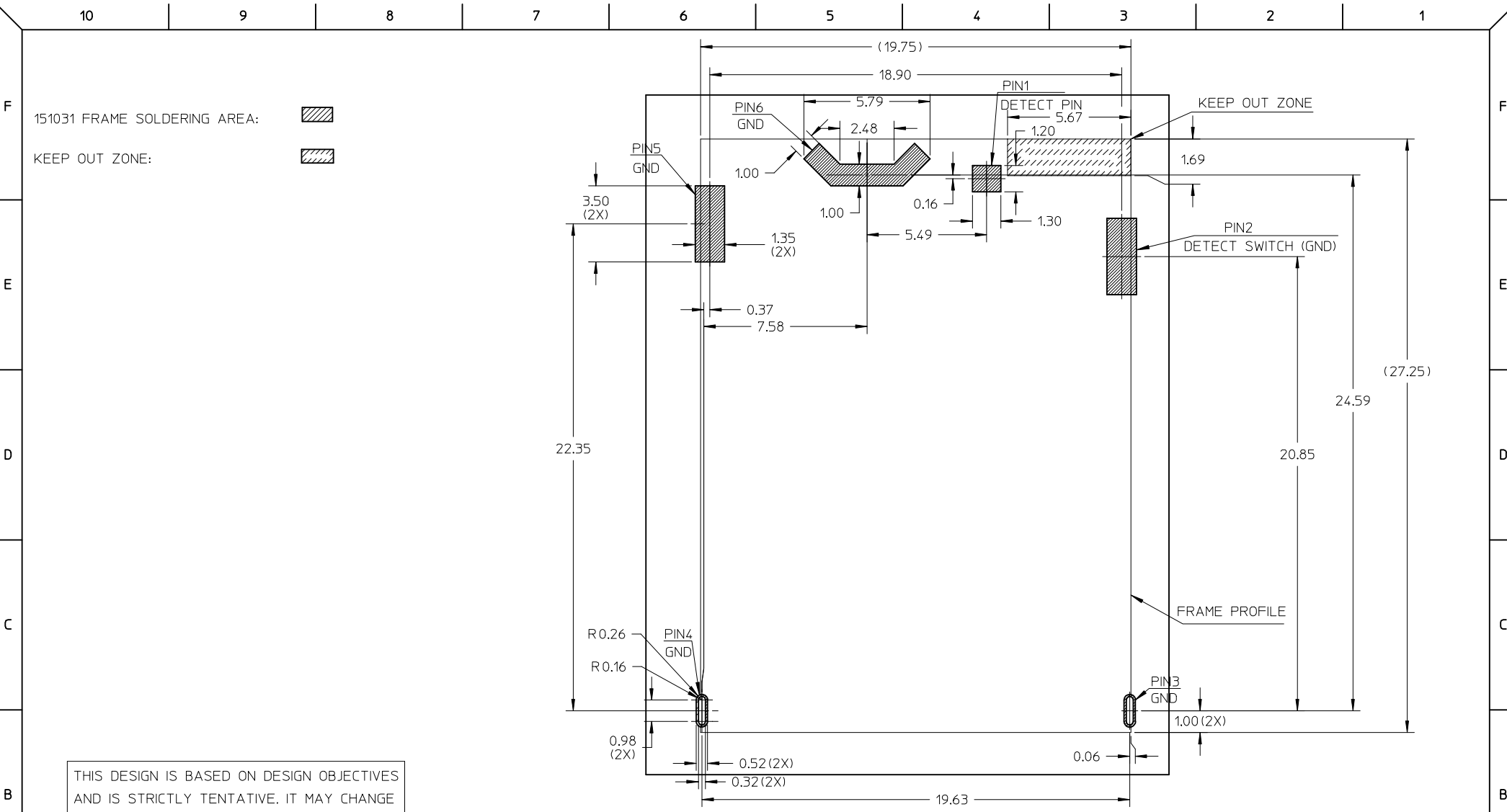
SIGNAL PIN



DETECT SWITCH PIN






ENTER DESCRIPTION EC NO: S2014-0434 DRWN: JZENG CHKD: JTAN02 APPR: KHL IM	DESCRIPTION 2013/11/04 2014/01/02 2014/01/27	QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION
		$F_A=0$ $F_G=0$ $F_P=0$	mm    INCH 4 PLACES ± --- ± --- 3 PLACES ± --- ± --- 2 PLACES ± 0.20 ± --- 1 PLACE ± 0.20 ± --- 0 PLACE ± --- ± ---	DRAWN BY JZENG	DATE 2013/11/04	TITLE DUAL MICRO SIM FRAME 1.40 H			
		ANGULAR ± 3 ° DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS	MATERIAL NO. 1510310001	CHECKED BY JTAN02	DATE 2013/12/05	DOCUMENT NO. SD-151031-0001			
			SIZE A3	APPROVED BY KHL IM	DATE 2014/01/27	SHEET NO. 3 OF 5			

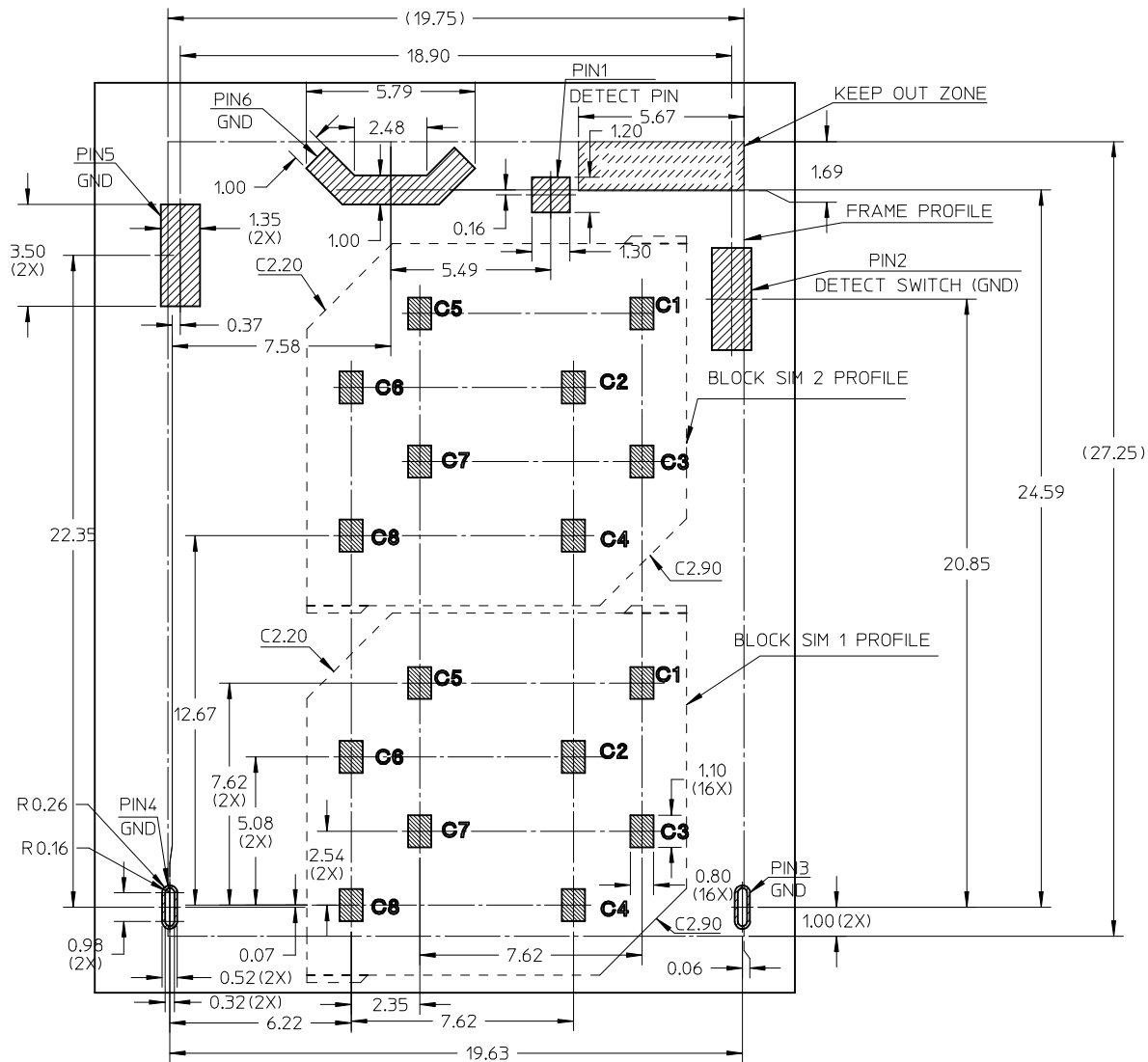


THIS DESIGN IS BASED ON DESIGN OBJECTIVES AND IS STRICTLY TENTATIVE. IT MAY CHANGE BASED ON RESULTS OF ADDITIONAL DESIGN REVIEWS & VERIFICATIONS.

RECOMMENDED PCB LAYOUT: TOLERANCE ±0.05  
 RECOMMENDED PCB THICKNESS: 0.80MM  
 RECOMMENDED STENCIL THICKNESS: 0.10MM

SEE SHEET 1 EC NO: S2014-0434 DRWN: JZENG CHKD: JTAN02 APPR: KHL IM	2013/11/04 2014/01/02 2014/01/27	QUALITY SYMBOLS $F_A=0$ $F_C=0$ $F_P=0$	GENERAL TOLERANCES (UNLESS SPECIFIED) mm    INCH 4 PLACES ± --- ± --- 3 PLACES ± --- ± --- 2 PLACES ± 0.20 ± --- 1 PLACE ± 0.20 ± --- 0 PLACE ± --- ± ---	DIMENSION STYLE <b>MM ONLY</b> DRAWN BY: JZENG    DATE: 2013/11/04 CHECKED BY: JTAN02    DATE: 2013/12/05 APPROVED BY: KHL IM    DATE: 2014/01/27	SCALE: NTS DESIGN UNITS: METRIC THIRD ANGLE PROJECTION	TITLE: DUAL MICRO SIM FRAME 1.40 H 
	MATERIAL NO.: 1510310001 DOCUMENT NO.: SD-151031-0001 SHEET NO.: 4 OF 5					
	ANGULAR ± 3 ° DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS					
	SIZE: A3 THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION					

151031 FRAME SOLDERING AREA:   
 151032 BLOCK SIM SOLDERING AREA:   
 KEEP OUT ZONE: 



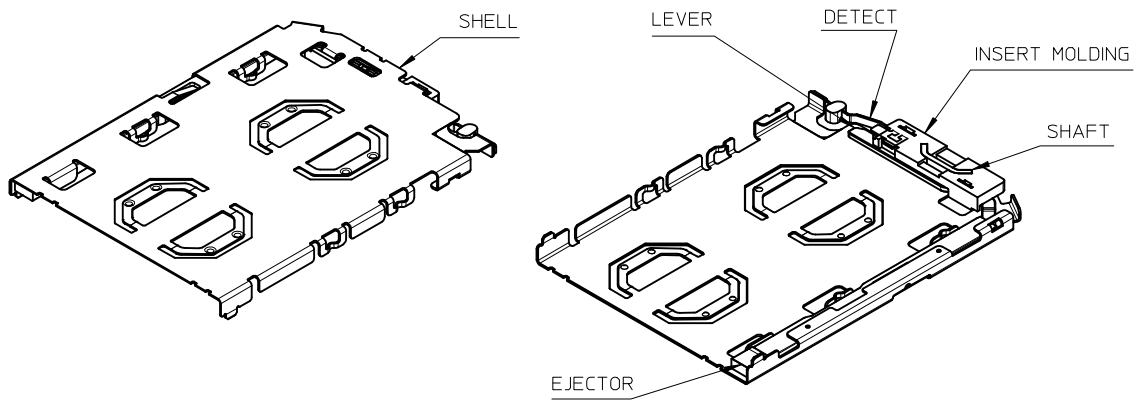
THIS DESIGN IS BASED ON DESIGN OBJECTIVES AND IS STRICTLY TENTATIVE. IT MAY CHANGE BASED ON RESULTS OF ADDITIONAL DESIGN REVIEWS & VERIFICATIONS.

RECOMMENDED PCB LAYOUT: TOLERANCE ±0.05  
 RECOMMENDED PCB THICKNESS: 0.80MM  
 RECOMMENDED STENCIL THICKNESS: 0.10MM

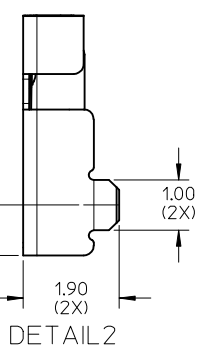
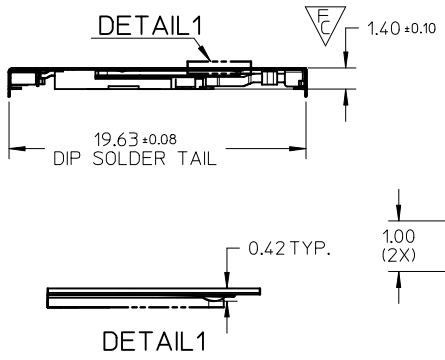
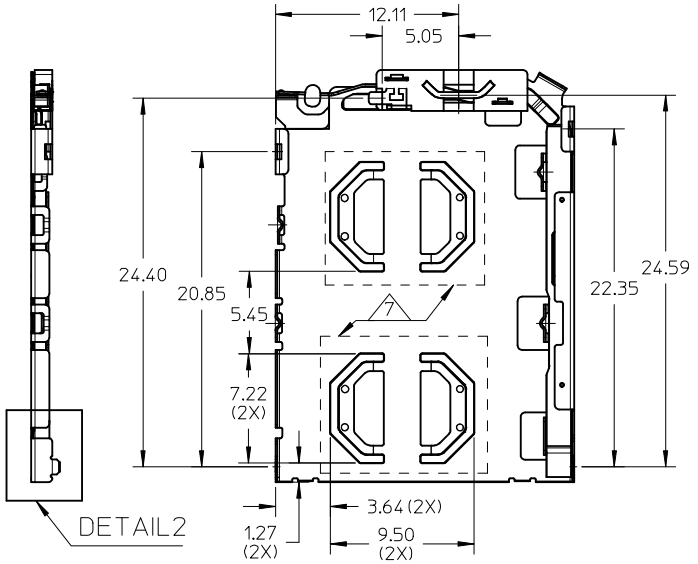
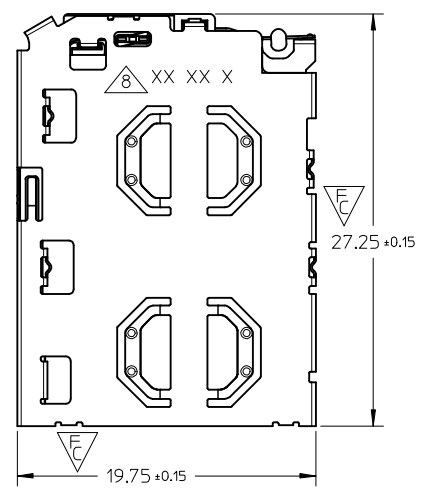
SEE SHEET 1	EC NO: S2014-0434	2013/11/04	DESCRIPTION	QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE		SCALE	DESIGN UNITS	THIRD ANGLE PROJECTION
	DRWN: JZENG	2014/01/02		▽ <sub>A</sub> =0	mm	INCH	MM ONLY	NTS	METRIC	DUAL MICRO SIM FRAME 1.40 H	
	CHKD: JTAN02	2014/01/27		▽ <sub>C</sub> =0	4 PLACES ± --- ± ---	3 PLACES ± --- ± ---	DRAWN BY: JZENG	DATE: 2013/11/04	TITLE		
	APPR: KHL IM			▽ <sub>P</sub> =0	2 PLACES ± 0.20 ± ---	1 PLACE ± 0.20 ± ---	CHECKED BY: JTAN02	DATE: 2013/12/05			
				0 PLACE ± --- ± ---	APPROVED BY: KHL IM	DATE: 2014/01/27			<b>molex</b>		
				ANGULAR ± 3 °	MATERIAL NO.	DOCUMENT NO.	SHEET NO.				
				DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS	1510310001	SD-151031-0001	5 OF 5	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION			

10 9 8 7 6 5 4 3 2 1

THIS DESIGN IS BASED ON DESIGN OBJECTIVES AND IS STRICTLY TENTATIVE. IT MAY CHANGE BASED ON RESULTS OF ADDITIONAL DESIGN REVIEWS & VERIFICATIONS.



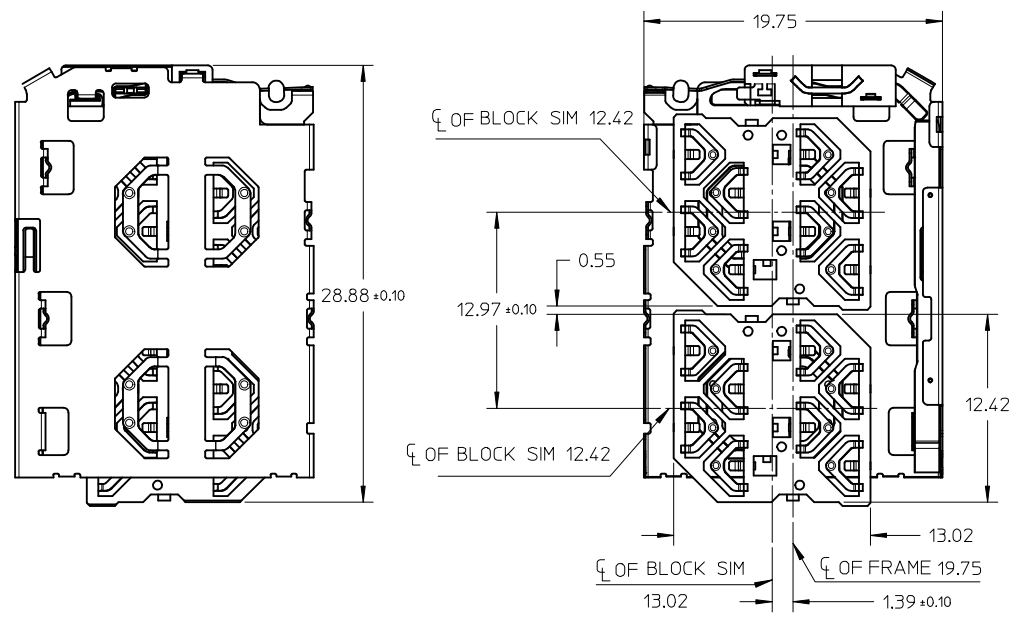
- NOTES:
- MATERIALS:  
INSERT MOLD HOUSING: LCP, UL94V-0;  
LEVER, SHAFT, EJECTOR, SHELL: STAINLESS STEEL;  
DETECT SPRING: COPPER ALLOY;
  - FINISHES:  
DETECT SPRING:  
1.27um MIN. NICKEL UNDERPLATING OVERALL;  
0.127um MIN. GOLD PLATING ON CONTACT AREA;  
1.27 um MIN. TIN PLATING ON SOLDERING TAIL;  
SHELL:  
1.27um MIN NICKEL UNDERPLATING OVERALL;  
0.025um MIN GOLD PLATING ON CONTACT AREA AND SOLDERING AREA;  
SHAFT: 1.27um MIN TIN ON SOLDERING TAIL;
  - PRODUCT SPECIFICATION: PS-151031-001;
  - PACKAGING SPECIFICATION: PK-151031-001;PK-151032-001
  - SOLDER TAIL COPLANARITY: 0.10 MM MAX BEFORE REFLOW
  - THIS PART IS A FRAME ONLY, IT SHOULD BE USED TOGETHER WITH 0.35MM BLOCK SIM 151032 FOR AN ENTIRE SIM POP OUT SYSTEM;
  - 0.10 MINIMUM KEEP OUT ZONE FROM TOP SURFACE OF SHELL DURING INSERTION AND WITHDRAWAL OF TRAY (WITH SIM CARD)
  - DATE CODE PRINTED: XX XX X  
    - DAY
    - WEEK
    - YEAR



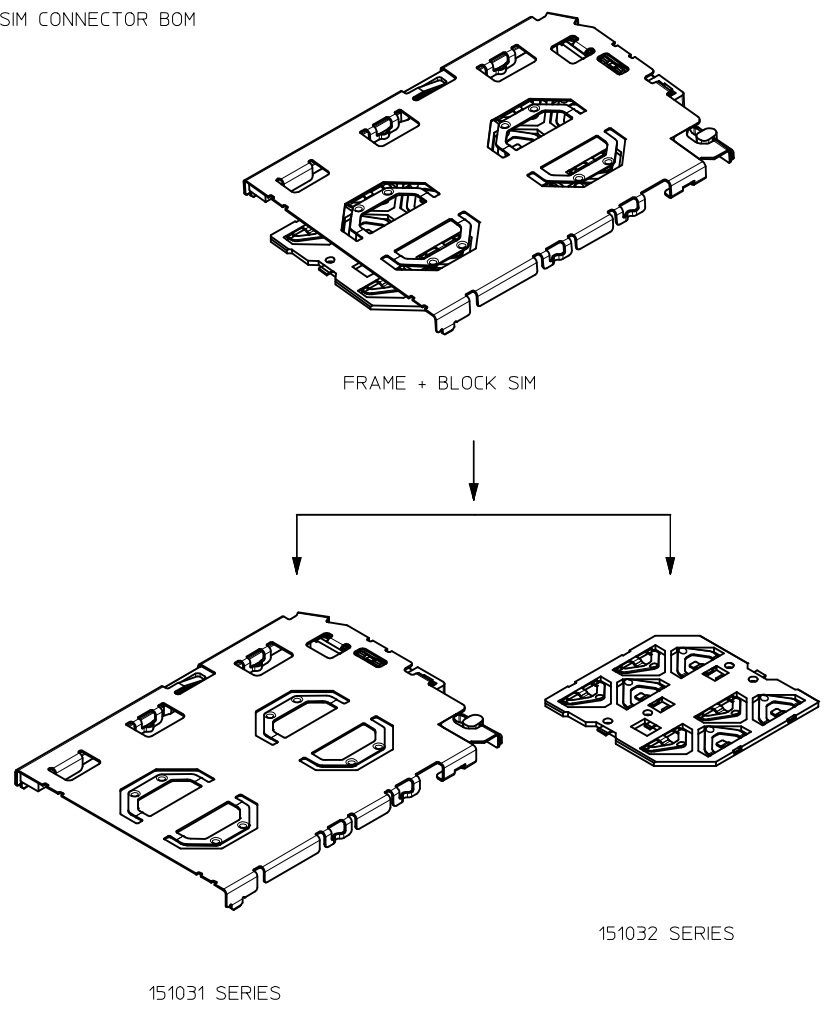
UPDATED DRAWING EC NO: S2014-0434 DRWN: JZENG 2013/12/13 CHKD: JTAN02 2014/01/02 APPR: KHLIM 2014/01/27	QUALITY SYMBOLS F <sub>A</sub> =0 F <sub>C</sub> =4 F <sub>P</sub> =0	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE	SCALE	DESIGN UNITS	THIRD ANGLE PROJECTION	
				MM ONLY	NTS	METRIC		
				DRAWN BY	DATE	TITLE		
				CHECKED BY	DATE	DUAL MICRO SIM FRAME 1.40H		
		APPROVED BY		DATE				
		KHLIM		2014/01/27				
		MATERIAL NO.		DOCUMENT NO.	SHEET NO.			
		1510310001		SD-151031-0002	1 OF 4			
		SIZE		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION				
		A3						

9 8 7 6 5 4 3 2 1

SIM CONNECTOR  
(WITH 151032 BLOCK SIM CONNECTOR)



SIM CONNECTOR BOM



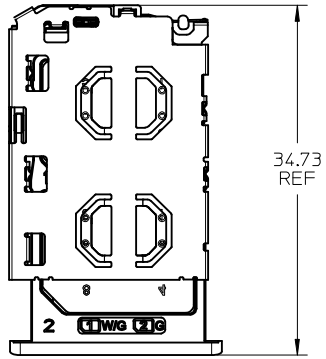
THIS DESIGN IS BASED ON DESIGN OBJECTIVES AND IS STRICTLY TENTATIVE. IT MAY CHANGE BASED ON RESULTS OF ADDITIONAL DESIGN REVIEWS & VERIFICATIONS.

SEE SHEET 1	EC NO: S2014-0434	2013/12/13
	DRWN: JZENG	2014/01/02
	CHKD: JIAN02	2014/01/02
	APPR: KHL IM	2014/01/27
REV	DESCRIPTION	

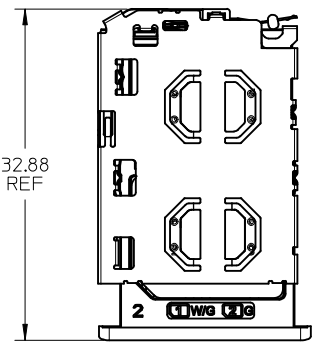
QUALITY SYMBOLS	$F_{A=0}$		
	$F_{C=0}$		
GENERAL TOLERANCES (UNLESS SPECIFIED)		mm	INCH
	4 PLACES	± ---	± ---
	3 PLACES	± ---	± ---
	2 PLACES	± 0.20	± ---
	1 PLACE	± 0.20	± ---
0 PLACE	± ---	± ---	
ANGULAR ± 3 °			
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS			

DIMENSION STYLE		SCALE		DESIGN UNITS		THIRD ANGLE PROJECTION	
MM ONLY		NTS		METRIC			
DRAWN BY	DATE	TITLE		DUAL MICRO SIM FRAME 1.40H			
JZENG	2013/12/13						
CHECKED BY	DATE			molex			
APPROVED BY	DATE			SD-151031-0002			
KHL IM	2014/01/27						
MATERIAL NO.	1510310001			DOCUMENT NO.		SHEET NO.	
SIZE	A3			THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION		2 OF 4	

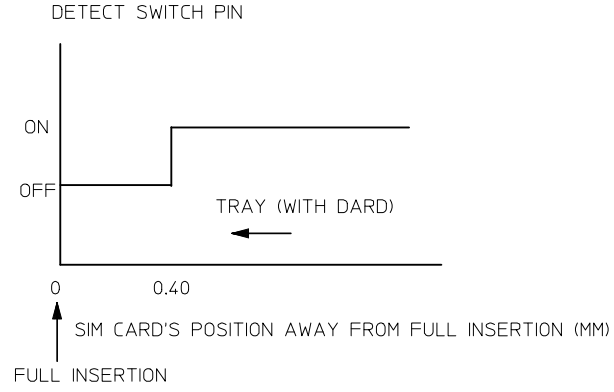
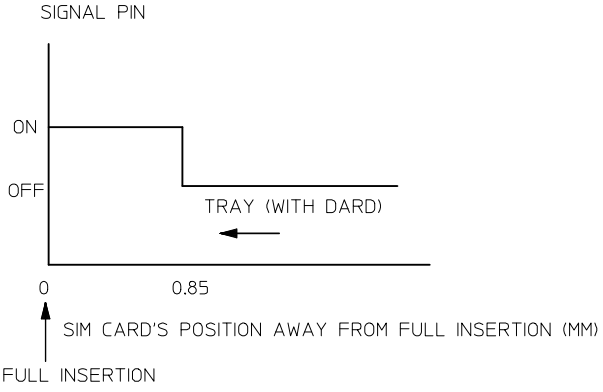
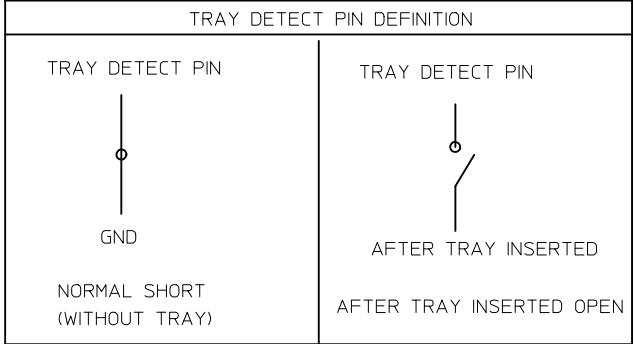
SIM CONNECTOR FRAME AND TRAY



TRAY EJECTED POSITION






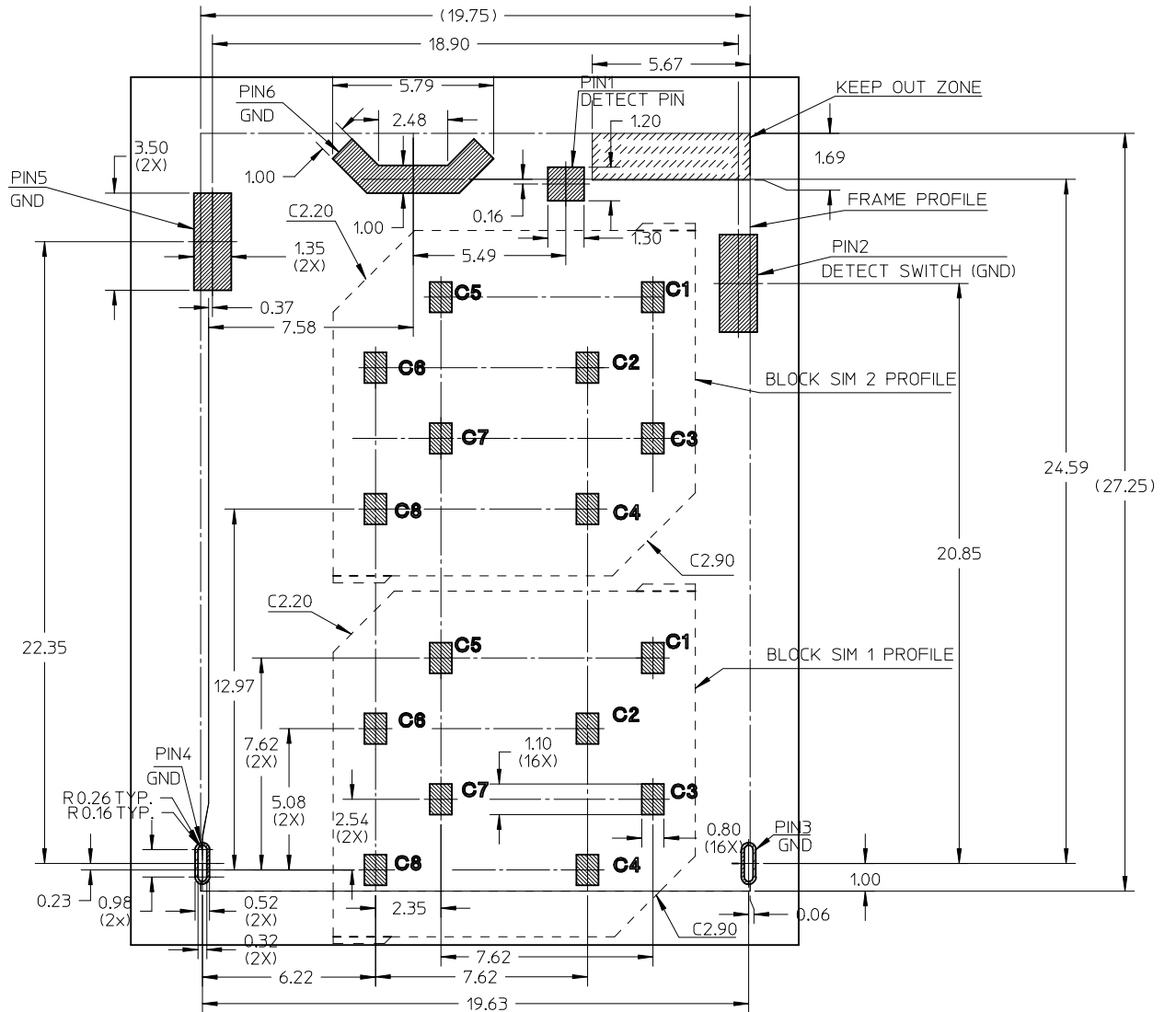
TRAY INSERTION POSITION



SEE SHEET 1 EC NO: S2014-0434 DRWN: JZENG CHKD: JIAN02 APPR: KHL IM	2013/12/13 2014/01/02 2014/01/27	DESCRIPTION F <sub>A</sub> =0 F <sub>G</sub> =0 F <sub>P</sub> =0	QUALITY SYMBOLS		GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE <b>MM ONLY</b>		SCALE	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION				
					mm	INCH	DRAWN BY	DATE	TITLE						
					4 PLACES	± ---	± ---	JZENG	2013/12/13	DUAL MICRO SIM FRAME 1.40H					
					3 PLACES	± ---	± ---	CHECKED BY	DATE						
		2 PLACES	± 0.20	± ---	APPROVED BY		DATE	<b>molex</b>							
		1 PLACE	± 0.20	± ---	KHL IM		2014/01/27								
		0 PLACE	± ---	± ---	MATERIAL NO.		DOCUMENT NO.	SHEET NO.							
		ANGULAR ± 3 °		1510310001		SD-151031-0002		3 OF 4							
		DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		SIZE A3		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION									




151031 FRAME SOLDERING AREA:   
 151032 BLOCK SIM SOLDERING AREA:   
 KEEP OUT ZONE: 



THIS DESIGN IS BASED ON DESIGN OBJECTIVES AND IS STRICTLY TENTATIVE. IT MAY CHANGE BASED ON RESULTS OF ADDITIONAL DESIGN REVIEWS & VERIFICATIONS.

RECOMMENDED PCB LAYOUT: TOLERANCE ±0.05  
 RECOMMENDED PCB THICKNESS: 0.80MM  
 RECOMMENDED STENCIL THICKNESS: 0.10MM

SEE SHEET 1 EC NO: S2014-0434 DRWN: JZENG CHKD: JTAN02 APPR: KHLIM	2013/12/13 2014/01/02 2014/01/27	QUALITY SYMBOLS $F_A=0$ $F_G=0$ $F_P=0$	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE	SCALE	DESIGN UNITS	THIRD ANGLE PROJECTION				
					MM ONLY	NTS	METRIC					
					mm	INCH	DRAWN BY	DATE	TITLE			
					4 PLACES ± --- ± ---	3 PLACES ± --- ± ---	JZENG	2013/12/13	DUAL MICRO SIM FRAME 1.40H			
		2 PLACES ± 0.20 ± ---	1 PLACE ± 0.20 ± ---	CHECKED BY	DATE							
		0 PLACE ± --- ± ---		APPROVED BY	DATE							
		ANGULAR ± 3 °		KHLIM	2014/01/27	MATERIAL NO.		DOCUMENT NO.				
		DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		1510310001		SD-151031-0002		SHEET NO. 4 OF 4				
				THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION								

## Данный компонент на территории Российской Федерации

### Вы можете приобрести в компании MosChip.

Для оперативного оформления запроса Вам необходимо перейти по данной ссылке:

<http://moschip.ru/get-element>

Вы можете разместить у нас заказ для любого Вашего проекта, будь то серийное производство или разработка единичного прибора.

В нашем ассортименте представлены ведущие мировые производители активных и пассивных электронных компонентов.

Нашей специализацией является поставка электронной компонентной базы двойного назначения, продукции таких производителей как XILINX, Intel (ex.ALTERA), Vicor, Microchip, Texas Instruments, Analog Devices, Mini-Circuits, Amphenol, Glenair.

Сотрудничество с глобальными дистрибьюторами электронных компонентов, предоставляет возможность заказывать и получать с международных складов практически любой перечень компонентов в оптимальные для Вас сроки.

На всех этапах разработки и производства наши партнеры могут получить квалифицированную поддержку опытных инженеров.

Система менеджмента качества компании отвечает требованиям в соответствии с ГОСТ Р ИСО 9001, ГОСТ РВ 0015-002 и ЭС РД 009

### Офис по работе с юридическими лицами:

105318, г.Москва, ул.Щербаковская д.3, офис 1107, 1118, ДЦ «Щербаковский»

Телефон: +7 495 668-12-70 (многоканальный)

Факс: +7 495 668-12-70 (доб.304)

E-mail: [info@moschip.ru](mailto:info@moschip.ru)

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moschip.ru

moschip.ru\_4

moschip.ru\_6

moschip.ru\_9