

Features and Benefits

- Low cost
- High sensitivity Hall Sensor
- Locked rotor protection and auto-restart
- Power-efficient CMOS and power MOSFETs
- Built-in output protection clamping diode
- Integrated tachometer (US62) or alarm (US63) signal protected output
- 2-coil fan driver with FG/RD output in 4-pin VK package RoHS Compliant

Application Examples

- 5/12V fans
- Peak current up to 700mA

Ordering Code

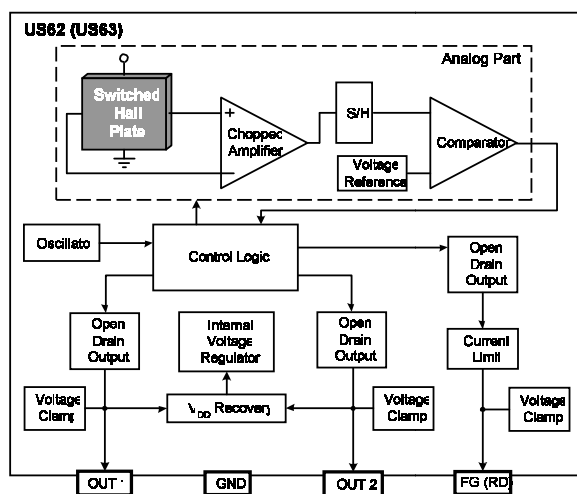
Product Code	Temperature Code	Package Code	Option Code	Packing Form Code
US63	E	VK	AAA-000	BU
US62	E	VK	AAA-000	BU

Legend:

Temperature Code: E for Temperature Range -40 °C to 85 °C
 Package Code: VK for Plastic Single in Line
 Option Code: xxx-000: standard Version
 Packing Form: BU for Bulk

Ordering example: US63EVK-AAA-000-BU

1 Functional Diagram



2 General Description

The US62/63 is a one-chip solution for driving two-coil brushless DC cooling fans.

Based on the advanced Melexis CMOS process, the IC contains a Hall-effect sensor, dynamic offset correction and low side output drivers.

Frequency Generator or Rotation Detection is available. The open-drain output makes easier the connectivity with any external interface such as hardware monitoring or Super I/O IC.

These features are combined with the Melexis patented no- V_{DD} design to fit the IC in a small 4-pin VK package.

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3 Glossary of Terms

Two-coil fan	A fan with two-coil windings where current alternates from 1 coil to the other depending on the direction of the magnetic field.
MilliTesla (mT), Gauss	Units of magnetic flux density : 1mT = 10 Gauss
V _{DD}	Voltage on the common point of the fan coils.
I _{DD}	Current supplying the chip which flows through the coil connected to the switched off output driver.
Peak output current	The current flowing in the coil at start-up, only limited by the coil resistance R _{COIL} and the output driver resistance R _{DSON} .
Continuous output current	The current flowing in the coil when the fan is spinning normally.
Locked rotor	The state when the fan stopped spinning due to mechanical blockage.
FG	Frequency generator or tachometer output
RD	Rotation detection or alarm output
LFPM	Linear Feet Per Minute – Unit of airflow velocity

4 Absolute Maximum Ratings

Parameter	Symbol	Value	Units
Fan Supply Voltage	V _{DD}	18	V
Peak Output Current	I _{OUTp}	700	mA
Continuous Output Current	I _{OUTc}	250	mA
Operating Temperature Range	T _A	-40 to 85	°C
Junction Temperature	T _J	125	°C
Storage Temperature Range	T _S	-55 to 150	°C
Magnetic Flux Density	B	Unlimited	mT
ESD Sensitivity (AEC Q100 002)	-	2	kV

Table 1: Absolute maximum ratings

Exceeding the absolute maximum ratings may cause permanent damage. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

5 Pin Definitions and Descriptions



Pin Name	Pin number	Function
FG (RD)	1	Frequency Generator (Rotation Detection) open drain output signal
OUT1	2	Open Drain Coil Driver 1
OUT2	3	Open Drain Coil Driver 2
GND	4	Ground pin

Table 2: Pin definitions and descriptions US62 (US63)

6 General Electrical Specifications

DC Operating Parameters $T_A = 25^\circ\text{C}$, $V_{DD} = 5\text{V to } 12\text{V}$ (unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Fan Supply Voltage	V_{DD}	Operating – $R_{COIL} = 50\Omega$	3.2 ⁽¹⁾		18	V
Supply Current	I_{DD}			2	4	mA
OUT1, OUT2 ON Resistance	$R_{DSON\ 1,2}$	$V_{DD} = 5\text{V}$		3.5		Ω
OUT1, OUT2 ON Resistance	$R_{DSON\ 1,2}$	$V_{DD} = 12\text{V}$		2.5		Ω
OUT1, OUT2 Output Clamp Voltage	$V_{OUT1,2}$		36			V
FG / RD Output Low Voltage	V_{OL}	$I_{OL} = 10\text{mA}$		250	500	mV
FG / RD Output Clamp Voltage	V_{CLMP}		18			V
FG / RD Output Leakage Current	I_{LEAK}	$V_{FG} (V_{RD}) = 18\text{V}$			10	μA
FG / RD Output Current Limit	I_{FGLIM}	$V_{FG} (V_{RD}) = 12\text{V}$		20		mA
Package Thermal Resistance	R_{TH}	One-sided PCB, zero LFPM		200		$^\circ\text{C/Watt}$
Locked Rotor Period	T_{ON}			0.22		S
Locked Rotor Period	T_{OFF}			1.3		S
Output Switching Delay	T_{DELAY}	“Dead time” when both drivers are off		100		us

Table 3: Electrical specifications

Note 1: The minimal value of V_{DD} should be determined using the following equation:

$$V_{DD} = 3\text{V} + R_{COIL} * I_{DD}$$

7 Magnetic Characteristics

DC Operating Parameters $T_A = 25^\circ\text{C}$, $V_{DD} = 12\text{V}$ (unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Operate point	B_{OP}			3	6	mT
Release point	B_{RP}		-6	-3		mT
Hysteresis	B_{HYST}		2	6		mT

Table 4: Magnetic specifications

8 Driver Output vs Magnetic Pole

Parameter	Test Conditions	OUT1	OUT2
North pole	$B < B_{RP}$	High	Low
South pole	$B > B_{OP}$	Low	High

Table 5: Driver output vs magnetic pole

Note 1: The magnetic pole is applied facing the branded side of the package

9 Detailed General Description

The US62/63 is a one-chip solution for driving two-coil brushless DC fans. Based on advanced Melexis CMOS process, the IC contains a Hall-effect sensor, dynamic offset correction and low side output drivers.

The output drivers OUT1 and OUT2 are fully protected against switching transients. So there is no need for an external zener diode to cut the high voltage spikes induced by the fan coils.

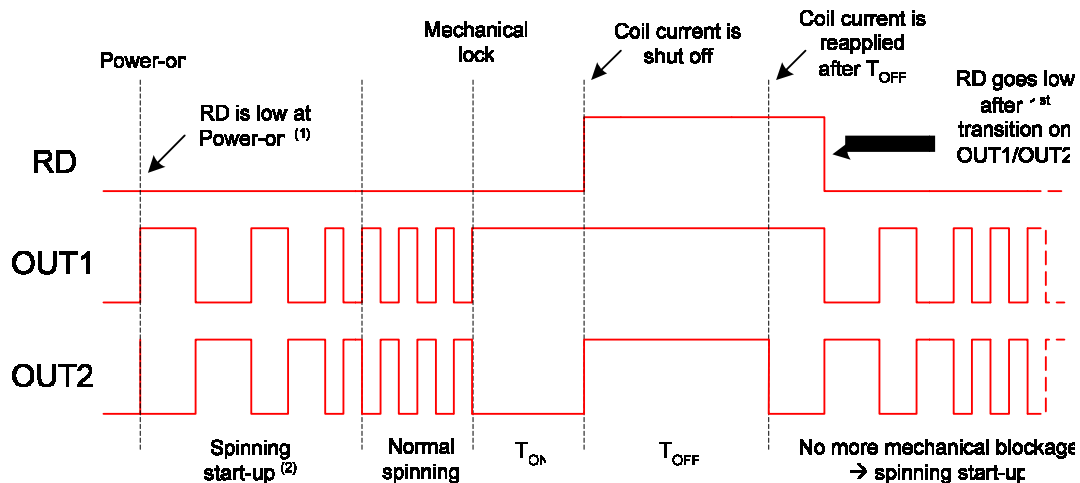
The US62 has an open-drain integrated tachometer FG output that follows the Hall signal. In the US63, the open-drain rotation detection output RD is active low during normal spinning of the motor. It goes high when the flux switching frequency becomes too low, which means the motor is blocked.

10 Unique Features

The FG/RD output driver provides an over current protection limiting the current flowing in the FG/RD driver while switched ON. It can occur if a too small pull-up resistor is used or if this resistor is short connected. This protection helps avoid EOS damage on the FG/RD driver.

The absence of a V_{DD} pin enables the two low side output drivers and FG/RD signal output to fit in a four-pin VK package. The lack of a V_{DD} pin decreases also the probability to damage the chip due to reverse voltage connection, using the coil resistance to limit the reverse current. In this condition, the total reverse current is twice the peak current value of the fan used.

The built-in locked-rotor protection automatically shuts off the coil current when the rotor is mechanically blocked, or the rotational speed becomes too low. The fan tries to restart every 1.3 seconds until the rotation of the shaft is allowed again. The On / Off cycling reduces the average current to 1/7 normal. It is enough to prevent overheating or damage to most fans.



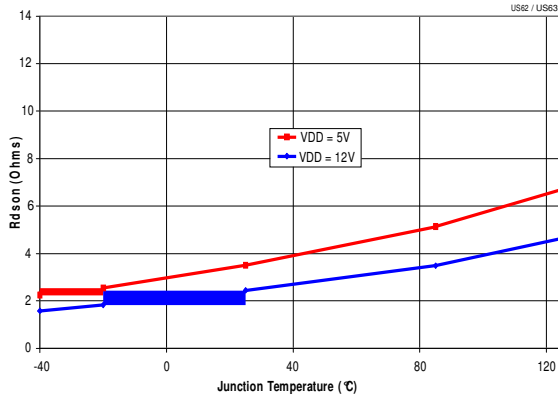
Note 1 : At Power-on, RD is low. If no transition occurs on OUT1/OUT2 before T_{ON} time, RD goes high.

Note 2 : The spinning start-up is the ramp between the zero speed and maximal speed. It depends on the motor characteristics.

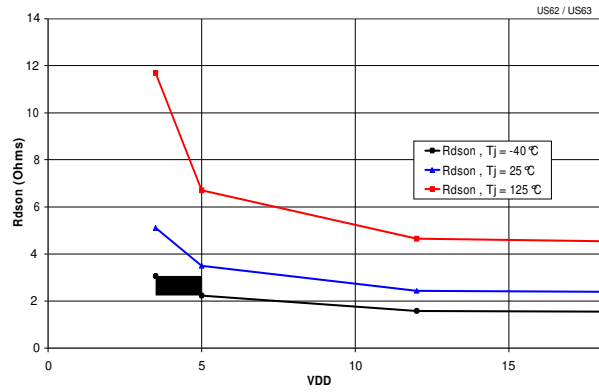
Note 3 : This graph does not represent the EMF effect.

11 Performance Graphs

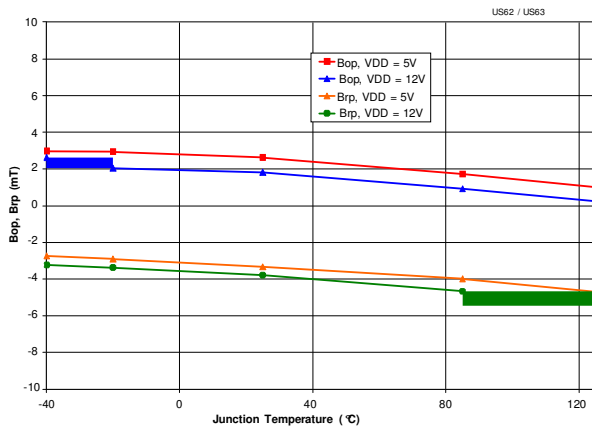
11.1 R_{DS(on)} vs T_J



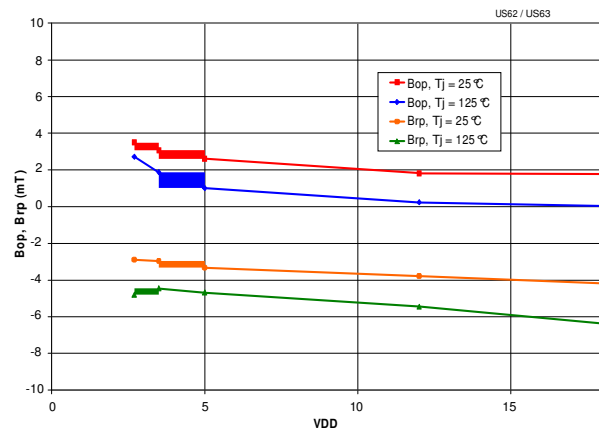
11.2 R_{DS(on)} vs V_{DD}



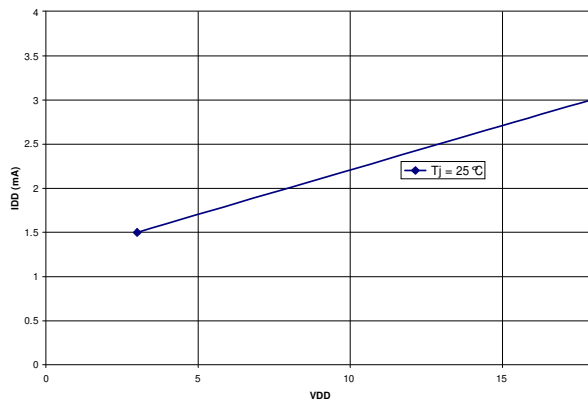
11.3 Magnetic Parameters vs T_J



11.4 Magnetic Parameters vs V_{DD}

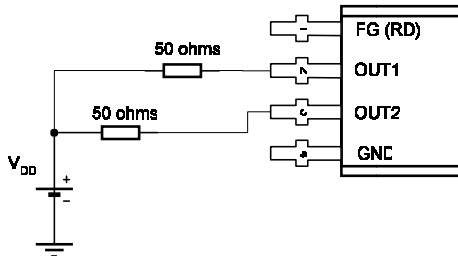


11.5 I_{DD} vs V_{DD}



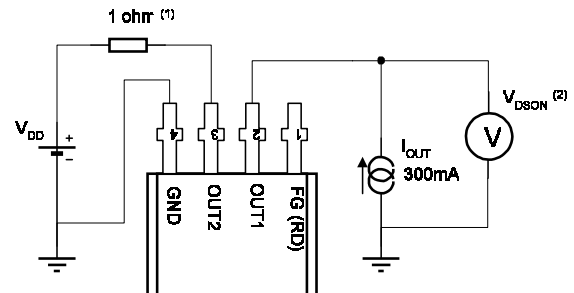
12 Test conditions

12.1 V_{DD} – operating



Note 1 - "Operating" states the condition when all the chip functions are working normally

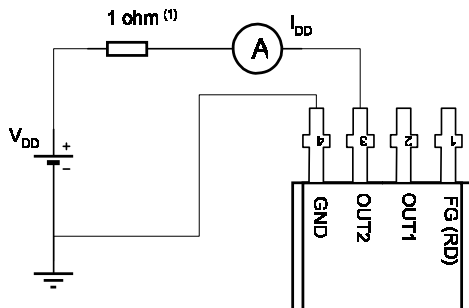
12.2 R_{DSON}



Note 1 - The chip is triggered to have OUT2 = high and OUT1 = low. A small resistor value is used to have V_{DD} equal to V_{OUT2}

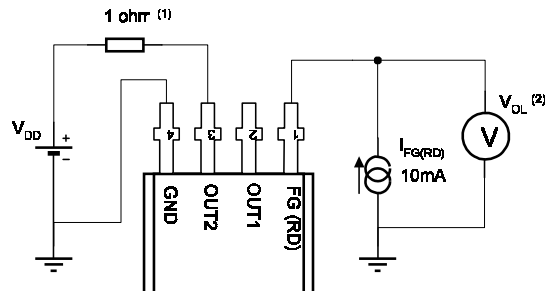
Note 2 - The output driver resistance R_{DSON} is calculated via the relation:
 $R_{DSON} = V_{DSON} / I_{OUT}$

12.3 I_{DD}



Note 1 - The chip is triggered to have OUT2 = high and OUT1 = low. A small resistor value is used to have V_{DD} equal to V_{OUT2}

12.4 FG/RD Output Low Voltage

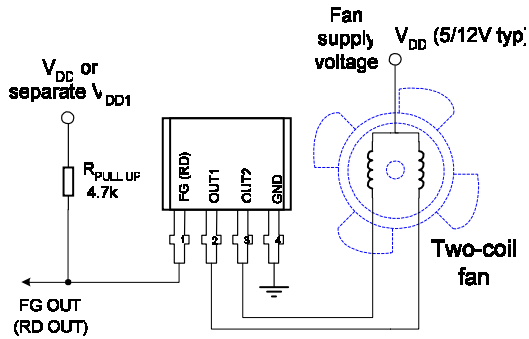


Note 1 - The chip is triggered to have OUT2 = high and OUT1 = low. A small resistor value is used to have V_{DD} equal to V_{OUT2}

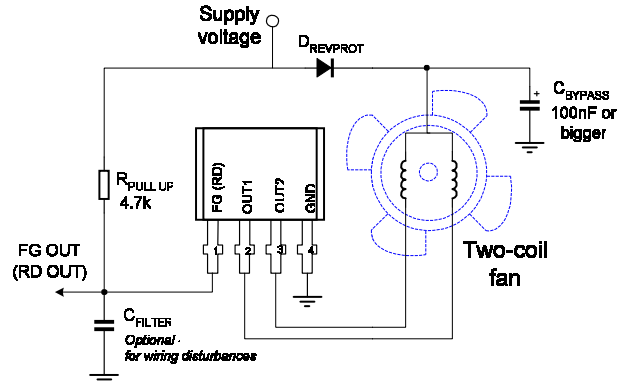
Note 2 - The FG/RD output low voltage is measured during the first T_{DN} period at the power-on event.

13 Application Information

13.1 Typical application with pull-up resistor



13.2 Configuration for reverse voltage / ground hot-switch protection



14 Application Comments

Figure 13.2 shows the application including several external protections.

The diode $D_{REVPROT}$ best protects the chip and fan coils for reverse voltage condition.

The capacitor C_{BYPASS} is recommended if the supply ground may be disconnected while running even though the positive supply point is still connected. This capacitor provides a path to ground for the remaining energy in the coil which has to be evacuated. Without it, the energy can flow from OUT1/OUT2 to the FG/RD driver, without any limitation and may cause EOS damage.

Its value has to be tuned depending on the coils characteristics. Bigger inductance requires bigger capacitor.

The optional capacitor C_{FILTER} suppresses wire disturbances on the FG/RD output line.

15 Standard information regarding manufacturability of Melexis products with different soldering processes

Our products are classified and qualified regarding soldering technology, solderability and moisture sensitivity level according to following test methods:

Reflow Soldering SMD's (Surface Mount Devices)

- IPC/JEDEC J-STD-020
Moisture/Reflow Sensitivity Classification for Nonhermetic Solid State Surface Mount Devices (classification reflow profiles according to table 5-2)
- EIA/JEDEC JESD22-A113
Preconditioning of Nonhermetic Surface Mount Devices Prior to Reliability Testing (reflow profiles according to table 2)

Wave Soldering SMD's (Surface Mount Devices) and THD's (Through Hole Devices)

- EN60749-20
Resistance of plastic- encapsulated SMD's to combined effect of moisture and soldering heat
- EIA/JEDEC JESD22-B106 and EN60749-15
Resistance to soldering temperature for through-hole mounted devices

Iron Soldering THD's (Through Hole Devices)

- EN60749-15
Resistance to soldering temperature for through-hole mounted devices

Solderability SMD's (Surface Mount Devices) and THD's (Through Hole Devices)

- EIA/JEDEC JESD22-B102 and EN60749-21
Solderability

For all soldering technologies deviating from above mentioned standard conditions (regarding peak temperature, temperature gradient, temperature profile etc) additional classification and qualification tests have to be agreed upon with Melexis.

The application of Wave Soldering for SMD's is allowed only after consulting Melexis regarding assurance of adhesive strength between device and board.

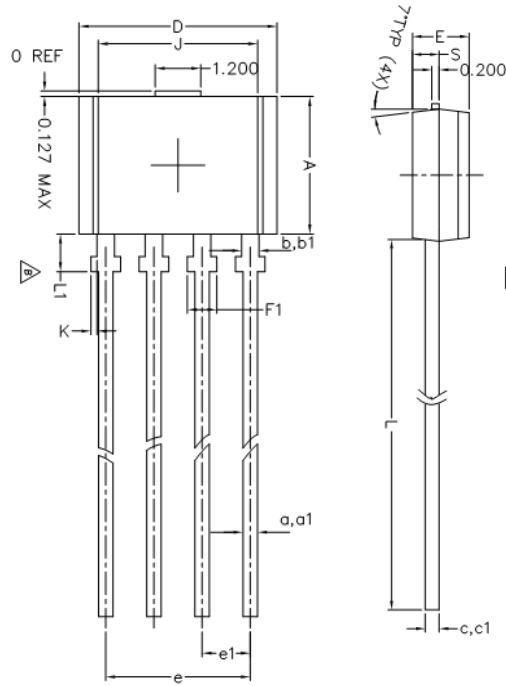
Melexis is contributing to global environmental conservation by promoting **lead free** solutions. For more information on qualifications of **RoHS** compliant products (RoHS = European directive on the Restriction Of the use of certain Hazardous Substances) please visit the quality page on our website:

<http://www.melexis.com/quality.aspx>

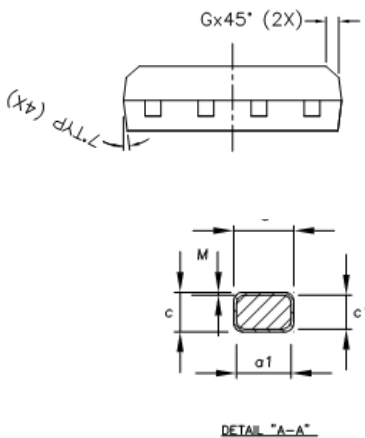
16 ESD Precautions

Electronic semiconductor products are sensitive to Electro Static Discharge (ESD). Always observe Electro Static Discharge control procedures whenever handling semiconductor products.

17 VK Package Information



SYMBOL	DIMENSIONS [MM/INCH]			SYMBOL	DIMENSIONS [MM/INCH]		
	MIN	NOM	MAX		MIN	NOM	MAX
A	3.55	3.65	3.75	e1	1.24	1.27	1.30
	.140	.144	.148	E	1.45	1.55	1.65
a1	0.35	-	0.41		.057	.061	.065
	.014	-	.016	J	4.10	4.20	4.30
a	0.35	-	0.44		.161	.165	.169
	.014	-	.017	L	10.20	10.50	10.80
b1	0.43	-	0.49		.402	.413	.425
	.017	-	.019	L1	0.90	1.00	1.10
b	0.43	-	0.52		.035	.039	.042
	.017	-	.020	S	0.63	0.73	0.83
c1	0.35	-	0.41		.025	.029	.033
	.014	-	.016	K	0.00	-	.015
c	0.35	-	0.44		.00	-	.006
	.014	-	.017	G	0.20	0.30	0.40
D	5.12	5.22	5.32		.008	.012	.016
	.201	.205	.209	M	-	-	0.0152
F1	0.45	-	0.66		-	-	.0006
	.018	-	.026				
e	3.78	3.81	3.84				
	.149	.150	.151				



NOTES :

1. DIMENSIONS A AND D DO NOT INCLUDE MOLD GATE AND SIDE FLASH (PROTUSION) ALLOWABLE IS 0.127 [.005] PER SIDE. A AND D ARE MAXIMUM PLASTIC BODY SIZE DIMENSIONS INCLUDING MOLD MISMATCH.
2. CONTROLLING DIMENSION IN MM.
3. a1, b1, c1 ARE DIM FOR BARE LEAD FRAMES
4. a, b, c ARE DIM FOR PLATED LEAD FRAMES.
5. b, b1 ARE LEAD WIDTH ABOVE DAMBAR.
6. a, a1 ARE LEAD WIDTH BELOW DAMBAR.

18 Disclaimer

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