

NB7VPQ701MMEVK

NB7VPQ701MMEVK Evaluation Board User's Manual



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Eval Board User's Manual

Introduction

The NB7VPQ701MMEVK evaluation board was developed to provide a convenient platform to quickly verify the operation of the NB7VPQ701M re-driver in a USB type-C system environment.

This evaluation board manual contains:

- Information on the NB7VPQ701MMEVK Evaluation Kit
- Board Schematics
- Bill of Materials

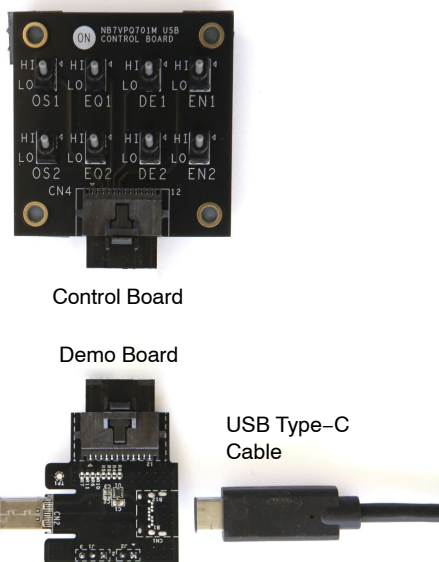


Figure 1. Kit Contents

Board Features

- Accommodates the Functional Evaluation of the NB7VPQ701M
- Acts as a Reference Design that Can Easily be Modified for Active Cables, UFP (Upstream Facing Port), DFP (Downstream Facing Port), and DRP (Dual Role Port) Applications
- Type-C Plug and Receptacle to Easily Place in the Existing System Environment
- External Control Board to Easily Change Pin Settings without Compromising Form Factor

Part Description

The NB7VPQ701M is a 1.8 V single channel re-driver for USB 3.1 applications that supports both 5 and 10 Gbps data rates. Signal integrity degrades from PCB traces, transmission cables, and inter-symbol interference (ISI). The NB7VPQ701M compensates for these losses by engaging varying levels of equalization at the input receiver. The output transmitter circuitry provides user selectable de-emphasis and output amplitude settings to create the best eye openings for the outgoing data signals. The flexibility of this part allows it to fit into many system applications.

The NB7VPQ701M features an intelligent LFPS circuit. This circuit senses the low frequency signals and automatically disables driver de-emphasis for full USB 3.1 compliances.

After power up, the NB7VPQ701M periodically checks both of the TX output pairs for a SuperSpeed USB receiver. When the receiver is detected, the RX termination becomes enabled and the NB7VPQ701M is set to perform the re-drive function.

The NB7VPQ701M comes in a small 1.6 x 1.6 mm UQFN-12 package and is specified to operate across the entire industrial temperature range, -40°C to 85°C.

This manual should be used in conjunction with the device datasheet which contains full technical details on the device specifications and operation.

NB7VPQ701MMEVK

BOARD MAP

Figure 2 illustrates the locations of major features and components. The following information will guide you through the necessarily configuration for proper operation.

The steps correspond to the numbers found in the below figure.

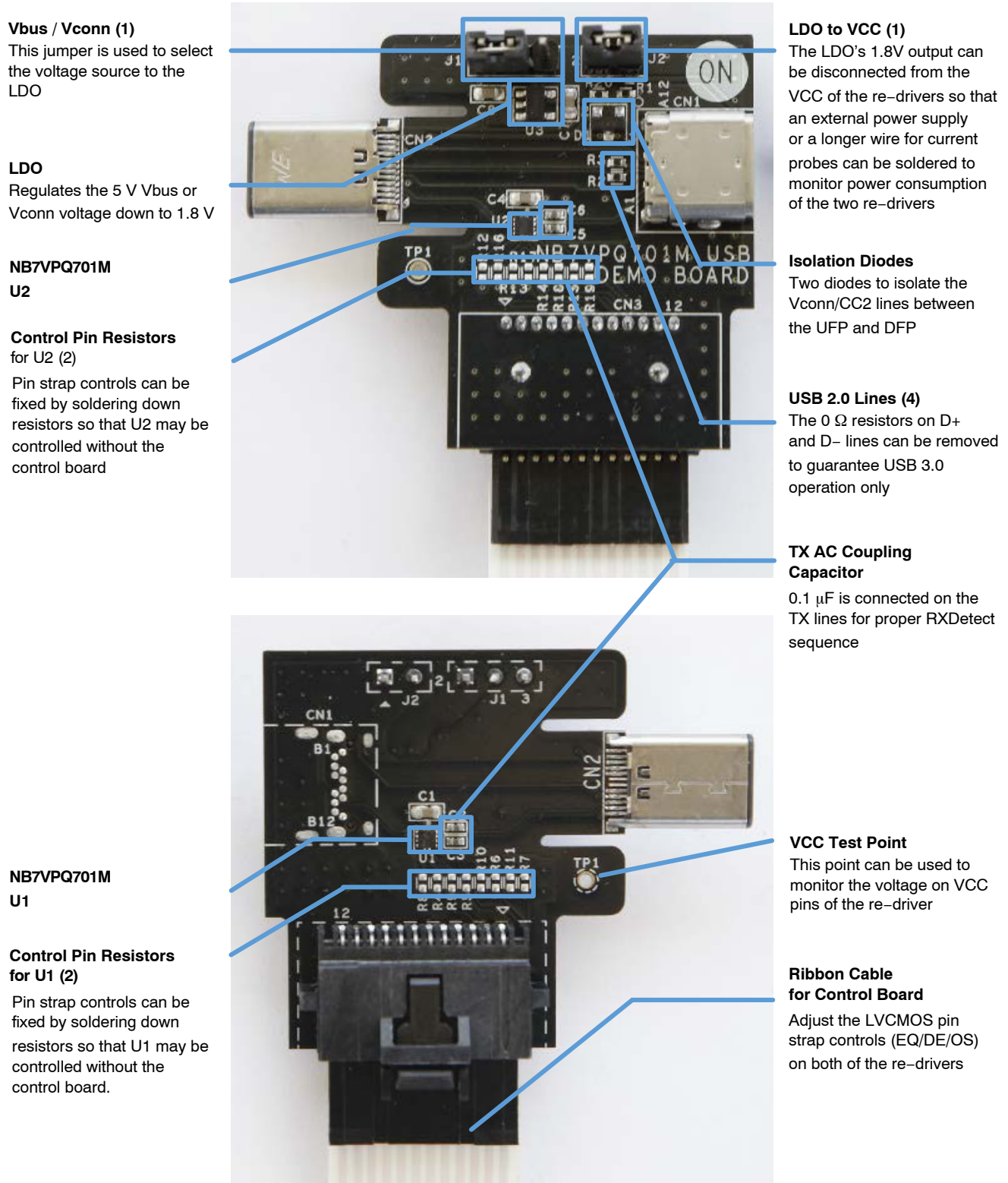
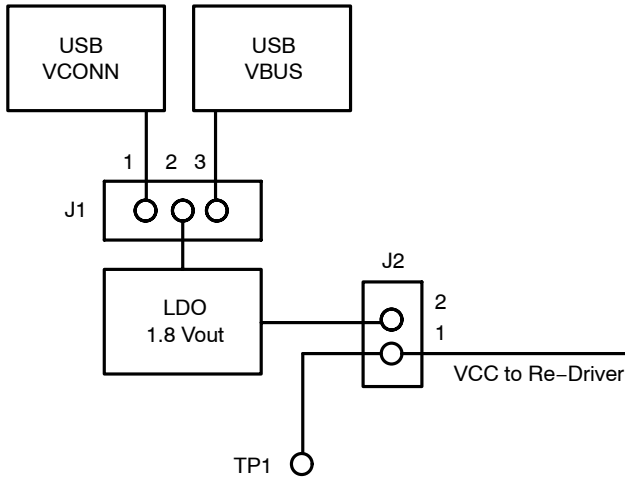


Figure 2. NB7VPQ701MMEVK Board Top and Bottom

NB7VPQ701MMEVK

Step 1: Select Power Source

The NB7VPQ701MMEVK has the flexibility to be powered through USB's VBUS, VCONN, or an external power supply. Table 1 describes the jumper settings for each of the configuration:



Step 1 Select Power Source	
VBUS or VCONN	J2 Short pins 1 and 2
External Supply	Open J2 Connect positive lead of external supply to pin 1 or TP1 and apply 1.8 V

Step 2 Select VBUS or VCONN	
VBUS	J1 Short pins 2 and 3
VCONN	J1 Short pins 1 and 2

Figure 3. Power Source Selection

Monitoring Current / Power

There are two easy ways to monitor the current consumed by the re-drivers. If you are using an external power supply, you can simply use the current meter commonly found on the equipment. If you would like to use a current probe, simply connect a long wire using clips or solder between pins 1 and 2 on J2.

Step 2: Set DC Control Pins

Each of the NB7VPQ701M re-drivers has its own enable, de-emphasis, equalization, and output swing pins that must be independently set. This can be done by soldering a 0 Ω resistor to force a setting on the un-populated pads, or by connecting the demo board to the control board with the ribbon cable and using the 3-position switches. If the pins are left floating (neither resistor is soldered nor is the control board attached) then the parts will be enabled and de-emphasis, equalization, and output swing will default to the mid-level. **The amount of de-emphasis, equalization, and output swing needed will depend on the cable length / material that is added by the user.** With longer the loss material, higher settings will be required. For minimum loss (or no loss), all pins should be set to Low.

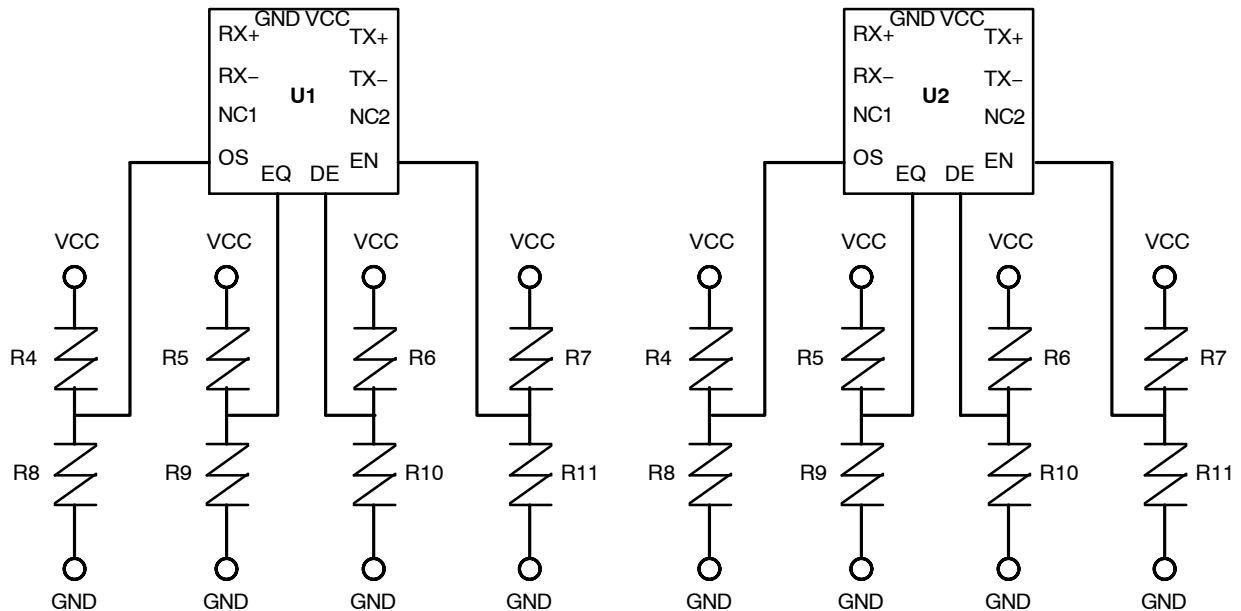


Figure 4. Resistor Pads to Set DC Control Pins

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Step 3: Connect in the USB Type-C Application

The standard (non-active, non-full featured) cable has a CC pin on the top and bottom side of the plug connector. As

seen in the USB Type-C Spec (rev 1.1) Table 3-6 USB Type-C Standard Cable Wire Assignments, only one CC pin is connected from plug to plug as seen below:



Figure 5. Standard Type-C Cable CC Line

This creates the need for the following two rules for proper operation:

1. If powering from VCONN, EVB must be placed on the DFP side of the cable.
When a USB type-C connection is detected, the CC controllers will determine the orientation by seeing which CC# pin is making a connection between the DFP and UFP. On the DFP side, the other CC# pin will look for the Ra resistor in order to power up VCONN (5V line).
If not followed: If the EVB is placed on the UFP side, due to the break in the 'other' CC line in the cable, the DFP will not be able to detect the Ra resistor. Consequently, VCONN power will not be applied to the CC2 line. Even if VCONN was somehow enabled, the EVB will not see this voltage as there is still a break in the cable.
2. The cable's CC wire must be connected to EVB's CC1 line.
As mentioned in rule 1's explanation, the CC

controllers will determine the orientation by seeing which CC# pin is making a connection between the DFP and the UFP.

If not followed: If the standard type-C cable is plugged in so that the open CC pin is connected to CC1 and the CC wire is connected to VCONN/CC2's isolation diode of the EVB, neither side will establish a connection to the CC# pin. As a result, the PD controllers will not go through the hand-shaking process and it will assume that there are no peripheral devices attached to the host. In most cases, power will be turned off on both VBUS and VCONN.

The flipping function of Type-C connector can be demonstrated between the DFP and the EVB as well as between UFP and the EVB. Since this board emulates an active cable application, the actual implementation would only function in this manner.

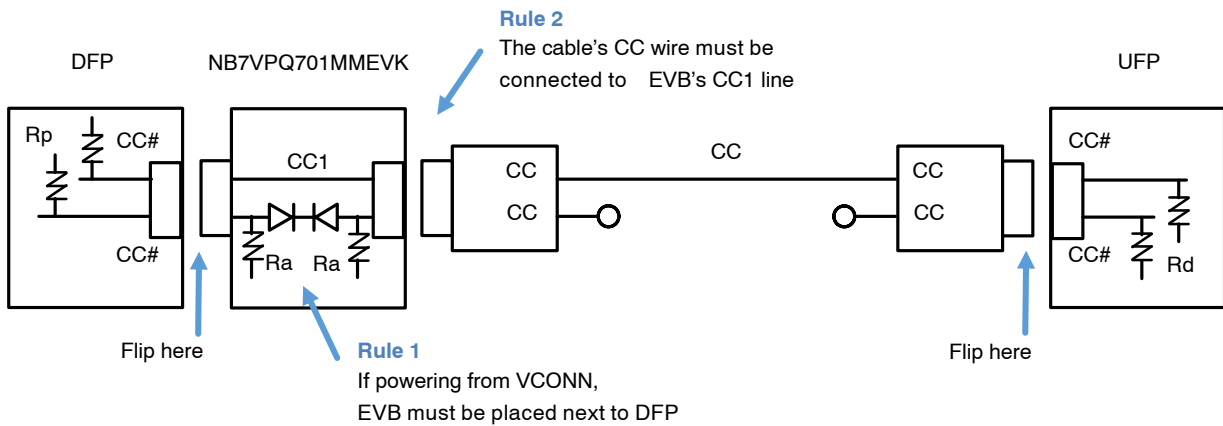


Figure 6. How to Connect the EVB to a Type-C System

Step 4: USB 2.0 Data Lines

The USB 2.0 lines can be disconnected by removing the $0\ \Omega$ resistors R2 and R3 on the D+ and D- lines. This is useful if you cannot easily tell whether the DFP has acknowledged a super speed (+) connection with the inserted loss, or if it stepped down to high speed. When a receiver is detected through RxDetect, the DFP will initialize link training. It will send a test signal out at the highest data rate and expect to see the same signal sent back by the UFP. If the signals do not match due to ISI (or any other connection issues) then it will drop down the data rate to USB 2.0 speed.

On Windows machines, an easy way to tell that a super speed connection was not established is to look for a pop-up in the task bar letting the user know that “This device can perform faster”. A disk benchmarking tool like Crystal Disk Mark that lets you test read and write speeds to a peripheral storage can also be used. To see the difference in data rate, the super speed lines can be disabled by setting the enable (EN) pins on the re-drivers to Low, and then connecting the peripheral device.

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BILL OF MATERIALS

Table 1. BILL OF MATERIALS

Quantity	Reference	Description	Value	Manufacturer	Part Number
2	U1, U2	USB 3.1 re-driver, uQFN-12	-	ON Semiconductor	NB7VPQ701M
1	CN2	USB 3.1 Type-C Plug	-	Würth Elektronik	632 712 000 011
1	CN1	USB 3.1 Type-C Receptacle	-	Mill-Max Manufacturing Corp.	898-73-024-90-310001
1	D1	Dual Diode, Common Cathode	-	ON Semiconductor	BAV70LT3G
8	Control Board Switches	Toggle Switch	-	NKK Switches	G13AP
1	U3	1.8 V Voltage Regulator	-	ON Semiconductor	NCP4625DSN18T1G
2	CN3, CN4	12 Position Right Angle Header	-	TE Connectivity	6-104074-1
1		4" 12 Position Flex Cable	-	TE Connectivity	A9CCA-1204E
1	J1	Header, 0.100"	hdr_1x03_.100	Amphenol FCI	68001-203HLF
1	J2	Header, 0.100"	hdr_1x02_.100	Amphenol FCI	77311-118-02LF
4	C1, C4, C7, C8	0603 Ceramic Cap	1 μ F		
4	C2, C3, C5, C6	0402 Ceramic Cap	0.1 μ F		
2	R20, R1	0402 Resistor	1 K Ω		
2	R2, R3	0402 Resistor	0.0 Ω		

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