



#### N-CHANNEL ENHANCEMENT MODE MOSFET

## Product Summary (Typ.@ V<sub>GS</sub> = 4.5V, T<sub>A</sub> = +25°C)

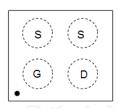
BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub>
12V	38mΩ	4.0A

## **Description**

This new generation MOSFET is engineered to minimize on-state losses and switch ultra-fast, making it ideal for high-efficiency power transfer. It uses Chip-Scale Package (CSP) to increase power density by combining low thermal impedance with minimal  $R_{\text{DS(ON)}}$  per footprint area.

## **Applications**

- DC-DC Converters
- Battery Management
- Load Switch



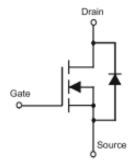
Top-View Pin Configuration

#### **Features**

- TR-MOS Technology with the Lowest R<sub>DS(ON)</sub>
- CSP with Footprint 0.81mm × 0.81mm (Typ.)
- Height = 0.29mm for Low Profile
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

#### **Mechanical Data**

- Case: X3-DSN0808-4
- Terminal Connections: See Diagram Below
- Terminal Finish: Matte Tin Annealed Over Copper Pillar (3)
- UBM: 203µm



**Equivalent Circuit** 

### Ordering Information (Note 4)

Part Number	Case	Packaging
DMN1053UCP4-7	X3-DSN0808-4	3,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## **Marking Information**

4B YM 4B = Product Type Marking Code YM = Date Code Marking Y or  $\overline{Y}$  = Year (ex: E = 2017) M or  $\overline{M}$  = Month (ex: 9 = September)

Date Code Key

Year	201	6	2017		2018	20	19	2020		2021		2022
Code	D		E		F	(	3	Н				J
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



## **Maximum Ratings**

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	$V_{DSS}$	12	V	
Gate-Source Voltage	V <sub>GSS</sub>	±8	V	
Continuous Source Current @ V <sub>GS</sub> = 4.5V (Note 5)	$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	2.7 2.2	А
Continuous Source Current @ V <sub>GS</sub> = 4.5V (Note 6)	I <sub>D</sub>	4.0 3.2	А	
Pulsed Drain Current (Pulse Duration 10µs, Duty Cycle ≤1%	I <sub>DM</sub>	8	А	
Continuous Source-Drain Diode Current	IS	0.74	A	
Pulse Diode Forward Current		I <sub>SM</sub>	15	A

#### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	$P_{D}$	0.74	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{ heta JA}$	167	°C/W
Total Power Dissipation (Note 6)	P <sub>D</sub>	1.34	W
Thermal Resistance, Junction to Ambient (Note 6)	$R_{ heta JA}$	93	°C/W
Operating and Storage Temperature Range	$T_{J_1}T_{STG}$	-55 to +150	°C

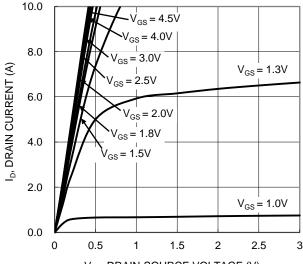
# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition			
OFF CHARACTERISTICS (Note 7)									
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	12	ı	-	V	$V_{GS} = 0V, I_{D} = 250\mu A$			
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	ı	i	1.0	μA	$V_{DS} = 9.6V, V_{GS} = 0V$			
Gate-Body Leakage	I <sub>GSS</sub>	ı	ı	±100	nA	$V_{GS} = \pm 8V, V_{DS} = 0V$			
ON CHARACTERISTICS (Note 7)									
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.35	0.5	0.7	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$			
			38	42		$V_{GS} = 4.5V, I_D = 1.0A$			
			42	50		$V_{GS} = 2.5V, I_D = 1.0A$			
Static Drain-Source On-Resistance	Paggan	_	45	53	mΩ	$V_{GS} = 2.1V, I_D = 1.0A$			
Static Dialii-Source On-Nesistance	R <sub>DS(ON)</sub>	=	49	65	11152	$V_{GS} = 1.8V, I_D = 0.5A$			
			57	80		$V_{GS} = 1.5V, I_D = 0.2A$			
			82	110		$V_{GS} = 1.2V, I_D = 0.1A$			
Forward Transfer Admittance	Y <sub>fs</sub>	-	6.0	-	S	$V_{DS} = 6V, I_{S} = 1.0A$			
Body Diode Forward Voltage	$V_{SD}$	-	0.7	1	V	$V_{GS} = 0V, I_{S} = 1.0A$			
DYNAMIC CHARACTERISTICS (Note 8)									
Input Capacitance	$C_{iss}$	-	612	908	pF	$V_{DS} = 6V$ , $V_{GS} = 0V$ .			
Output Capacitance	Coss	-	91	127	pF	-f = 1.0MHz			
Reverse Transfer Capacitance	$C_{rss}$	-	84	126	pF	· · · · · · · · · · · · · · · · · · ·			
Gate Resistance	$R_g$	-	1.3	2.6	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$			
Total Gate Charge	$Q_g$	-	7.2	15	nC				
Gate-Source Charge	$Q_{gs}$	-	0.6	-	nC	$V_{GS} = 4.5V, V_{DS} = 6V,$ $I_{D} = 1.0A$			
Gate-Drain Charge	$Q_gd$	-	1.3	-	nC	ID = 1.0A			
Turn-On Delay Time	t <sub>D(ON)</sub>	-	3.6	10	ns				
Turn-On Rise Time	t <sub>R</sub>	•	6.0	14	ns	$V_{DD} = 6V, I_{D} = 1.0A$			
Turn-Off Delay Time	t <sub>D(OFF)</sub>	•	13.5	32	ns	$V_{GEN}=4.5V,~R_G=1\Omega,~R_L=6\Omega$			
Turn-Off Fall Time	t <sub>F</sub>	П	2	4	ns				
Reverse Recovery Charge	Q <sub>RR</sub>	-	0.7	1.5	nC	I <sub>F</sub> = 1A, di/dt = 100A/µs			
Body Diode Reverse Recovery Time	t <sub>RR</sub>	-	6.4	14	ns	11- = 1A, αι/αι = 100A/μ5			

Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.

<sup>8.</sup> Guaranteed by design. Not subject to production testing.





V<sub>DS</sub>, DRAIN-SOURCE VOLTAGE (V) Figure 1. Typical Output Characteristic

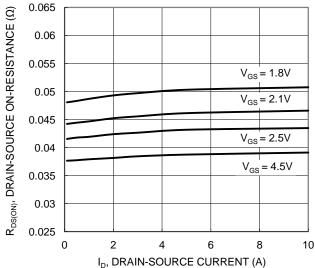


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

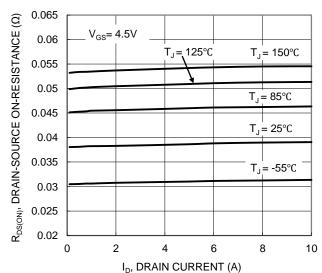
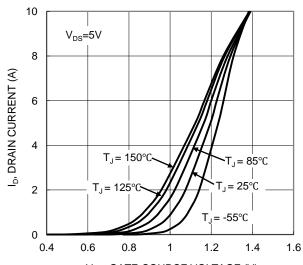
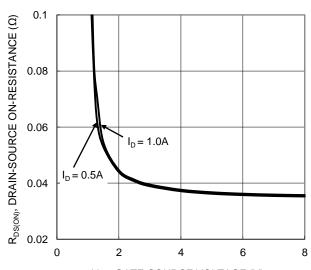


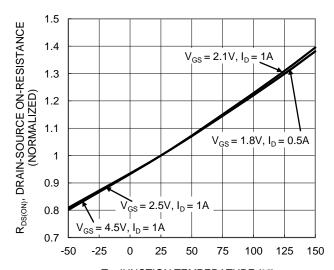
Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature



V<sub>GS</sub>, GATE-SOURCE VOLTAGE (V) Figure 2. Typical Transfer Characteristic

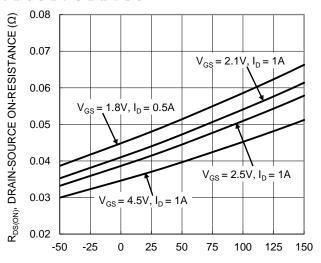


V<sub>GS</sub>, GATE-SOURCE VOLTAGE (V) Figure 4. Typical Transfer Characteristic

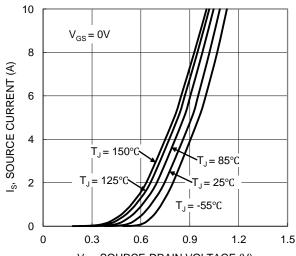


T<sub>J</sub>, JUNCTION TEMPERATURE (°C) Figure 6. On-Resistance Variation with Junction Temperature





T<sub>J</sub>, JUNCTION TEMPERATURE (°C) Figure 7. On-Resistance Variation with Junction Temperature



V<sub>SD</sub>, SOURCE-DRAIN VOLTAGE (V) Figure 9. Diode Forward Voltage vs. Current

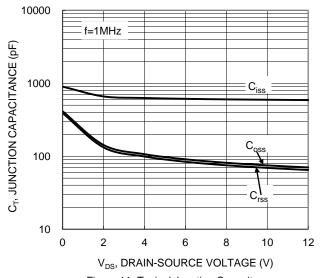
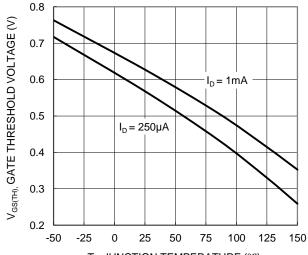
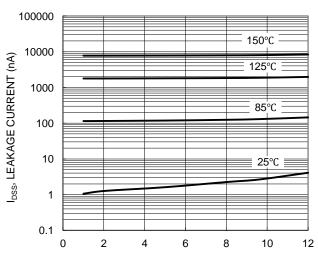


Figure 11. Typical Junction Capacitance



T<sub>J</sub>, JUNCTION TEMPERATURE (°C) Figure 8. Gate Threshold Variation vs. Junction Temperature



V<sub>DS</sub>, DRAIN-SOURCE VOLTAGE (V) Figure 10. Typical Drain-Source Leakage Current vs. Voltage

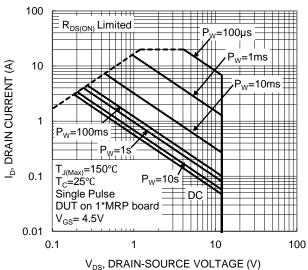


Figure 12. SOA, Safe Operation Area



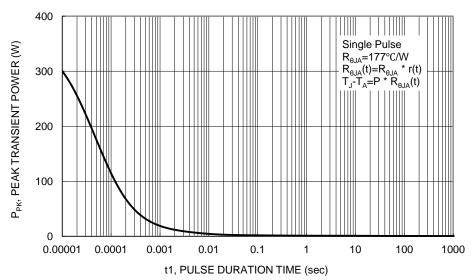


Figure 13. Single Pulse Maximum Power Dissipation

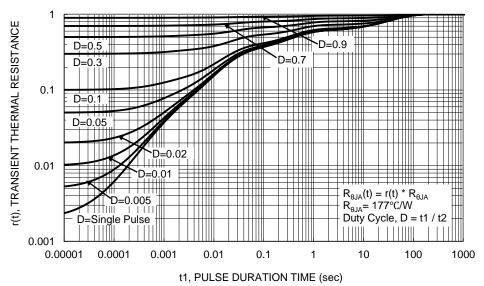


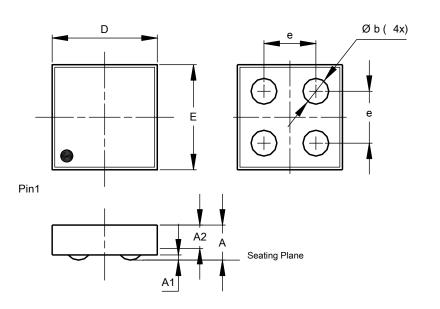
Figure 14. Transient Thermal Resistance



## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### X3-DSN0808-4

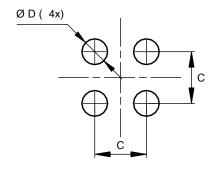


X3-DSN0808-4						
Dim	Min	Max	Тур			
Α	0.2510	0.2890	0.2700			
A1	0.0360	0.0440	0.0400			
A2	0.2150	0.2450	0.2300			
b	0.1836	0.2244	0.2040			
D	0.7900	0.8300	0.810			
Е	0.7900	0.8300	0.810			
е	-	-	0.400			
All Dimensions in mm						

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### X3-DSN0808-4



Dimensions	Value (in mm)		
С	0.400		
D	0.2040		



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многоканальный

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#### Офис по работе с юридическими лицами:

105318, г. Москва, ул. Щербаковская д. 3, офис 1107, 1118, ДЦ «Щербаковский»

Телефон: +7 495 668-12-70 (многоканальный)

Факс: +7 495 668-12-70 (доб.304)

E-mail: info@moschip.ru

Skype отдела продаж:

moschip.ru moschip.ru\_6 moschip.ru 4 moschip.ru 9