

1. General description

Dual Silicon Carbide Schottky diode in a 3-lead TO-247 plastic package, designed for high frequency switched-mode power supplies.

2. Features and benefits

- Highly stable switching performance
- High forward surge capability IFSM
- Extremely fast reverse recovery time
- Superior in efficiency to Silicon Diode alternatives
- Reduced losses in associated MOSFET
- Reduced EMI
- Reduced cooling requirements
- RoHS compliant

3. Applications

- Power factor correction
- Telecom / Server SMPS
- UPS
- PV inverter
- Electrical Vehicle Charger
- Motor Drives

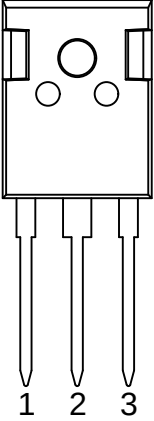
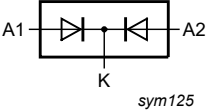
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V _{RRM}	repetitive peak reverse voltage			-	-	650	V
I _{O(AV)}	limiting average output current	T _{mb} ≤ 74 °C; δ _{factor} = 0.5 ; square-wave pulse; both diodes conducting; Fig. 1 ; Fig. 2 ; Fig. 3 ; Fig. 4		-	-	20	A
T _j	junction temperature			-	-	175	°C
Static characteristics							
V _F	forward voltage	I _F = 10 A; T _j = 25 °C; Fig. 6		-	1.65	1.85	V
		I _F = 10 A; T _j = 150 °C; Fig. 6		-	2.1	2.5	V
Dynamic characteristics							
Q _r	recovered charge	I _F = 10 A; dI _F /dt = 500 A/μs; V _R = 400 V; T _j = 25 °C; Fig. 7		-	11	-	nC

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	A1	anode	 TO-247 (SOT429N)	
2	K	cathode		
3	A2	anode		
mb	mb	mounting base; connected to cathode		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
NXPLQSC20650W	TO-247	Plastic single-ended through-hole package; heatsink mounted; 1 mounting hole; 3-lead TO-247	SOT429N

7. Marking

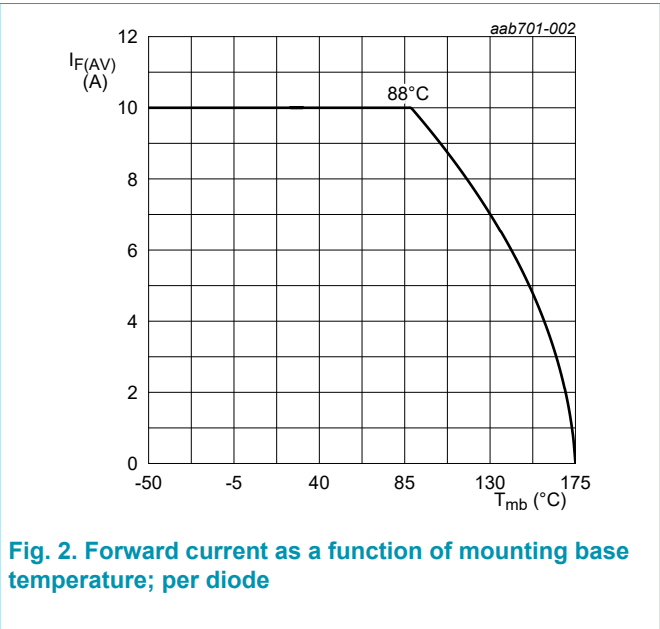
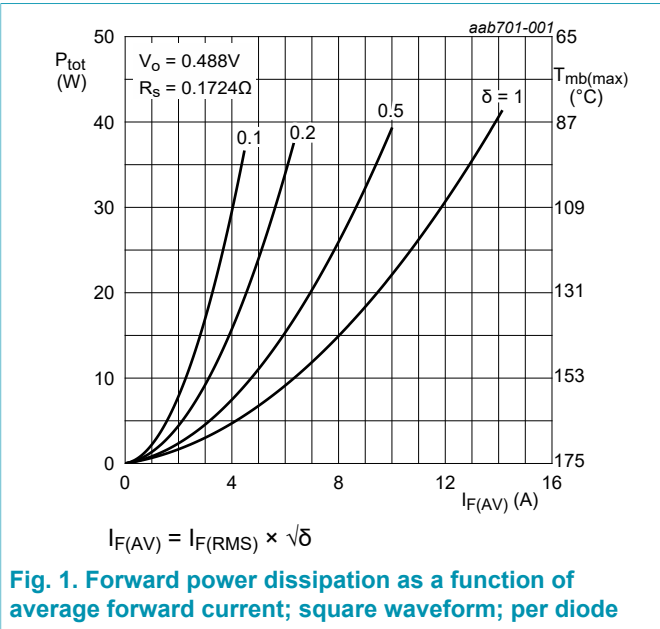
Table 4. Marking codes

Type number	Marking code
NXPLQSC20650W	NXPLQSC20650W

8. Limiting values

Table 5. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{RRM}	repetitive peak reverse voltage		-	650	V
V _{RWM}	crest working reverse voltage		-	650	V
V _R	reverse voltage	DC	-	650	V
I _{FRM}	repetitive peak forward current	δ = 0.5 ; t _p = 25 μs; T _{mb} ≤ 88 °C; square-wave pulse; per diode	-	20	A
I _{O(AV)}	limiting average output current	T _{mb} ≤ 74 °C; δ _{factor} = 0.5 ; square-wave pulse; both diodes conducting; Fig. 1; Fig. 2; Fig. 3; Fig. 4	-	20	A
I _{FSM}	non-repetitive peak forward current	t _p = 8.3 ms; T _{J(init)} = 25 °C; sine-wave pulse; per diode	-	52	A
		t _p = 10 μs; T _{J(init)} = 25 °C; square-wave pulse; per diode	-	385	A
T _{stg}	storage temperature		-55	175	°C
T _j	junction temperature		-	175	°C



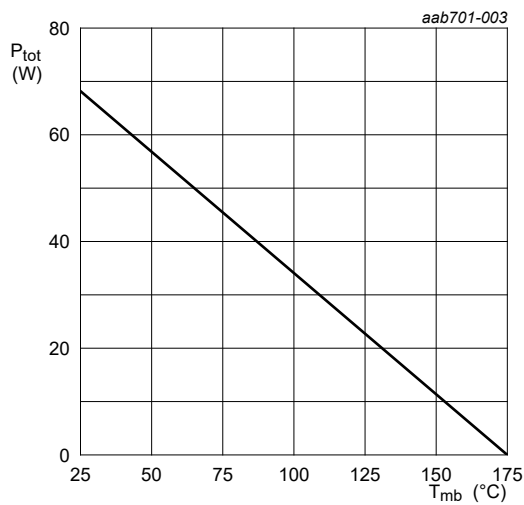


Fig. 3. Total power dissipation as a function of mounting base temperature; per diode

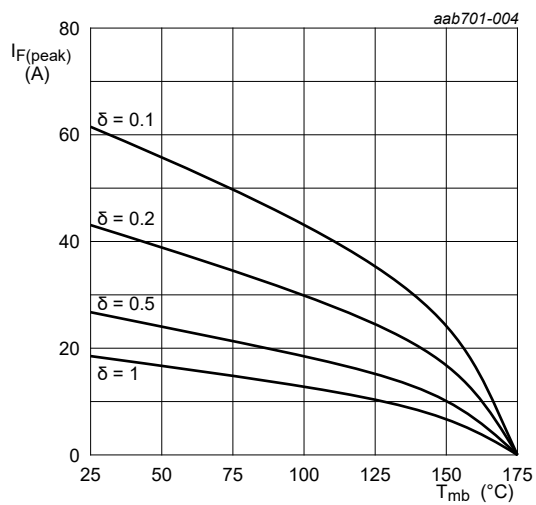
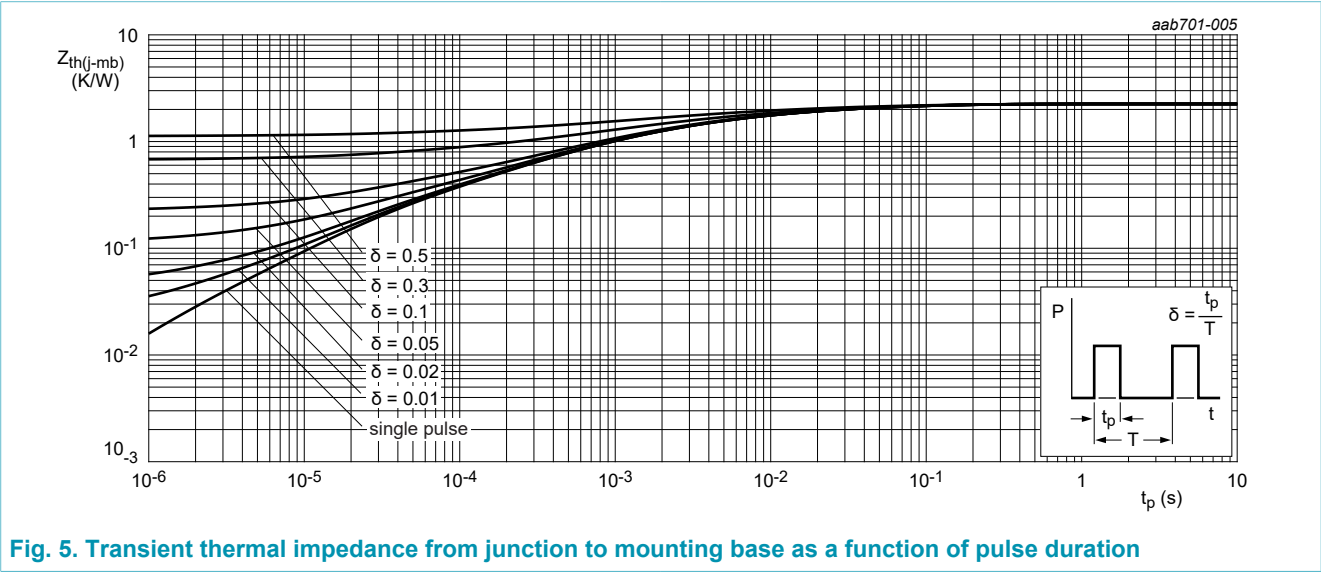


Fig. 4. Current derating as a function of mounting base temperature; per diode

9. Thermal characteristics

Table 6. Thermal characteristics

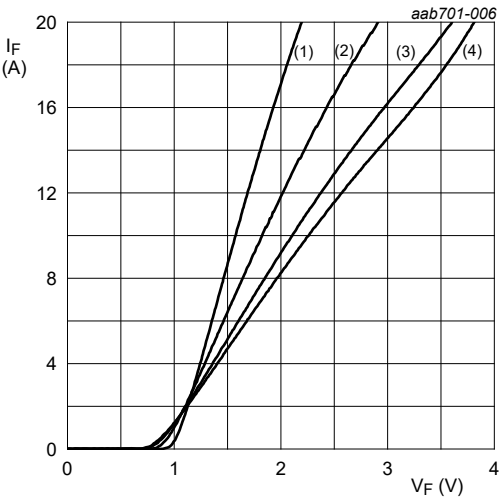
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	per diode; Fig. 5	-	-	2.2	K/W
		both diodes conducting	-	-	1.3	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient free air	in free air	-	45	-	K/W



10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Static characteristics							
V _F	forward voltage	I _F = 10 A; T _j = 25 °C; Fig. 6		-	1.65	1.85	V
		I _F = 10 A; T _j = 150 °C; Fig. 6		-	2.1	2.5	V
I _R	reverse current	V _R = 650 V; T _j = 25 °C		-	-	230	μA
		V _R = 650 V; T _j = 150 °C		-	-	700	μA
Dynamic characteristics							
Q _r	recovered charge	I _F = 10 A; dI _F /dt = 500 A/μs; V _R = 400 V; T _j = 25 °C; Fig. 7		-	11	-	nC
C _d	diode capacitance	f = 1 MHz; V _R = 1 V; T _j = 25 °C		-	250	-	pF
		f = 1 MHz; V _R = 300 V; T _j = 25 °C		-	26	-	pF
		f = 1 MHz; V _R = 600 V; T _j = 25 °C		-	21	-	pF



- (1) $T_j = 25\text{ °C}$; typical values
- (2) $T_j = 100\text{ °C}$; typical values
- (3) $T_j = 150\text{ °C}$; typical values
- (4) $T_j = 175\text{ °C}$; typical values

Fig. 6. Forward current as a function of forward voltage; typical values; per diode

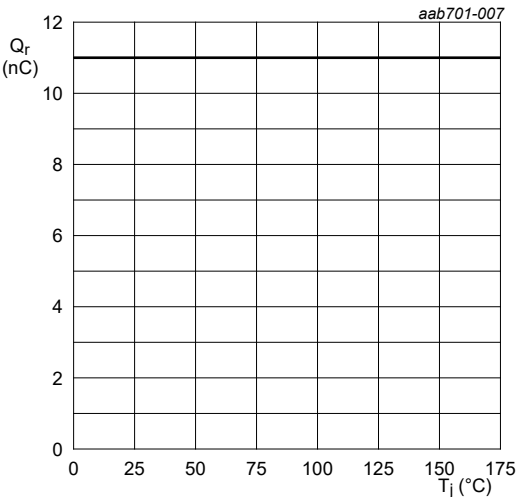


Fig. 7. Recovered charge as a function of junction temperature; per diode

11. Package outline

Plastic single-ended through-hole package; heatsink mounted; 1 mounting hole; 3-lead TO-247 SOT429N

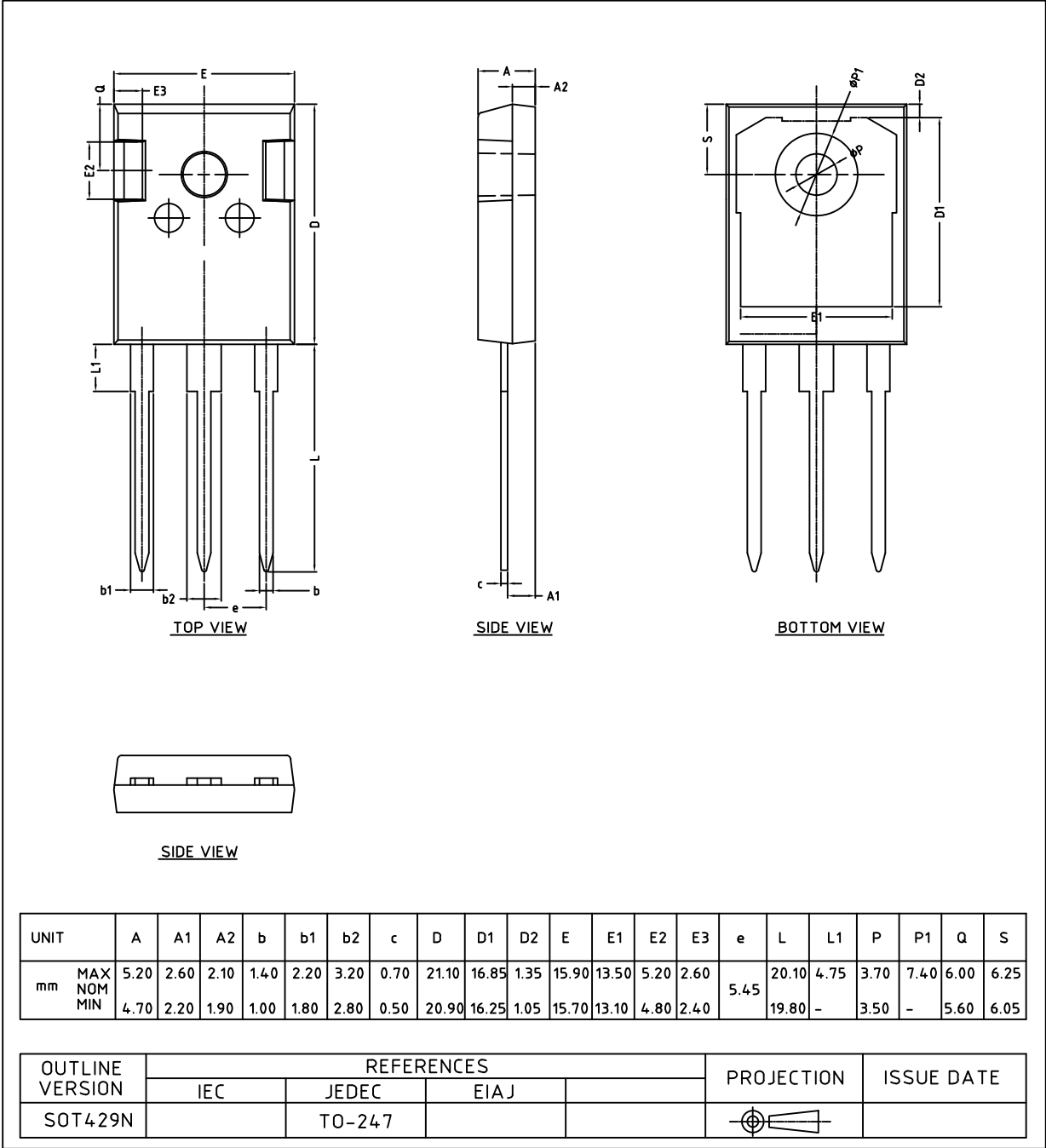


Fig. 8. Package outline TO-247 (SOT429N)

12. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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