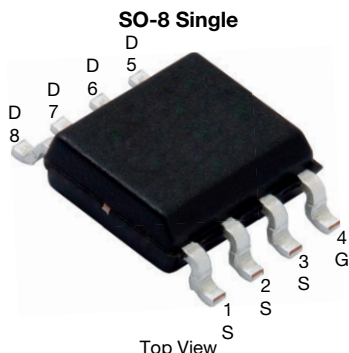


N-Channel 60 V (D-S) MOSFET



FEATURES

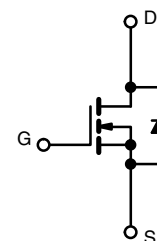
- TrenchFET® Gen IV power MOSFET
- 100 % R_g and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE

APPLICATIONS

- Synchronous rectification
- Primary side switch
- DC/DC converters
- Power supplies
- Motor drive control
- Battery and load switch



N-Channel MOSFET

PRODUCT SUMMARY

V _{DS} (V)	60
R _{DS(on)} max. (Ω) at V _{GS} = 10 V	0.0195
R _{DS(on)} max. (Ω) at V _{GS} = 4.5 V	0.0250
Q _g typ. (nC)	5.2
I _D (A)	11.3
Configuration	Single

ORDERING INFORMATION

Package	SO-8
Lead (Pb)-free and halogen-free	Si4850BDY-T1-GE3

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-source voltage	V _{DS}	60	V
Gate-source voltage	V _{GS}	± 20	V
Continuous drain current (T _J = 150 °C)	I _D	T _C = 25 °C	11.3
		T _C = 70 °C	9
		T _A = 25 °C	8.4 a, b
		T _A = 70 °C	6.8 a, b
Pulsed drain current (t = 100 μs)	I _{DM}	40	A
Continuous source-drain diode current	I _S	T _C = 25 °C	3.8
		T _A = 25 °C	2.1 a, b
Single pulse avalanche current	I _{AS}	15	A
Single pulse avalanche energy	E _{AS}	11.3	mJ
Maximum power dissipation	P _D	T _C = 25 °C	4.5
		T _C = 70 °C	2.8
		T _A = 25 °C	2.5 a, b
		T _A = 70 °C	1.6 a, b
Operating junction and storage temperature range	T _J , T _{stg}	-55 to +150	°C
Soldering recommendations (peak temperature) °C		260	

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum junction-to-ambient ^a	R _{thJA}	38	50	°C/W
Maximum junction-to-foot (drain)	R _{thJF}	22	28	°C/W

Notes

- Surface mounted on 1" x 1" FR4 board
- t = 10 s
- Maximum under steady state conditions is 85 °C/W



SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-source breakdown voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	60	-	-	V	
V _{DS} temperature coefficient	ΔV _{DS} /T _J	I _D = 250 μA	-	33	-	mV/°C	
V _{GS(th)} temperature coefficient	ΔV _{GS(th)} /T _J		-	-4.8	-		
Gate-source threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1	-	2.8	V	
Gate-source leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V	-	-	100	nA	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V	-	-	1	μA	
		V _{DS} = 60 V, V _{GS} = 0 V, T _J = 70 °C	-	-	10		
On-state drain current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	10	-	-	A	
Drain-source on-state resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 10 A	-	0.0160	0.0195	Ω	
		V _{GS} = 4.5 V, I _D = 5 A	-	0.0200	0.0250		
Forward transconductance ^a	g _{fs}	V _{DS} = 10 V, I _D = 10 A	-	39	-	S	
Dynamic ^b							
Input capacitance	C _{iss}	V _{DS} = 30 V, V _{GS} = 0 V, f = 1 MHz	-	790	-	pF	
Output capacitance	C _{oss}		-	330	-		
Reverse transfer capacitance	C _{rss}		-	14	-		
Total gate charge	Q _g	V _{DS} = 30 V, V _{GS} = 10 V, I _D = 5 A	-	11.1	17	nC	
		V _{DS} = 30 V, V _{GS} = 4.5 V, I _D = 5 A	-	5.2	8		
Gate-source charge	Q _{gs}		-	2.2	-		
Gate-drain charge	Q _{gd}		-	1.1	-		
Gate resistance	R _g	f = 1 MHz	0.1	0.6	1.2	Ω	
Turn-on delay time	t _{d(on)}	V _{DD} = 30 V, R _L = 6 Ω, I _D ≅ 5 A, V _{GEN} = 10 V, R _g = 1 Ω	-	7	15	ns	
Rise time	t _r		-	21	40		
Turn-off delay time	t _{d(off)}		-	10	20		
Fall time	t _f		-	10	20		
Turn-on delay time	t _{d(on)}	V _{DD} = 30 V, R _L = 6 Ω, I _D ≅ 5 A, V _{GEN} = 4.5 V, R _g = 1 Ω	-	13	25		
Rise time	t _r		-	25	50		
Turn-off delay time	t _{d(off)}		-	10	20		
Fall time	t _f		-	22	45		
Drain-Source Body Diode Characteristics							
Continuous source-drain diode current	I _S	T _C = 25 °C	-	-	3.8	A	
Pulse diode forward current	I _{SM}		-	-	40		
Body diode voltage	V _{SD}	I _S = 5 A, V _{GS} = 0 V	-	0.79	1.2	V	
Body diode reverse recovery time	t _{rr}	I _F = 5 A, di/dt = 100 A/μs, T _J = 25 °C	-	30	60	ns	
Body diode reverse recovery charge	Q _{rr}		-	60	120	nC	
Reverse recovery fall time	t _a		-	15	-	ns	
Reverse recovery rise time	t _b		-	15	-		

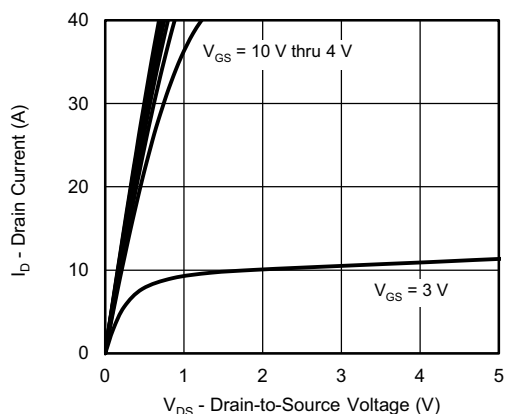
Notes

- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$
b. Guaranteed by design, not subject to production testing

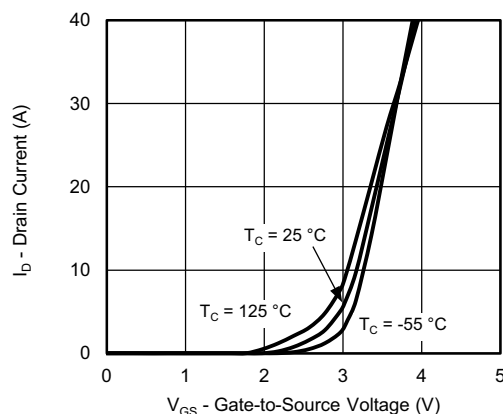
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



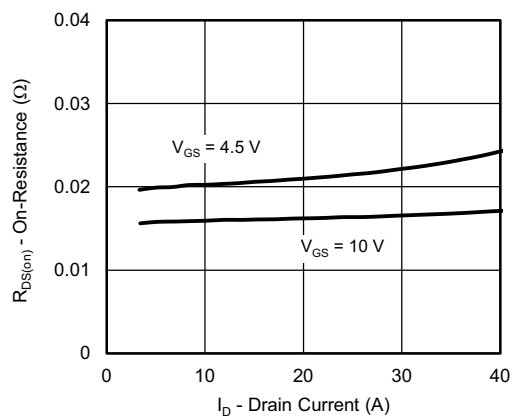
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



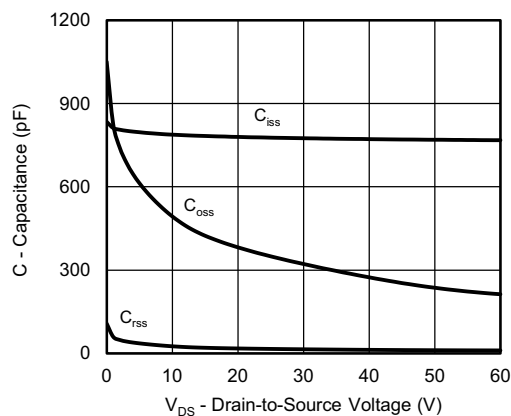
Output Characteristics



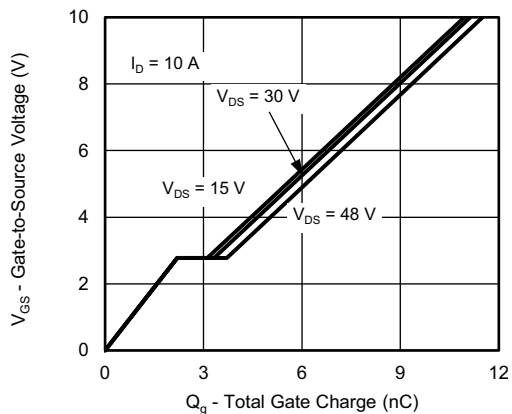
Transfer Characteristics



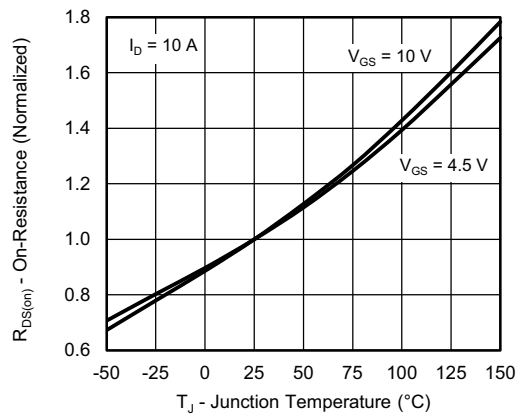
On-Resistance vs. Drain Current and Gate Voltage



Capacitance



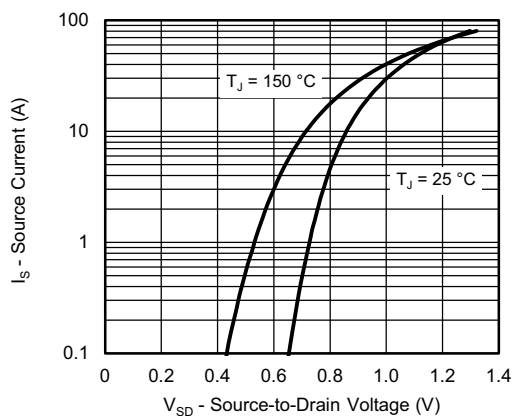
Gate Charge



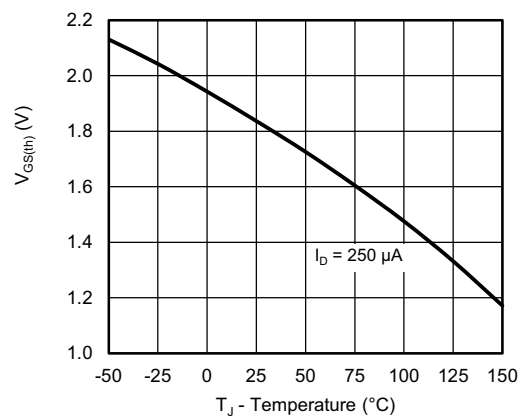
On-Resistance vs. Junction Temperature



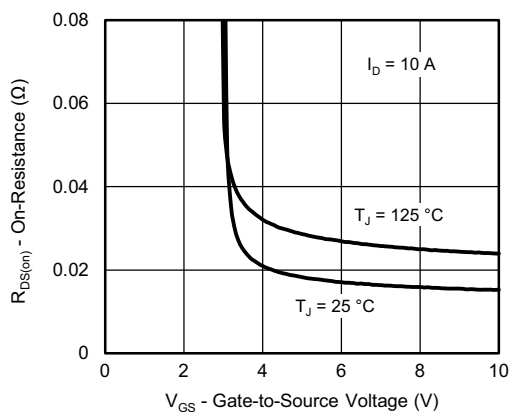
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



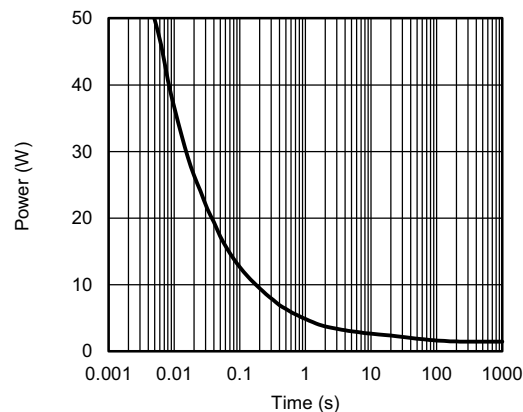
Source-Drain Diode Forward Voltage



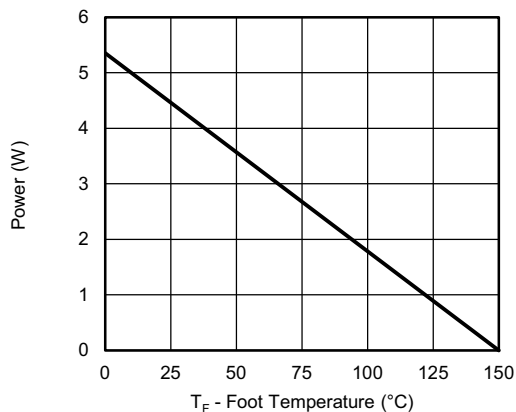
Threshold Voltage



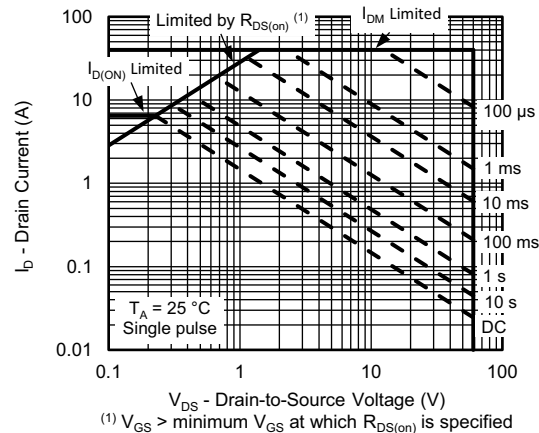
On-Resistance vs. Gate-to-Source Voltage



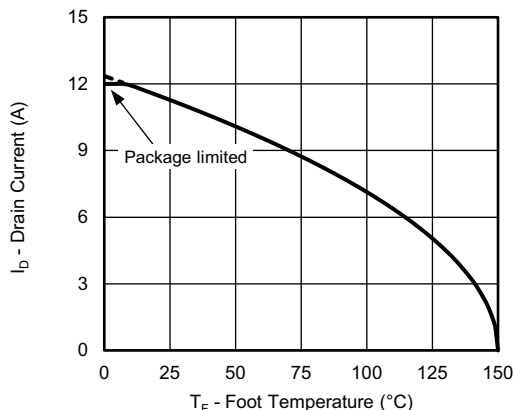
Single Pulse Power, Junction-to-Ambient



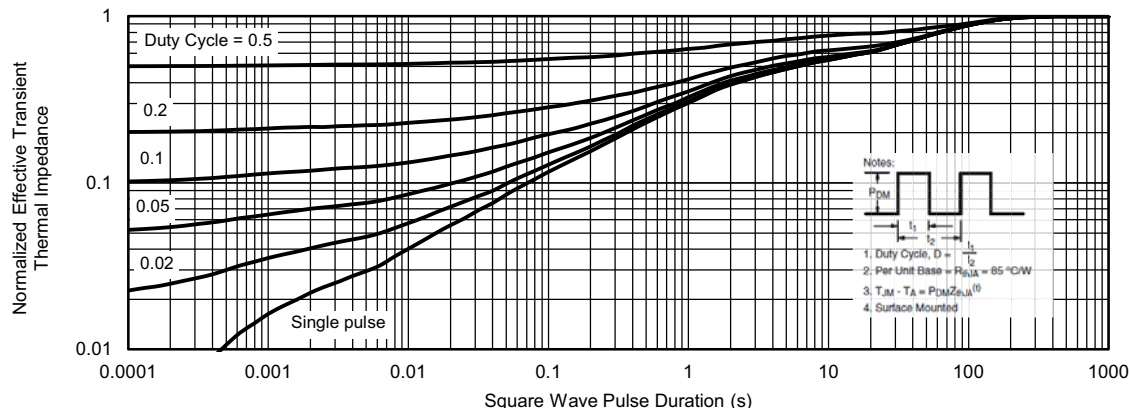
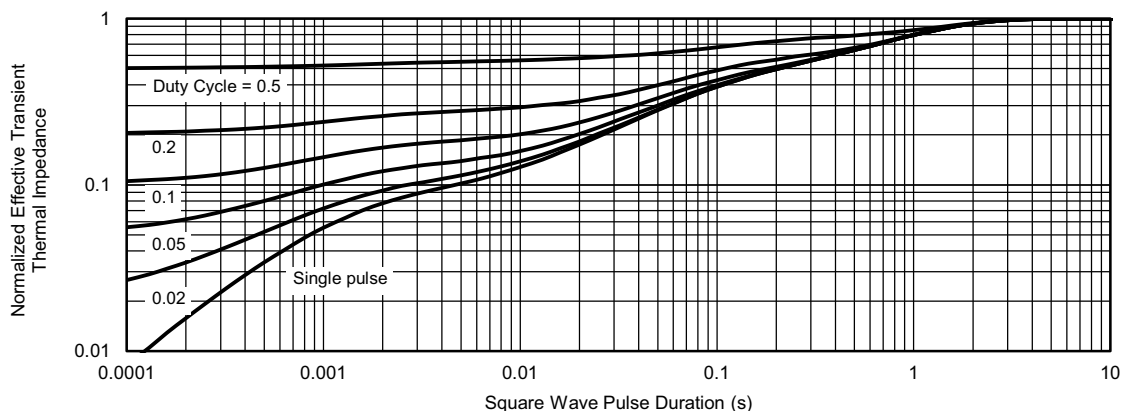
Power, Junction-to-Foot



Safe Operating Area, Junction-to-Ambient

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

Current Derating ^a
Note

- a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.


Normalized Thermal Transient Impedance, Junction-to-Ambient

Normalized Thermal Transient Impedance, Junction-to-Foot

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