

MC74HC4067A

Quad Analog Switch/ Multiplexer/Demultiplexer

High-Performance Silicon-Gate CMOS

The MC74HC4067A utilizes silicon-gate CMOS technology to achieve fast propagation delays, low ON resistances, and low OFF-channel leakage current. This bilateral switch/multiplexer/demultiplexer controls analog and digital voltages that may vary across the full power-supply range (from V_{CC} to GND).

The ON/OFF control inputs are compatible with standard CMOS outputs; with pullup resistors, they are compatible with LSTTL outputs. For analog switches with voltage-level translators, see the HC4316A.

Features

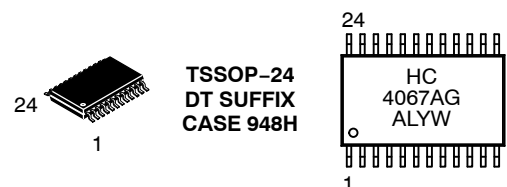
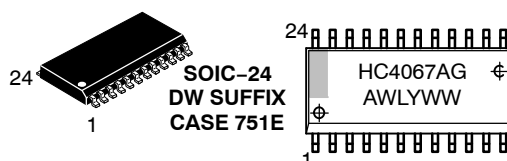
- Fast Switching and Propagation Speeds
- High ON/OFF Output Voltage Ratio
- Low Crosstalk Between Switches
- Diode Protection on All Inputs/Outputs
- Wide Power-Supply Voltage Range ($V_{CC} - GND$) = 2.0 to 6.0 V
- Analog Input Voltage Range ($V_{CC} - GND$) = 0 to 6.0 V
- Improved Linearity and Lower ON Resistance over Input Voltage
- Low Noise
- These are Pb-Free Devices



ON Semiconductor®

<http://onsemi.com>

MARKING DIAGRAMS



A = Assembly Location
L, WL = Wafer Lot
Y, YY = Year
W, WW = Work Week
G = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 10 of this data sheet.

MC74HC4067A

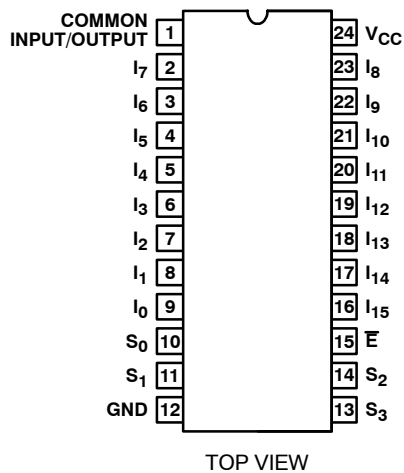


Figure 1. Pin Assignment

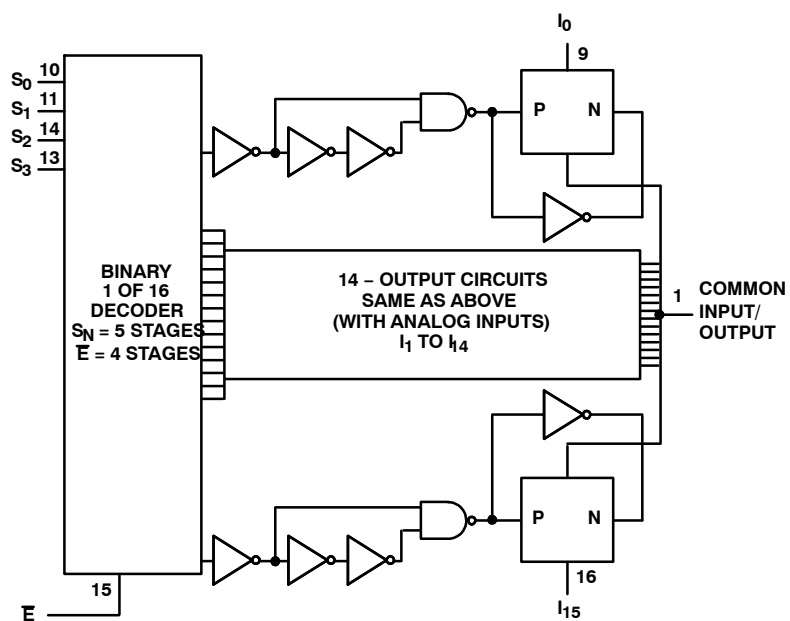


Figure 2. Function Diagram

TRUTH TABLE

S0	S1	S2	S3	$\bar{E}$	SELECTED CHANNEL
X	X	X	X	1	None
0	0	0	0	0	0
1	0	0	0	0	1
0	1	0	0	0	2
1	1	0	0	0	3
0	0	1	0	0	4
1	0	1	0	0	5
0	1	1	0	0	6
1	1	1	0	0	7
0	0	0	1	0	8
1	0	0	1	0	9
0	1	0	1	0	10
1	1	0	1	0	11
0	0	1	1	0	12
1	0	1	1	0	13
0	1	1	1	0	14
1	1	1	1	0	15

H= High Level
L= Low Level
X= Don't Care

MC74HC4067A

MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_{CC}	DC Supply Voltage	-0.5 to +7.0	V
V_{IS}	Analog Input Voltage	-0.5 to $V_{CC} + 0.5$	V
V_{IN}	Digital Input Voltage	-0.5 to $V_{CC} + 0.5$	V
I_{IK}	Input Clamping Current $V_{IN} < -0.5 \text{ V}$ or $V_{IN} > V_{CC} + 0.5 \text{ V}$	± 20	mA
I_{SK}	Switch Input Clamping Current $V_{IS} < -0.5 \text{ V}$ or $V_{IS} > V_{CC} + 0.5 \text{ V}$	± 20	mA
I_{IS}	DC Switch Current	± 25	mA
I_O	DC Output Source / Sink Current	± 25	mA
I_{CC}	DC Supply Current per Supply Pin	± 100	mA
I_{GND}	DC Ground Current per Ground Pin	± 100	mA
T_{STG}	Storage Temperature Range	-65 to +150	°C
T_L	Lead Temperature, 1 mm from Case for 10 Seconds	260	°C
T_J	Junction Temperature under Bias	+150	°C
θ_{JA}	Thermal Resistance SOIC TSSOP	97 148	°C/W
P_D	Power Dissipation in Still Air at 85°C SOIC TSSOP	500 450	mW
MSL	Moisture Sensitivity	Level 1	
F_R	Flammability Rating Oxygen Index: 30% – 35%	UL-94-VO (0.125 in)	
V_{ESD}	ESD Withstand Voltage Human Body Model (Note 1) Machine Model (Note 2)	>3000 >200	V
$I_{Latchup}$	Latchup Performance Above V_{CC} and Below GND at 85°C (Note 3)	± 100	mA

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Tested to EIA/JESD22-A114-A.
2. Tested to EIA/JESD22-A115-A.
3. Tested to EIA/JESD78.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
V_{CC}	Positive DC Supply Voltage (Referenced to GND)	2.0	6.0	V
V_{IS}	Analog Input Voltage (Referenced to GND)	GND	V_{CC}	V
V_{in}	Digital Input Voltage (Referenced to GND)	GND	V_{CC}	V
V_{IO}^*	Static or Dynamic Voltage Across Switch	-	1.2	V
T_A	Operating Temperature, All Package Types	-55	+125	°C
t_r, t_f	Input Rise and Fall Rate (Digital Inputs) $V_{CC} = 2.0 \text{ V}$ $V_{CC} = 3.0 \text{ V}$ $V_{CC} = 4.5 \text{ V}$ $V_{CC} = 6.0 \text{ V}$	0 0 0 0	1000 600 500 400	ns

*For voltage drops across the switch greater than 1.2 V (switch on), excessive V_{CC} current may be drawn; i.e., the current out of the switch may contain both V_{CC} and switch input components. The reliability of the device will be unaffected unless the Maximum Ratings are exceeded.

MC74HC4067A

DC ELECTRICAL CHARACTERISTIC Digital Section (Voltages Referenced to GND)

Symbol	Parameter	Conditions	V _{CC} (V)	Guaranteed Limit							Unit
				25°C			−40 to 85°C		−55 to 125°C		
				Min	Typ	Max	Min	Max	Min	Max	
V _{IH}	Minimum High-Level Input Voltage, Channel-Select or Enable Inputs		2.0 3.0 4.5 6.0	1.5 2.1 3.15 4.2			1.5 2.1 3.15 4.2			1.5 2.1 3.15 4.2	V
V _{IL}	Maximum Low-Level Input Voltage, Channel-Select or Enable Inputs		2.0 3.0 4.5 6.0			0.5 0.9 1.35 1.8		0.5 0.9 1.35 1.8		0.5 0.9 1.35 1.8	V
I _{IN}	Input Leakage Current, Control Inputs	V _{IN} = V _{CC} or GND	6.0			±0.1		±1.0		±1.0	μA
I _{CC}	Maximum Supply Current per Package	V _{IN} = V _{CC} or GND, I _O =0 V _{IS} = GND or V _{CC} , V _{OS} = V _{CC} or GND	6.0			4.0		40		80	μA
R _{ON}	ON Resistance	I _O = 1 mA V _{IN} = V _{CC} or GND, V _{IS} = V _{CC} or GND	4.5 6.0		70 60	160 140		200 175		240 210	Ω
R _{ON(peak)}	ON Resistance (peak)	I _O = 1 mA V _{IN} = V _{CC} to GND, V _{IS} = V _{CC} to GND	4.5 6.0		90 80	180 160		225 200		270 240	Ω
ΔR _{on}	ON Resistance Mismatch Between Any 2 Switches		4.5 6.0		10 8.5						Ω
I _{OFF}	OFF-State Leakage Current, All Channels	SW OFF, V _{IS} = V _{CC} or GND	6.0			±0.8		±8		±8	μA
I _{ON}	ON-State Leakage Current	SW OFF, V _{IS} = V _{CC} or GND	6.0			±0.8		±8		±8	μA

MC74HC4067A

AC CHARACTERISTICS (INPUT $t_r, t_f = 6 \text{ ns}$)

Symbol	Parameter	Conditions	V _{CC} (V)	Guaranteed Limits							Unit
				25°C			−40 to 85°C		−55 to 125°C		
				Min	Typ	Max	Min	Max	Min	Max	
t _{PLH} , t _{PHL}	Propagation Delay Switch In to Out	C _L = 50 pF	2.0 4.5 6.0			75 15 13		95 19 16		110 22 19	ns
		C _L = 15 pF	5.0		6.0						
t _{ON}	Switch Turn–ON Time										ns
	E̅ to Out	C _L = 50 pF	2.0 4.5 6.0			275 55 47		345 69 59		415 83 71	
		C _L = 15 pF	5.0		23						
	SN to Out	C _L = 50 pF	2.0 4.5 6.0			300 60 51		375 75 64		450 90 76	
		C _L = 15 pF	5.0		25						
t _{OFF}	Switch Turn–OFF Time										ns
	E̅ to Out	C _L = 50 pF	2.0 4.5 6.0			275 55 47		345 69 59		415 83 71	
		C _L = 15 pF	5.0		23						
	SN to Out	C _L = 50 pF	2.0 4.5 6.0			290 58 49		365 73 62		435 87 74	
		C _L = 15 pF	5.0		21						
C _{in}	Input Capacitance, Control Pins				3.5	10		10		10	pF
C _{PD}	Power Dissipation Capacitance (Note 4)	C _L = 15 pF	5.0			29					pF

4. C_{PD} is used to determine the dynamic power consumption, per multivibrator.

MC74HC4067A

ANALOG SWITCH CHANNEL CHARACTERISTICS

Symbol	Parameter	Conditions	V _{CC} (V)	Limit* 25°C	Unit
BW	Maximum On-Channel Bandwidth or Minimum Frequency Response	f _{in} = 1 MHz Sine Wave Adjust f _{in} Voltage to Obtain 0 dBm at V _{OS} Increase f _{in} Frequency Until dB Meter Reads – 3 dB R _L = 50 Ω, C _L = 10 pF	4.5	90	MHz
–	Off-Channel Feedthrough Isolation	f _{in} ≡ Sine Wave Adjust f _{in} Voltage to Obtain 0 dBm at V _{IS} f _{in} = 10 kHz, R _L = 600 Ω, C _L = 50 pF f _{in} = 1.0 MHz, R _L = 50 Ω, C _L = 10 pF	4.5 4.5	–65 –75	dB
–	Feedthrough Noise E _i , S _n to Switch	V _{in} ≤ 1 MHz Square Wave (t _r = t _f = 6 ns) Adjust R _L at Setup so that I _S = 0 A R _L = 600 Ω, C _L = 50 pF R _L = 10 kΩ, C _L = 10 pF	4.5 4.5	60 30	mV _{PP}
–	Crosstalk Between Any Two Switches	f _{in} ≡ Sine Wave Adjust f _{in} Voltage to Obtain 0 dBm at V _{IS} f _{in} = 10 kHz, R _L = 600 Ω, C _L = 50 pF f _{in} = 1.0 MHz, R _L = 50 Ω, C _L = 10 pF	4.5 4.5	–70 –80	dB
THD	Total Harmonic Distortion	f _{in} = 1 kHz, R _L = 10 kΩ, C _L = 50 pF THD = THD _{Measured} – THD _{Source} V _{IS} = 4.0 V _{PP} sine wave	4.5	0.04	%
C _S	Switch Input Capacitance			5	pF
C _{COM}	Switch Common Capacitance			45	pF

*Limits not tested. Determined by design and verified by qualification.

MC74HC4067A

TYPICAL CHARACTERISTICS

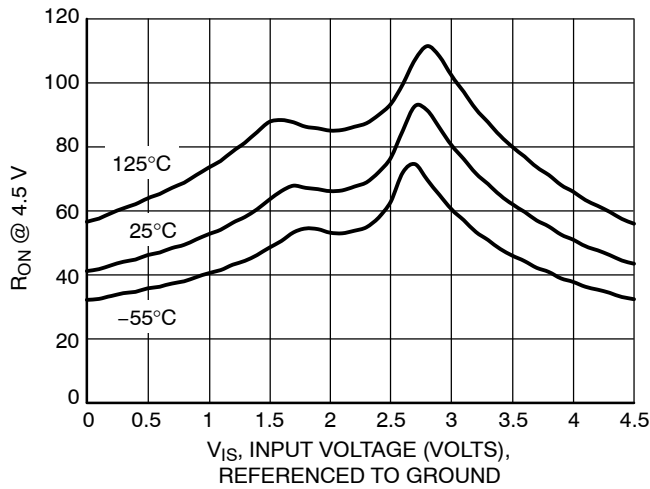


Figure 3. Typical On Resistance, $V_{CC} = 4.5$ V

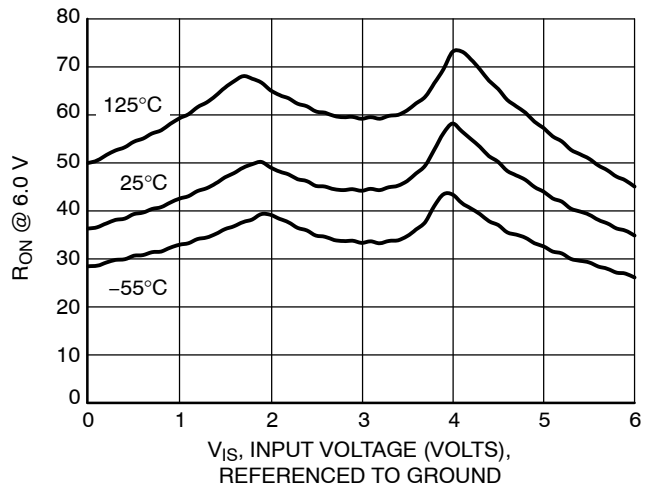


Figure 4. Typical On Resistance, $V_{CC} = 6.0$ V

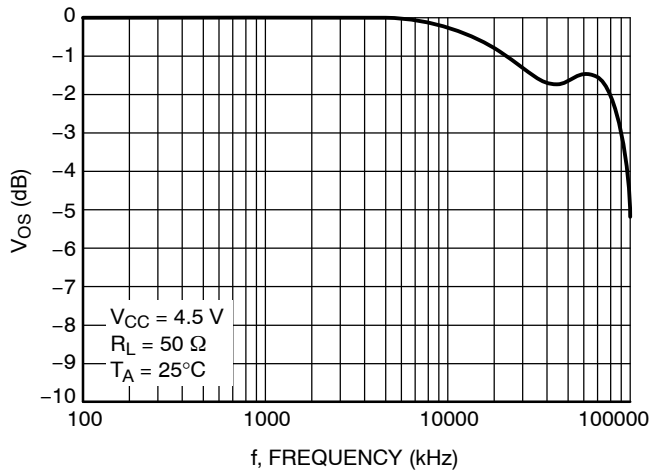


Figure 5. Typical Switch Frequency Response

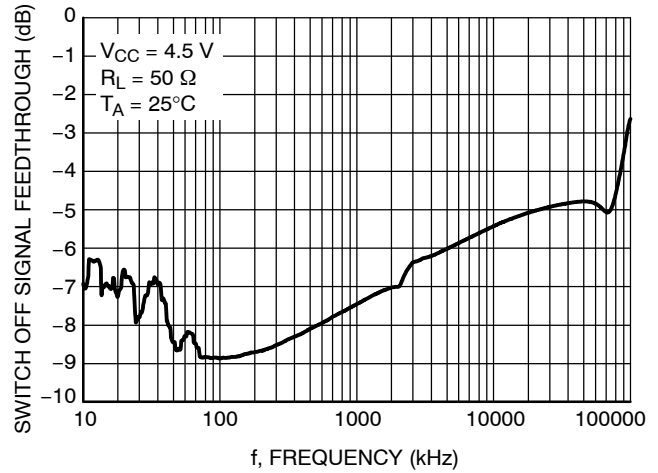


Figure 6. Typical Switch OFF Signal Feedthrough vs Frequency

MC74HC4067A

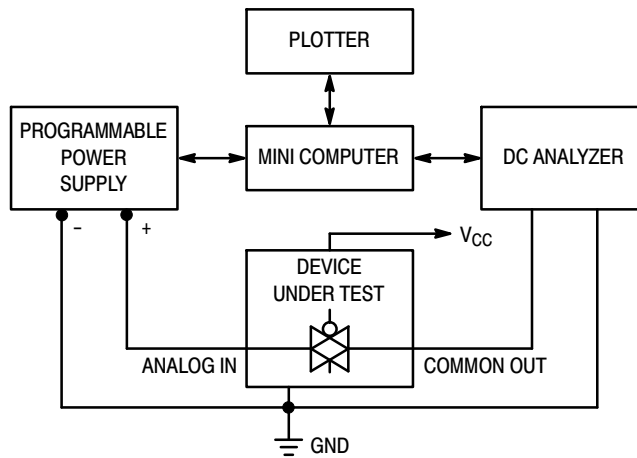


Figure 7. On Resistance Test Setup

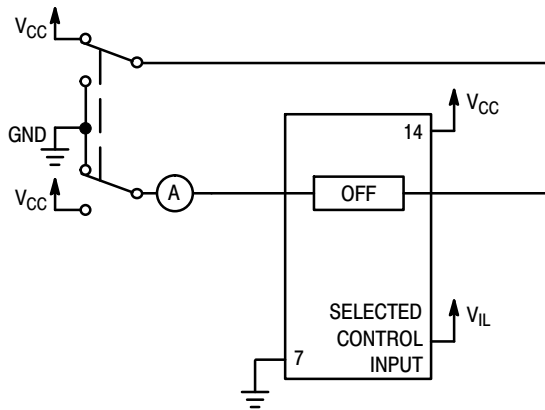


Figure 8. OFF Channel Leakage Current Test Setup, Any One Channel

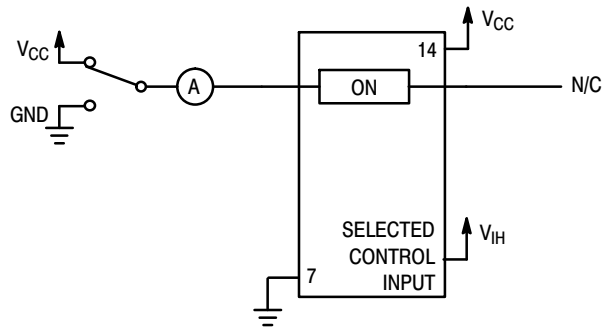


Figure 9. ON Channel Leakage Current Test Setup

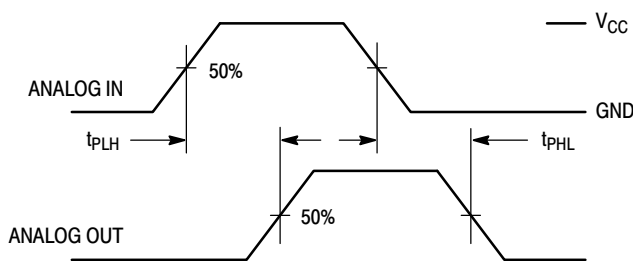
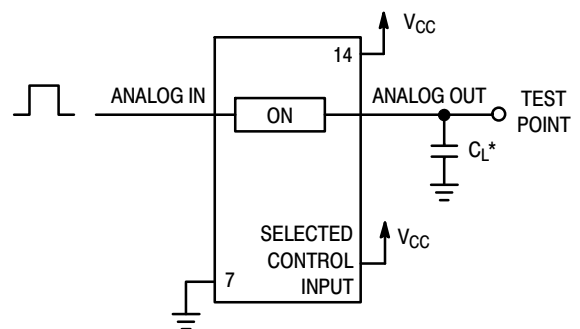


Figure 10. Propagation Delay, Analog In to Analog Out



*Includes all probe and jig capacitance.

Figure 11. Propagation Delay Test Setup

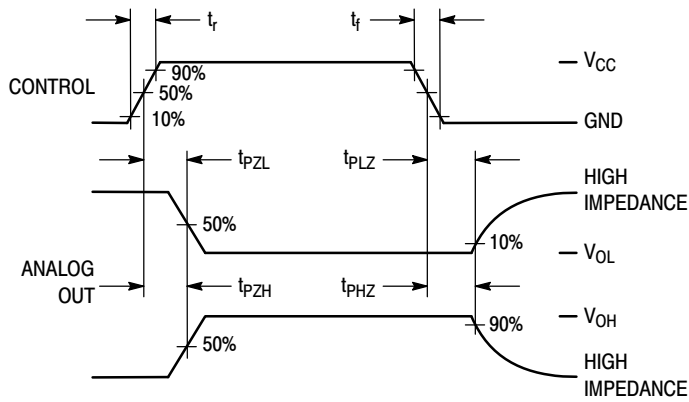
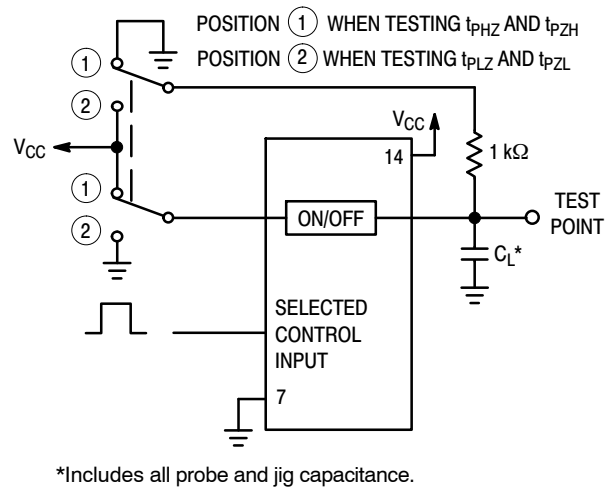


Figure 12. Turn-ON / Turn-OFF Times



*Includes all probe and jig capacitance.

Figure 13. Turn-ON / Turn-OFF Time Test Setup

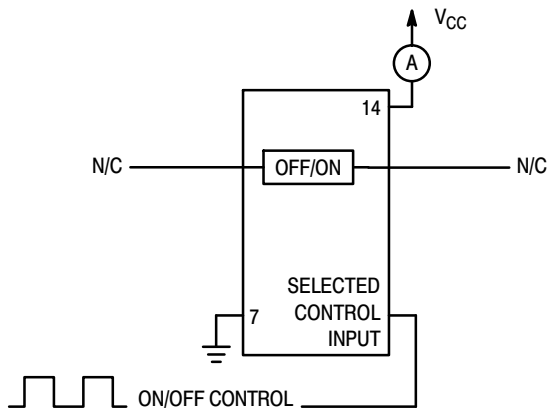
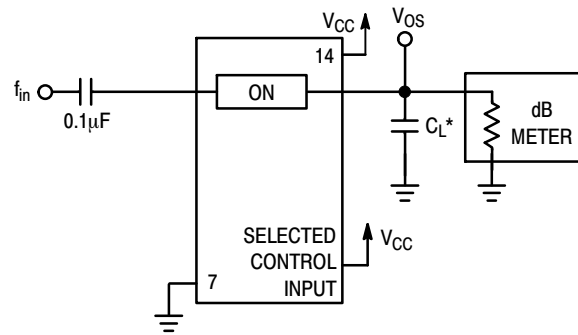
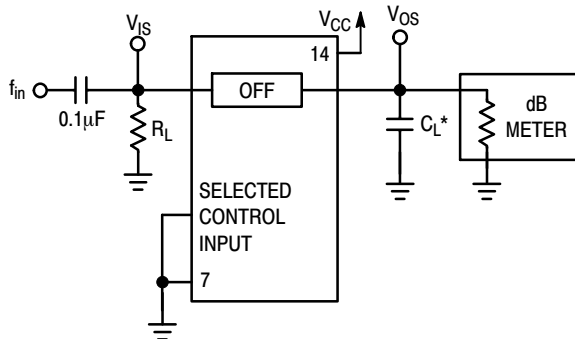


Figure 14. Power Dissipation Capacitance Test Setup



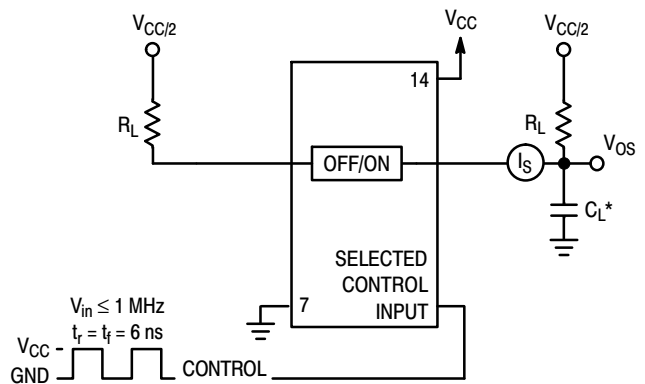
*Includes all probe and jig capacitance.

Figure 15. ON Channel Bandwidth Test Setup



*Includes all probe and jig capacitance.

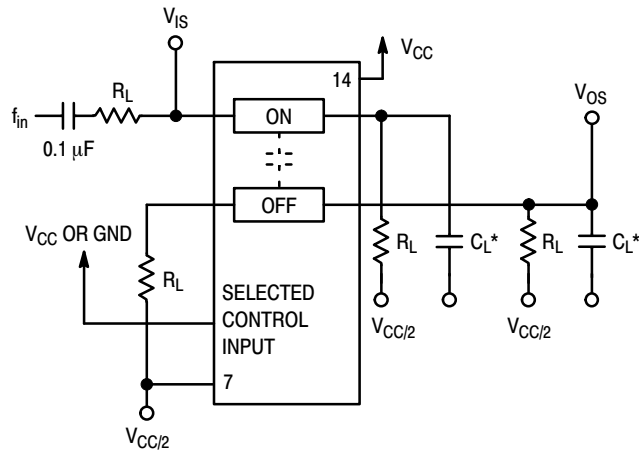
Figure 16. OFF Channel Feedthrough Isolation Test Setup



*Includes all probe and jig capacitance.

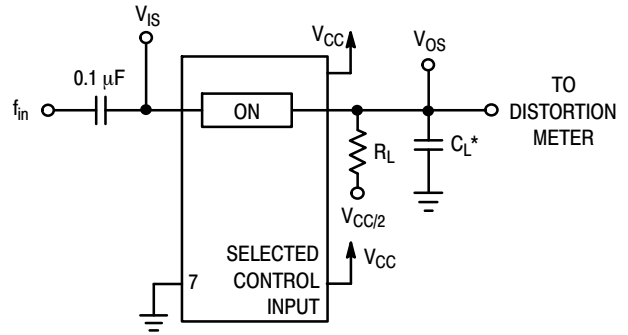
Figure 17. Feedthrough Noise Test Setup

MC74HC4067A



*Includes all probe and jig capacitance.

Figure 18. Crosstalk Between Any Two Switches Test Setup



*Includes all probe and jig capacitance.

Figure 19. Total Harmonic Distortion Test Setup

ORDERING INFORMATION

Device	Package	Shipping [†]
MC74HC4067ADWR2G	SOIC-24 (Pb-Free)	1000 / Tape & Reel
MC74HC4067ADTR2G	TSSOP-24*	2500 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*This package is inherently Pb-Free.

PACKAGE DIMENSIONS

Technical drawing of a 24-pin D-sub connector. The drawing includes a top view showing dimensions A (overall width), B (body width), and D (pin pitch). It also shows a side view with dimensions C (height), K (mounting hole offset), and G (body length). A detail view shows the pin profile with dimensions J (height), F (width), and R (radius). A table of dimensions is provided: A = 0.010 (0.25) M, B = M, D = 0.010 (0.25) M, T = A, S = B, S = B. The drawing is labeled "24X D" and "22X G".

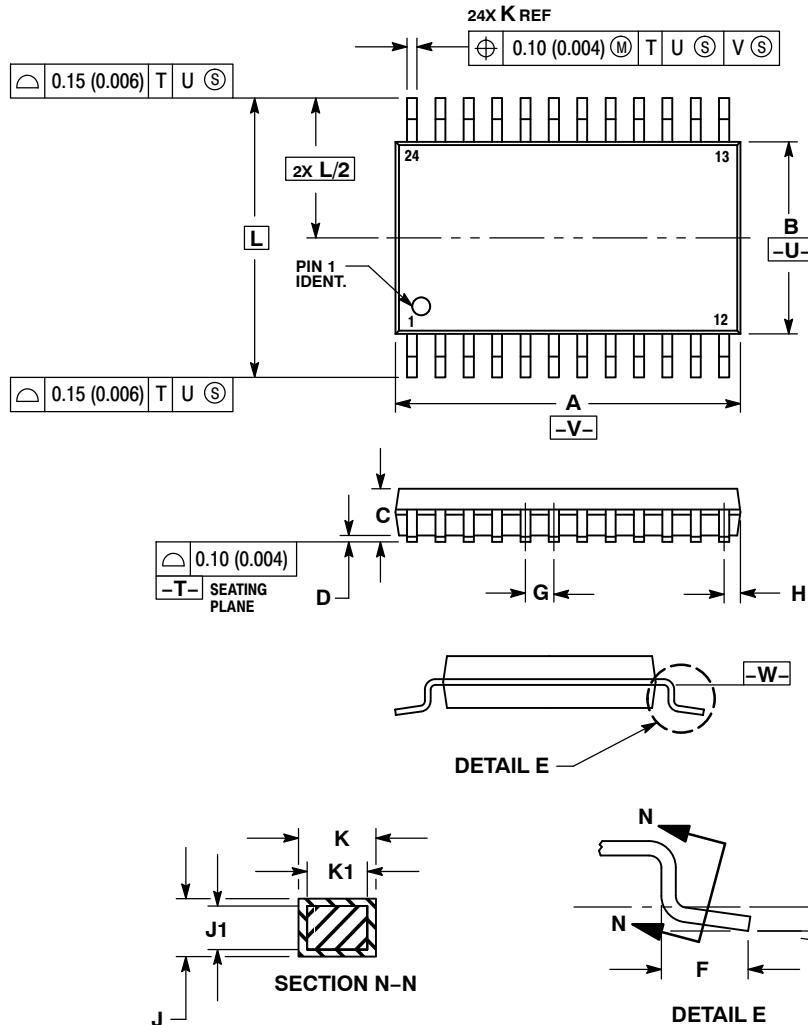
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSIONS A AND B DO NOT INCLUDE MOLD PROTRUSION.
4. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.
5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.13 (0.005) TOTAL IN EXCESS OF D DIMENSION AT MAXIMUM MATERIAL CONDITION.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	15.25	15.54	0.601	0.612
B	7.40	7.60	0.292	0.299
C	2.35	2.65	0.093	0.104
D	0.35	0.49	0.014	0.019
F	0.41	0.90	0.016	0.035
G	1.27 BSC		0.050 BSC	
J	0.23	0.32	0.009	0.013
K	0.13	0.29	0.005	0.011
M	0 °	8 °	0 °	8 °
P	10.05	10.55	0.395	0.415
R	0.25	0.75	0.010	0.029

MC74HC4067A

PACKAGE DIMENSIONS

TSSOP-24
CASE 948H-01
ISSUE A



NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSION A DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH OR GATE BURRS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.
4. DIMENSION B DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION. INTERLEAD FLASH OR PROTRUSION SHALL NOT EXCEED 0.25 (0.010) PER SIDE.
5. DIMENSION K DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 (0.003) TOTAL IN EXCESS OF THE K DIMENSION AT MAXIMUM MATERIAL CONDITION.
6. TERMINAL NUMBERS ARE SHOWN FOR REFERENCE ONLY.
7. DIMENSION A AND B ARE TO BE DETERMINED AT DATUM PLANE -W-.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	7.70	7.90	0.303	0.311
B	4.30	4.50	0.169	0.177
C	---	1.20	---	0.047
D	0.05	0.15	0.002	0.006
F	0.50	0.75	0.020	0.030
G	0.65 BSC		0.026 BSC	
H	0.27	0.37	0.011	0.015
J	0.09	0.20	0.004	0.008
J1	0.09	0.16	0.004	0.006
K	0.19	0.30	0.007	0.012
K1	0.19	0.25	0.007	0.010
L	6.40 BSC		0.252 BSC	
M	0°	8°	0°	8°

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:
Literature Distribution Center for ON Semiconductor
P.O. Box 5163, Denver, Colorado 80217 USA
Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada
Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada
Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada
Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910
Japan Customer Focus Center
Phone: 81-3-5773-3850

ON Semiconductor Website: www.onsemi.com
Order Literature: <http://www.onsemi.com/orderlit>

For additional information, please contact your local Sales Representative

MC74HC4067A/D

Данный компонент на территории Российской Федерации

Вы можете приобрести в компании MosChip.

Для оперативного оформления запроса Вам необходимо перейти по данной ссылке:

<http://moschip.ru/get-element>

Вы можете разместить у нас заказ для любого Вашего проекта, будь то серийное производство или разработка единичного прибора.

В нашем ассортименте представлены ведущие мировые производители активных и пассивных электронных компонентов.

Нашей специализацией является поставка электронной компонентной базы двойного назначения, продукции таких производителей как XILINX, Intel (ex.ALTERA), Vicor, Microchip, Texas Instruments, Analog Devices, Mini-Circuits, Amphenol, Glenair.

Сотрудничество с глобальными дистрибьюторами электронных компонентов, предоставляет возможность заказывать и получать с международных складов практически любой перечень компонентов в оптимальные для Вас сроки.

На всех этапах разработки и производства наши партнеры могут получить квалифицированную поддержку опытных инженеров.

Система менеджмента качества компании отвечает требованиям в соответствии с ГОСТ Р ИСО 9001, ГОСТ РВ 0015-002 и ЭС РД 009

Офис по работе с юридическими лицами:

105318, г.Москва, ул.Щербаковская д.3, офис 1107, 1118, ДЦ «Щербаковский»

Телефон: +7 495 668-12-70 (многоканальный)

Факс: +7 495 668-12-70 (доб.304)

E-mail: info@moschip.ru

Skype отдела продаж:

moschip.ru

moschip.ru_4

moschip.ru_6

moschip.ru_9