74AHC1G4214

14-stage divider and oscillator Rev. 3 — 26 April 2018

Product data sheet

General description

74AHC1G4214 is a 14-stage divider and oscillator. It consists of a chain of 14 flip-flops. Each flip-flop divides the frequency of the previous flip-flop by two, consequently the 74AHC1G4214 counts up to 2^{14} = 16384. The single inverting stage (X1 to X2) functions as a crystal oscillator or an input buffer for an external oscillator. When used as a buffer the output X2 should be left floating. The frequency of the output (Q) is the frequency applied to X1 divided by 16384. The divider advances on the negative-going transition of X1.

The X1 input is overvoltage tolerant. This feature allows the use of this device as a voltage level translator in mixed voltage environments.

Features and benefits 2

- Wide supply voltage range from 2.0 V to 5.5 V
- Overvoltage tolerant inputs to 5.5 V
- High noise immunity
- CMOS low power dissipation
- ESD protection:
 - HBM JESD22-A114F: exceeds 2000 V
 - CDM JESD22-C101E: exceeds 1000 V
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C

3 **Ordering information**

Table 1. Ordering information

Type number	Package	Package					
	Temperature range	Name	Description	Version			
74AHC1G4214GW	-40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1			

Marking

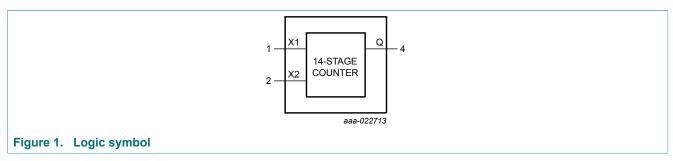
Table 2. Marking codes

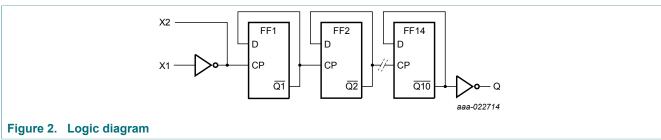
Type number	Marking ^[1]
74AHC1G4214GW	C4

^[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.



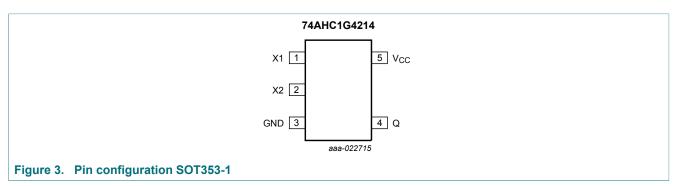
5 Functional diagram





6 Pinning information

6.1 Pinning



6.2 Pin description

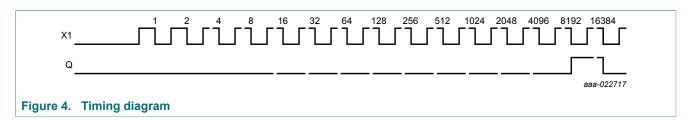
Table 3. Pin description

Symbol	Pin	Description
X1	1	clock input/oscillator pin
X2	2	oscillator pin
GND	3	ground (0 V)
Q	4	divider output
V _{CC}	5	supply voltage

74AHC1G4214

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Functional description



Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CC}	supply voltage			-0.5	+7.0	V
VI	input voltage			-0.5	+7.0	V
I _{IK}	input clamping current	V _I < -0.5 V		-20	-	mA
I _{OK}	output clamping current	V_{O} < -0.5 V or V_{O} > V_{CC} + 0.5 V	[1]	-	±20	mA
Io	output current	-0.5 V < V _O < V _{CC} + 0.5 V		-	±25	mA
I _{CC}	supply current			-	75	mA
I _{GND}	ground current			-75	-	mA
T _{stg}	storage temperature			-65	+150	°C
P _{tot}	total power dissipation	T _{amb} = -40 °C to +125 °C	[2]	-	250	mW

^[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed. [2] For TSSOP5 package: above 87.5 °C the value of P_{tot} derates linearly with 4.0 mW/K.

9 Recommended operating conditions

Table 5. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CC}	supply voltage		2.0	5.0	5.5	V
VI	input voltage		0	-	5.5	V
Vo	output voltage		0	-	V _{CC}	V
T _{amb}	ambient temperature		-40	+25	+125	°C
Δt/ΔV	input transition rise and fall	V _{CC} = 3.3 V ± 0.3 V	-	-	100	ns/V
rate		V _{CC} = 5.0 V ± 0.5 V	-	-	20	ns/V

10 Static characteristics

Table 6. Static characteristics

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		25 °C		-40 °C 1	to +85 °C	-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Max	
V _{IH}	HIGH-level	X1								
	input voltage	V _{CC} = 2.0 V	1.7	-	-	1.7	-	1.7	-	V
		V _{CC} = 3.0 V	2.4	-	-	2.4	-	2.4	-	V
		V _{CC} = 5.5 V	4.4	-	-	4.4	-	4.4	-	V
V _{IL}		X1								
	input voltage	V _{CC} = 2.0 V	-	-	0.3	-	0.3	-	0.3	V
		V _{CC} = 3.0 V	-	-	0.6	-	0.6	-	0.6	V
		V _{CC} = 5.5 V	-	-	1.1	-	1.1	-	1.1	V
V _{OH}	HIGH-level	$Q; V_I = V_{IH} \text{ or } V_{IL}$								
	output voltage	I _O = -50 μA; V _{CC} = 2.0 V	1.9	2.0	-	1.9	-	1.9	-	V
		I _O = -50 μA; V _{CC} = 3.0 V	2.9	3.0	-	2.9	-	2.9	-	V
		I _O = -50 μA; V _{CC} = 4.5 V	4.4	4.5	-	4.4	-	4.4	-	V
		$I_O = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.58	-	-	2.48	-	2.40	-	V
		$I_O = -8.0 \text{ mA}; V_{CC} = 4.5 \text{ V}$	3.94	-	-	3.8	-	3.70	-	V
		$X2; V_I = V_{IH} \text{ or } V_{IL}$								
		I _O = -50 μA; V _{CC} = 2.0 V	1.9	2.0	-	1.9	-	1.9	-	V
		I_{O} = -50 μ A; V_{CC} = 3.0 V	2.9	3.0	-	2.9	-	2.9	-	V
		I _O = -50 μA; V _{CC} = 4.5 V	4.4	4.5	-	4.4	-	4.4	-	V
		I_{O} = -2.0 mA; V_{CC} = 3.0 V	2.58	-	-	2.48	-	2.40	-	V
		I_{O} = -3.0 mA; V_{CC} = 4.5 V	3.94	-	-	3.8	-	3.70	-	V

Symbol	Parameter	Conditions		25 °C		-40 °C	to +85 °C	-40 °C t	o +125 °C	Unit
				Тур	Max	Min	Max	Min	Max	
V_{OL}	LOW-level	Q; $V_I = V_{IH}$ or V_{IL}								
	output voltage	$I_O = 50 \mu A; V_{CC} = 2.0 V$	-	0	0.1	-	0.1	-	0.1	V
		I _O = 50 μA; V _{CC} = 3.0 V	-	0	0.1	-	0.1	-	0.1	V
		$I_O = 50 \mu A; V_{CC} = 4.5 V$	-	0	0.1	-	0.1	-	0.1	V
		$I_O = 4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.36	-	0.44	-	0.55	V
		$I_O = 8.0 \text{ mA}; V_{CC} = 4.5 \text{ V}$	-	-	0.36	-	0.44	-	0.55	V
		$X2; V_I = V_{IH} \text{ or } V_{IL}$								
		I _O = 50 μA; V _{CC} = 2.0 V	-	0	0.1	-	0.1	-	0.1	V
		$I_O = 50 \mu A; V_{CC} = 3.0 V$	-	0	0.1	-	0.1	-	0.1	V
		I _O = 50 μA; V _{CC} = 4.5 V	-	0	0.1	-	0.1	-	0.1	V
		$I_O = 2.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.36	-	0.44	-	0.55	V
		I_{O} = 3.0 mA; V_{CC} = 4.5 V	-	-	0.36	-	0.44	-	0.55	V
I _I	input leakage current	X1; V _I = 5.5 V or GND; V _{CC} = 0 V to 5.5 V	-	-	0.1	-	1.0	-	2.0	μΑ
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5 \text{ V}$	-	-	1.0	-	10	-	40	μΑ
Cı	input capacitance	X1	-	3	8	-	8	-	8	pF

11 Dynamic characteristics

Table 7. Dynamic characteristics

GND = 0 V; $t_r = t_f = \le 3.0$ ns. For test circuit see Figure 7. For waveforms see Figure 5 and Figure 6.

Symbol	Parameter	Conditions			25 °C		-40 °C	to +85 °C	-40 °C t	o +125 °C	Unit
				Min	Тур	Max	Min	Max	Min	Max	
t _{pd}	propagation	X1 to X2	[1]								
	delay	V _{CC} = 3.0 V to 3.6 V	[2]								
		C _L = 15 pF		-	3	7	1	11	1	13	ns
		C _L = 50 pF		-	7	13	1	16	1	18	ns
		V _{CC} = 4.5 V to 5.5 V	[3]								
		C _L = 15 pF		-	2	5	1	7	1	9	ns
		C _L = 50 pF		-	6	10	1	11	1	12	ns
		X1 to Q	[1]								
		V _{CC} = 3.0 V to 3.6 V	[2]								
		C _L = 15 pF		-	33	55	1	67	1	78	ns
		C _L = 50 pF		-	35	60	1	71	1	82	ns
		V _{CC} = 4.5 V to 5.5 V	[3]								
		C _L = 15 pF		-	23	36	1	44	1	52	ns
		C _L = 50 pF		-	25	40	1	51	1	58	ns

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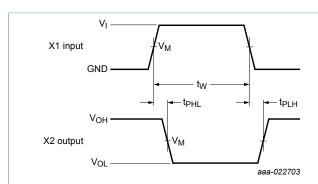
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Symbol	Parameter	Conditions		25 °C		-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Max	
t _W	pulse width	X1 HIGH or LOW								
		V _{CC} = 3.0 V to 3.6 V	4	-	-	5	-	7	-	ns
	V _{CC} = 4.5 V to 5.5 V	3	-	-	4	-	5	-	ns	
f _{max}	maximum	X1								
	frequency	V _{CC} = 3.3 V	125	-	-	100	-	70	-	MHz
		V _{CC} = 5 V	165	-	_	125	-	100	-	MHz
C _{PD}	power dissipation capacitance	C_L = 50 pF; f_i = 1 MHz; [4] V _I = GND to V _{CC}								
		V _{CC} = 3.3 V	-	4	-	-	-	-	-	pF
		V _{CC} = 5 V	-	5	-	-	-	-	-	pF

^[1] t_{pd} is the same as t_{PLH} and t_{PHL} . [2] Typical values are measured at V_{CC} = 3.3 V. [3] Typical values are measured at V_{CC} = 5.0 V. [4] C_{PD} is used to determine the dynamic power dissipation P_D (μ W). P_D = C_{PD} x V_{CC}^2 x f_i + C_L x V_{CC}^2 x f_i /16384 where:

 f_i = input frequency in MHz; C_L = output load capacitance in pF; V_{CC} = supply voltage in Volt.

11.1 Waveforms and test circuit



Measurement points are given in Table 8.

 $\ensuremath{V_{\text{OL}}}$ and $\ensuremath{V_{\text{OH}}}$ are typical output voltage levels that occur with the output load.

Figure 5. Input X1 to output X2 propagation delay times

X1 input

GND

V_I

V_M

V_{OH}

V_{OH}

V_{OL}

Aaa-022704

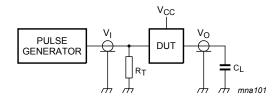
Measurement points are given in Table 8.

 $\ensuremath{V_{OL}}$ and $\ensuremath{V_{OH}}$ are typical output voltage levels that occur with the output load.

Figure 6. Input X1 to output Q propagation delay times

Table 8. Measurement points

Inputs		Output
V _I	V _M	V _M
GND to V _{CC}	0.5 x V _{CC}	0.5 x V _{CC}



Test data is given in <u>Table 7</u>. Definitions for test circuit:

C_L = Load capacitance including jig and probe capacitance.

 R_T = Termination resistance should be equal to output impedance Z_0 of the pulse generator.

Figure 7. Test circuit for measuring switching times

12 Crystal oscillator

12.1 Typical crystal oscillator circuit

A typical crystal oscillator schematic is shown in <u>Figure 8</u>. R1 is the power limiting resistor, its value depends on the frequency and required stability against changes in V_{CC} or average I_{CC} . For starting and maintaining oscillation a minimum transconductance is necessary, so R1 should not be too large. A practical value for R1 is 2.2 k Ω .

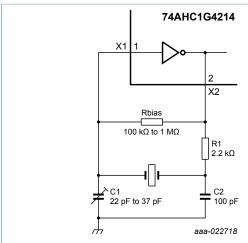
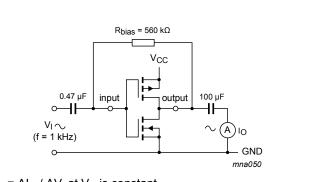
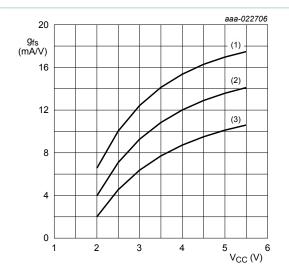


Figure 8. External component connection for a crystal oscillator



 $g_{fs} = \Delta I_O / \Delta V_I$ at V_O is constant. See also Figure 10.

Figure 9. Test set-up for measuring forward transconductance



 $T_{amb} = 25 \, ^{\circ}C.$

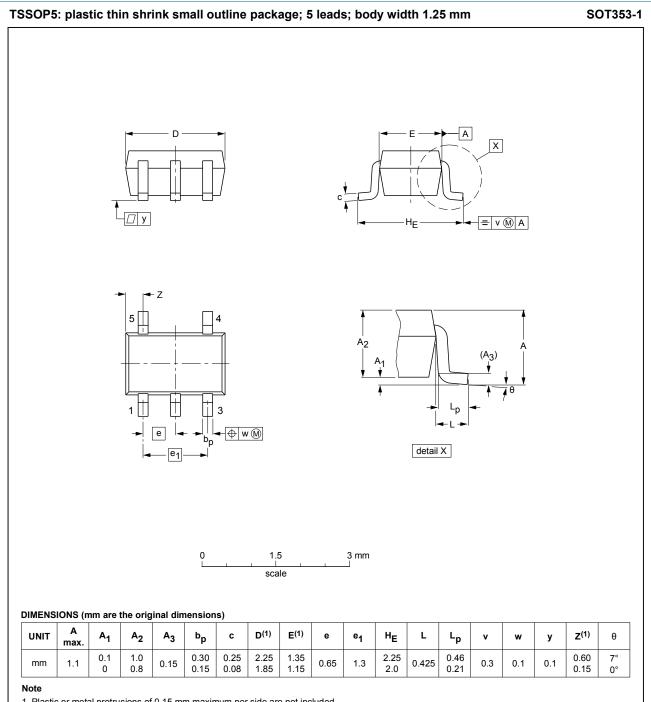
- (1) Maximum.
- (2) Typical.
- (3) Minimum.

Figure 10. Typical forward transconductance as function of the supply voltage

74AHC1G4214

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13 Package outline



1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.

OUTLINE		EUROPEAN	ISSUE DATE					
VERSION	IEC	JEDEC	JEITA		PROJECTION ISSUE DATI			
SOT353-1		MO-203	SC-88A			-00-09-01 03-02-19		

Figure 11. Package outline SOT353-1 (TSSOP5)

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14 Abbreviations

Table 9. Abbreviations

Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model

15 Revision history

Table 10. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
74AHC1G4214 v.3	20180426	Product data sheet	-	74AHC1G4214 v.2	
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. 				
74AHC1G4214 v.2	20161026	Product data sheet	-	74AHC1G4214 v.1	
Modifications:	Type number 74AHC1G4214GM removed.				
74AHC1G4214 v.1	20160415	Product data sheet	-	-	

16 Legal information

16.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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