

# SIOV metal oxide varistors

Leaded varistors, SuperioR, S25 series

Series/Type:B722\*Date:January 2018

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B722

Leaded varistors

#### SuperioR, S25 series

#### Construction

- Round varistor element, leaded
- Coating: epoxy resin, flame-retardant to UL 94 V-0
- Terminals: tinned wire

#### Features

- High-energy SuperioR series E4
- Wide operating voltage range 130 ... 750 V<sub>RMS</sub>
- All types duty cycle @ 6 kV/ 3 kA = >10 pulses, according to IEC 62368-1; G.8.2 and IEC 60950-1; Annex Q, IEC 61051-2
- UL approval to UL 1449 (file number E321126 exception 580 V)
- Very high surge current rating up to 20 kA

#### Approvals

- UL UL
- CSA
- IEC
- VDE

#### **Delivery mode**

- Bulk (standard)
- For further details refer to chapter "Taping, packaging and lead configuration" for leaded varistors.

#### General technical data

Climatic category	to IEC 60068-1	40/105/56	
Operating temperature	to IEC 61051	-40 +105	°C
Storage temperature		-40 +125	°C
Electric strength	to IEC 61051	≥ 2.5	kV <sub>RMS</sub>
Insulation resistance	to IEC 61051	≥ 100	MΩ



SuperioR, S25 series

# B722\*

## Electrical specifications and ordering codes

Maximum ratings (T<sub>A</sub> = 105 °C)

Ordering code	Туре	$V_{\text{RMS}}$	V <sub>DC</sub>	i <sub>max</sub>	<b>I</b> <sub>n</sub> <sup>1)</sup>	W <sub>max</sub>	P <sub>max</sub>
	(untaped)			(8/20 µs)	(8/20 µs)	(2 ms)	
	SIOV-			1 time	15 times		
		V	V		Α	J	W
				A			
B72225S4131K101	S25K130E4R12	130	170	20000	10000	185	1.0
B72225S4141K101	S25K140E4R12	140	180	20000	10000	195	1.0
B72225S4151K101	S25K150E4R12	150	200	20000	10000	215	1.0
B72225S4171K101	S25K175E4R12	175	225	20000	10000	245	1.0
B72225S4231K101	S25K230E4R12	230	300	20000	10000	315	1.0
B72225S4251K101	S25K250E4R12	250	320	20000	10000	345	1.0
B72225S4271K101	S25K275E4R12	275	350	20000	10000	375	1.0
B72225S4301K101	S25K300E4R12	300	385	20000	10000	410	1.0
B72225S4321K101	S25K320E4R12	320	420	20000	10000	445	1.0
B72225S4381K101	S25K385E4R12	385	505	20000	10000	600	1.0
B72225S4421K101	S25K420E4R12	420	560	20000	10000	700	1.0
B72225S4441K101	S25K440E4R12	440	585	20000	10000	710	1.0
B72225S4461K101	S25K460E4R12	460	615	20000	10000	720	1.0
B72225S4511K101	S25K510E4R12	510	670	20000	10000	750	1.0
B72225S4551K101	S25K550E4R12	550	745	20000	10000	780	1.0
B72225S4581K101	S25K580E4R12	580	780	20000	10000	800	1.0
B72225S4621K101	S25K625E4R12	625	825	20000	10000	855	1.0
B72225S4681K101	S25K680E4R12	680	895	20000	10000	940	1.0
B72225S4751K101	S25K750E4R12	750	1060	20000	10000	1025	1.0

 $^{1)}$  Note: Nominal discharge current  $I_n$  according to UL 1449,  $4^{th}$  edition.



B722\*

Leaded varistors

SuperioR, S25 series

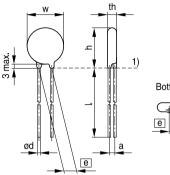
### Characteristics ( $T_A = 25 \ ^{\circ}C$ )

Ordering code	V <sub>v</sub>	$\Delta V_{v}$	V <sub>c,max</sub>	i <sub>c</sub>	C <sub>typ</sub>
-	(1 mA)	(1 mA)	(i <sub>c</sub> )		(1 kHz)
	V	%	V	А	pF
B72225S4131K101	205	±10	340	150	3800
B72225S4141K101	220	±10	360	150	3550
B72225S4151K101	240	±10	395	150	3250
B72225S4171K101	270	±10	455	150	2900
B72225S4231K101	360	±10	595	150	2250
B72225S4251K101	390	±10	650	150	2100
B72225S4271K101	430	±10	710	150	1900
B72225S4301K101	470	±10	775	150	1750
B72225S4321K101	510	±10	840	150	1600
B72225S4381K101	620	±10	1025	150	1250
B72225S4421K101	680	±10	1120	150	1150
B72225S4441K101	715	±10	1180	150	1100
B72225S4461K101	750	±10	1240	150	1050
B72225S4511K101	820	±10	1355	150	950
B72225S4551K101	910	±10	1500	150	860
B72225S4581K101	940	±10	1580	150	830
B72225S4621K101	1000	±10	1650	150	780
B72225S4681K101	1100	±10	1815	150	710
B72225S4751K101	1200	±10	2000	150	650



#### SuperioR, S25 series

### **Dimensional drawings**



Bottom view

VAR0408-C-E

1) Seating plane to IEC 60717

Dimensions

## Weight

Nominal diameter	V <sub>RMS</sub>	Weight
mm	V	g
25	130 750	5.2 22.2

B722\*

The weight of varistors in between these voltage classes can be interpolated.

Ordering code	[e] ±1	a (typical)	W <sub>max</sub>	th <sub>max</sub>	h <sub>max</sub>	I <sub>min</sub>	d ±0.05
-	mm	mm	mm	mm	mm	mm	mm
B72225S4131K101	12.7	3.4	27.5	5.7	31.0	25.0	1.0
B72225S4141K101	12.7	3.5	27.5	5.8	31.0	25.0	1.0
B72225S4151K101	12.7	3.6	27.5	5.9	31.0	25.0	1.0
B72225S4171K101	12.7	3.8	27.5	6.1	31.0	25.0	1.0
B72225S4231K101	12.7	4.4	27.5	6.7	31.0	25.0	1.0
B72225S4251K101	12.7	4.6	27.5	6.9	31.0	25.0	1.0
B72225S4271K101	12.7	4.7	27.5	7.2	31.0	25.0	1.0
B72225S4301K101	12.7	4.8	27.5	7.4	31.0	25.0	1.0
B72225S4321K101	12.7	4.9	27.5	7.6	31.0	25.0	1.0
B72225S4381K101	12.7	5.0	27.5	8.3	31.5	25.0	1.0
B72225S4421K101	12.7	5.2	27.5	8.6	31.5	25.0	1.0
B72225S4441K101	12.7	5.4	27.5	8.9	31.5	25.0	1.0
B72225S4461K101	12.7	5.6	27.5	9.1	31.5	25.0	1.0
B72225S4511K101	12.7	5.8	27.5	9.5	31.5	25.0	1.0
B72225S4551K101	12.7	6.0	27.5	10.0	32.0	25.0	1.0
B72225S4581K101	12.7	6.1	27.5	10.2	32.0	25.0	1.0
B72225S4621K101	12.7	6.2	27.5	10.6	32.0	25.0	1.0
B72225S4681K101	12.7	6.3	27.5	11.2	32.5	25.0	1.0
B72225S4751K101	12.7	6.5	27.5	11.7	32.5	25.0	1.0





B722\*

SuperioR, S25 series

## Reliability data

Test	Test methods/conditions	Requirement
Varistor voltage	The voltage between two terminals with the specified measuring current applied is called $V_v$ (1 mA <sub>DC</sub> @ 0.2 2 s).	To meet the specified value
Clamping voltage	The maximum voltage between two terminals with the specified standard impulse current (8/20 µs) applied.	To meet the specified value
Endurance at upper category temperature	1000 h at UCT After having continuously applied the maximum allowable AC voltage at UCT $\pm 2$ °C for 1000 h, the specimen shall be stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of V <sub>v</sub> shall be measured.	ΙΔV/V (1 mA)Ι ≤10%
Surge current derating, 8/20 μs	10 surge currents (8/20 $\mu$ s), unipolar, interval 30 s, amplitude corresponding to derating curve for 10 impulses at 20 $\mu$ s	I∆V/V (1 mA)I ≤10% (measured in direction of surge current) No visible damage
Surge current derating, 2 ms	10 surge currents (2 ms), unipolar, interval 120 s, amplitude corresponding to derating curve for 10 impulses at 2 ms	I∆V/V (1 mA)I ≤10% (measured in direction of surge current) No visible damage
Electric strength	IEC 61051-1, test 4.9.2 Metal balls method, 2500 V <sub>RMS</sub> , 60 s	No breakdown
	The varistor is placed in a container holding $1.6 \pm 0.2$ mm diameter metal balls such that only the terminations of the varistor are protruding. The specified voltage shall be applied between both terminals of the specimen connected together and the electrode inserted between the metal balls.	



#### SuperioR, S25 series

Test	Test methods/conditions	Requirement
Climatic sequence	The specimen shall be subjected to: a) dry heat at UCT, 16 h, IEC 60068-2-2, test Ba b) damp heat, 1st cycle: $55 ^{\circ}$ C, 93% r. H., 24 h, IEC 60068-2-30, test Db c) cold, LCT, 2 h, IEC 60068-2-1, test Aa d) damp heat, additional 5 cycles: $55 ^{\circ}$ C/25 $^{\circ}$ C, 93% r. H., 24 h/cycle, IEC 60068-2-30, test Db.	IΔV/V (1 mA)I ≤10% R <sub>ins</sub> ≥100 MΩ
	Then the specimen shall be stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of $V_v$ shall be measured. Thereafter, insulation resis- tance $R_{ins}$ shall be measured at V = 500 V.	
Rapid change of temperature	IEC 60068-2-14, test Na, LCT/UCT, dwell time 30 min, 5 cycles	l∆V/V (1 mA)l ≤5% No visible damage
Damp heat, steady state	IEC 60068-2-78, test Ca The specimen shall be subjected to $40 \pm 2 ^{\circ}$ C, 90 to 95% r. H. for 56 days without load / with 10% of the maxi- mum continuous DC operating voltage V <sub>DC</sub> . Then stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of V <sub>V</sub> shall be measured. Thereafter, insulation resis- tance R <sub>ins</sub> shall be measured at V = 500 V (insulated varistors only).	l∆V/V (1 mA)l ≤10% R <sub>ins</sub> ≥100 MΩ



B722\*





## Leaded varistors SuperioR, S25 series

B722\*

Test	Test methods/conditions	Requirement
Solderability	IEC 60068-2-20, test Ta,	The inspection shall be
	method 1 with modified conditions for lead-free solder alloys: 245 °C, 3 s:	carried out under adequate light with normal eyesight or
	After dipping the terminals to a depth of approximately 3 mm from the body in a soldering bath of 245 °C for 3 s, the terminals shall be visually examined.	with the assistance of a magnifier capable of giving a magnification of 4 to 10 times. The dipped surface shall be covered with a smooth and bright solder coating with no more than small amounts of scattered imperfections such as pinholes or un-wetted or de-wetted areas. These imperfections shall not be concentrated in one area.
Resistance to soldering	IEC 60068-2-20, test Tb, method 1A,	l∆V/V (1 mA)l ≤5%
heat	260 °C, 10 s:	No visible damage
	Each lead shall be dipped into a solder bath having a temperature of $260 \pm 5$ °C to a point 2.0 to 2.5 mm from the body of the specimen, be held there for $10 \pm 1$ s and then be stored at room temperature and normal humidity for 1 to 2 h. The change of V <sub>V</sub> shall be measured and the specimen shall be visually examined.	
Tensile strength	IEC 60068-2-21, test Ua1	l∆V/V (1 mA)l ≤5%
	After gradually applying the force specified below and keeping the unit fixed for 10 s, the terminal shall be visually examined for any damage.	No break of solder joint, no wire break
	Force for wire diameter: 0.6 mm = 10 N 0.8 mm = 10 N 1.0 mm = 20 N	



B722\*

Leaded varistors

#### SuperioR, S25 series

Test	Test methods/conditions	Requirement
Vibration	IEC 60068-2-6, test Fc, method B4	l∆V/V (1 mA)l ≤5%
	$\begin{array}{llllllllllllllllllllllllllllllllllll$	No visible damage
Bump	IEC 60068-2-29, test Eb Pulse duration: 6 ms Max. acceleration: 400 m/s <sup>2</sup> Number of bumps: 4000 Pulse: half sine	l∆V/V (1 mA)l ≤5% No visible damage
Fire hazard	IEC 60695-11-5 (needle flame test) Severity: vertical 10 s	5 s max.

#### Note:

UCT = Upper category temperature

LCT = Lower category temperature

R<sub>ins</sub> = Insulation resistance

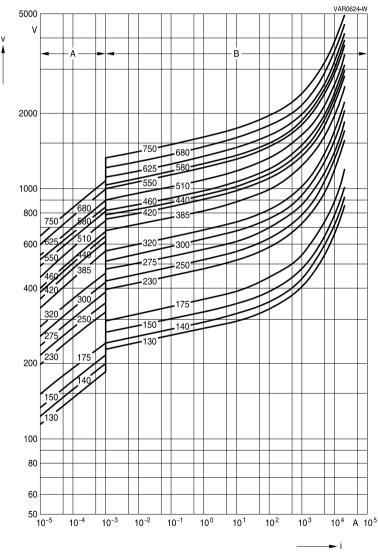


B722<sup>9</sup>

Leaded varistors SuperioR, S25 series

#### v/i characteristics

v = f (i) - for explanation of the characteristics refer to "General technical information", 1.6.3 A = Leakage current, B = Protection level } for worst-case varistor tolerances



SIOV-S25 ... E4R12

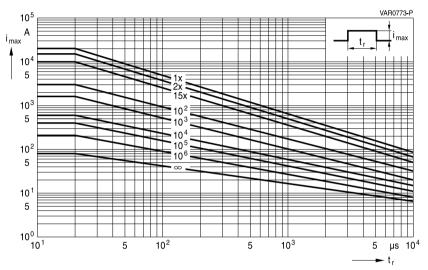
Please read *Cautions and warnings* and *Important notes* at the end of this document.



#### **Derating curves**

Maximum surge current  $i_{max} = f(t_r, pulse train)$ 

For explanation of the derating curves refer to "General technical information", section 1.8.1



SIOV-S25K130 ... K750E4R12



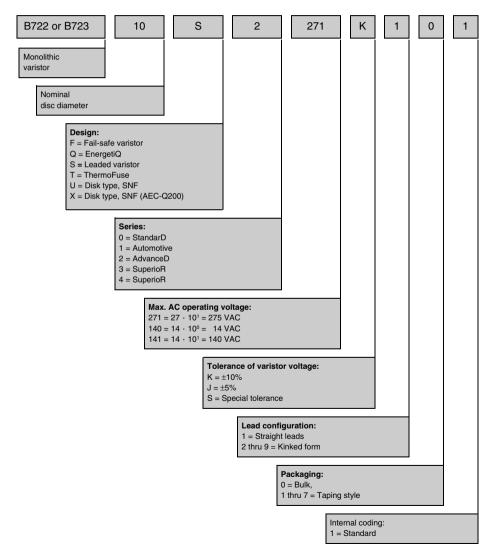
B722'



#### Taping, packaging and lead configuration

#### 1 EPCOS ordering code system

#### For leaded varistors

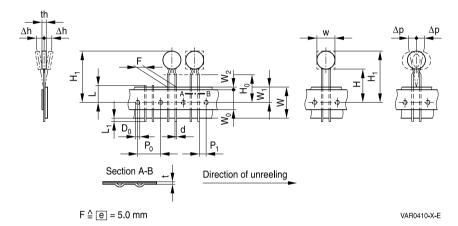




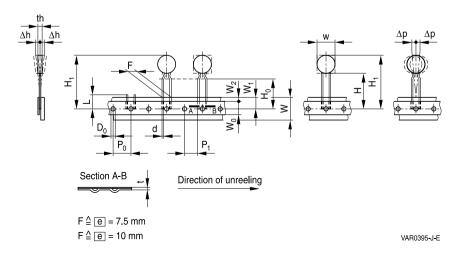
#### 2 Taping and packaging of leaded varistors

Tape packaging for lead spacing  $\boxed{e} = 5$  fully conforms to IEC 60286-2, while for lead spacings  $\boxed{e} = 7.5$  and 10 the taping mode is based on this standard.

#### 2.1 Taping in accordance with IEC 60286-2 for lead spacing 5.0 mm



#### 2.2 Taping based on IEC 60286-2 for lead spacing 7.5 and 10 mm





B722'



## Leaded varistors

SuperioR, S25 series

## 2.3 Tape dimensions (in mm)

Sym-	<i>e</i> = 5.0	Tolerance	<i>e</i> = 7.5	Tolerance	<i>e</i> = 10.0	Tolerance	Remarks
bol	0.0	reletance		1 oloranoo		reletance	
w		max.		max.		max.	see tables in
							each series
th		max.		max.		max.	under
							"Dimensions"
d	0.6	±0.05	0.8	±0.05	1.0	±0.05	
P <sub>0</sub>	12.7	±0.3	12.7 <sup>1)</sup>	±0.3	12.7	±0.3	±1 mm/20
							sprocket holes
P <sub>1</sub>	3.85	±0.7	8.95	±0.8	7.7	±0.8	
F	5.0	+0.6/-0.1	7.5	±0.8	10.0	±0.8	
Δh	0	±2.0	depends of	ns	depends on	S	measured at
Δp	0	±1.3	0	±2.0	0	±2.0	top of compo-
							nent body
W	18.0	±0.5	18.0	±0.5	18.0	±0.5	
$W_{0}$	5.5	min.	11.0	min.	11.0	min.	Peel-off
							force ≥ 5 N
$W_1$	9.0	±0.5	9.0	+0.75/-0.5	9.0	+0.75/-0.5	
$W_2$	3.0	max.	3.0	max.	3.0	max.	
Н	18.0	+2.0/-0	18.0	+2.0/-0	18.0	+2.0/-0	2)
H₀	16.0	±0.5	16.0	±0.5	16.0	±0.5	3)
	(18.0)		(18.0)				
H <sub>1</sub>	32.2	max.	45.0	max.	45.0	max.	
D <sub>0</sub>	4.0	±0.2	4.0	±0.2	4.0	±0.2	
t	0.9	max.	0.9	max.	0.9	max.	without lead
L	11.0	max.	11.0	max.	11.0	max.	
L <sub>1</sub>	0.5	max.					

1) Taping with  $P_0 = 15.0$  mm upon request

2) Applies only to uncrimped types

Applies only to crimped types (H<sub>0</sub> = 18 upon request)

SuperioR, S25 series

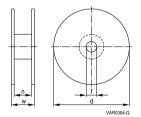
#### 2.4 Taping mode

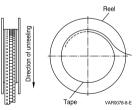
Example: B72210S0271K151

Digit 14

District	<b>T</b>	Destaurs			Disels all stars as
Digit 14	Taping	Reel type	Seating plane height H <sub>0</sub>	Seating plane height H	Pitch distance
	mode		for crimped types	for uncrimped types	Po
			mm	mm	mm
0	-	Bulk	-	-	-
1	G	I	16	18	12.7
2	G2	I	18	-	12.7
3	G3	П	16	18	12.7
4	G4	П	18	-	12.7
5	G5	Ш	16	18	12.7
6	GA	Ammo pack	16	18	12.7
7	G2A	Ammo pack	18	-	12.7
Internal	coding fo	r special tapin	g		
	G6	III	18	-	12.7
	G10	П	16	18	15.0
	G11	П	18	-	15.0
	G10A	Ammo pack	16	18	15.0
	G11A	Ammo pack	18	-	15.0

#### 2.5 Reel dimension





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B722'

#### Dimensions (in mm)

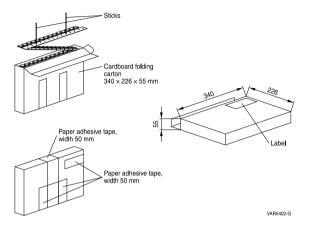
Reel type	d	f	n	W
I	360 max.	31 ±1	approx. 45	54 max.
II	360 max.	31 ±1	approx. 55	64 max.
<u>III</u>	500 max.	23 ±1	approx. 59	72 max.

If reel type III is not compatible with insertion equipment because of its large diameter, nominal disk diameter 10 mm and 14 mm can be supplied on reel II upon request (taping mode G3).





#### 2.6 Ammo pack dimensions



#### 3 Lead configuration

Straight leads are standard for disk varistors. Other lead configurations as crimp style or customer-specific lead wire length according to 3.1, 3.2, 3.3 and 3.4 are optional. Crimped leads (non-standard) are differently crimped for technical reasons; the individual crimp styles are denoted by consecutive numbers (S, S2 through S5) as shown in the dimensional drawings below.

The crimp styles of the individual types can be seen from the type designation in the ordering tables.

#### 3.1 Crimp style mode

Example: B72210S0271K 5 01

Digit 13

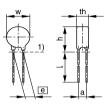
Digit 13 of ordering code	Crimp style	Figure	
1	Standard, straight leads	1	
2	S2	2	
3	S3	3	
5	S5	4	
Available upon request			
Internal coding	-	5	



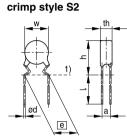
#### 3.2 Standard leads and non-standard crimp styles

The basic dimensions in figure 1 to 5 are valid for types with either round or square (EnergetiQ series) component head.

#### Standard, straight leads



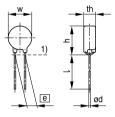
1) Seating plane to IEC 717 VAR0586-W-E



Non-standard,

1) Seating plane to IEC 60717 VAR0411-F-E

Non-standard, crimp style S3



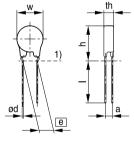
1) Seating plane to IEC 60717 VAR0396-R-E

Figure 3

#### Figure 1

Figure 2

#### Non-standard, crimp style S5



1) Seating plane to IEC 60717 VAR0726-M-E

Figure 4



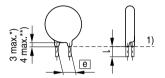
B722'

Leaded varistors SuperioR, S25 series

#### Trimmed leads (non-standard) 3.3

Varistors with cut leads available upon request.

Lead length tolerances:	
Straight leads	+/-0.8 mm
Crimped leads	+/-0.5 mm
Minimum lead length	3.0 mm



Seating plane to IEC 60717
For round component head
For EnergetiQ series, square component head

VAR0642-U-E

Figure 5



B722'

Leaded varistors

SuperioR, S25 series

#### Cautions and warnings

#### General

- EPCOS metal oxide varistors are designed for specific applications and should not be used for purposes not identified in our specifications, application notes and data books unless otherwise agreed with EPCOS during the design-in-phase.
- 2. Ensure suitability of SIOVs through reliability testing during the design-in phase. SIOVs should be evaluated taking into consideration worst-case conditions.
- 3. For applications of SIOVs in line-to-ground circuits based on various international and local standards there are restrictions existing or additional safety measures required.

#### Storage

- 1. Store SIOVs only in original packaging. Do not open the package prior to processing.
- Recommended storage conditions in original packaging: Storage temperature: -25 °C ... +45 °C, Relative humidity: <75% annual average, <95% on maximum 30 days a year. Dew precipitation: is to be avoided.
- 3. Avoid contamination of an SIOV's during storage, handling and processing.
- 4. Avoid storage of SIOVs in harmful environments that can affect the function during long-term operation (examples given under operation precautions).
- 5. The SIOV type series should be soldered after shipment from EPCOS within the time specified:

SIOV-S, -Q, -LS, -B, -SNF 24 months ETFV/ T series, -CU 12 months.

#### Handling

- 1. SIOVs must not be dropped.
- 2. Components must not be touched with bare hands. Gloves are recommended.
- 3. Avoid contamination of the surface of SIOV electrodes during handling, be careful of the sharp edge of SIOV electrodes.

#### Soldering (where applicable)

- 1. Use rosin-type flux or non-activated flux.
- 2. Insufficient preheating may cause ceramic cracks.
- 3. Rapid cooling by dipping in solvent is not recommended.
- 4. Complete removal of flux is recommended.
- Temperatures of all preheat stages and the solder bath must be strictly controlled especially for T series (T14 and T20).



**B722** 



Leaded varistors SuperioR, S25 series

#### Mounting

- 1. Potting, sealing or adhesive compounds can produce chemical reactions in the SIOV ceramic that will degrade the component's electrical characteristics.
- 2. Overloading SIOVs may result in ruptured packages and expulsion of hot materials. For this reason SIOVs should be physically shielded from adjacent components.

#### Operation

- 1. Use SIOVs only within the specified temperature operating range.
- 2. Use SIOVs only within the specified voltage and current ranges.
- Environmental conditions must not harm SIOVs. Use SIOVs only in normal atmospheric conditions. Avoid use in deoxidizing gases (chlorine gas, hydrogen sulfide gas, ammonia gas, sulfuric acid gas etc), corrosive agents, humid or salty conditions.Contact with any liquids and solvents should be prevented.

#### Display of ordering codes for EPCOS products

The ordering code for one and the same EPCOS product can be represented differently in data sheets, data books, other publications, on the EPCOS website, or in order-related documents such as shipping notes, order confirmations and product labels. The varying representations of the ordering codes are due to different processes employed and do not affect the specifications of the respective products. Detailed information can be found on the Internet under www.epcos.com/orderingcodes



SuperioR, S25 series

B722\*

#### Symbols and terms

Symbol	Term
С	Capacitance
C <sub>typ</sub>	Typical capacitance
i	Current
i <sub>c</sub>	Current at which $V_{c, max}$ is measured
I <sub>leak</sub>	Leakage current
i <sub>max</sub>	Maximum surge current (also termed peak current)
I <sub>max</sub>	Maximum discharge current
l <sub>n</sub>	Nominal discharge current to UL 1449
LCT	Lower category temperature
L <sub>typ</sub>	Typical inductance
P <sub>max</sub>	Maximum average power dissipation
R <sub>ins</sub>	Insulation resistance
R <sub>min</sub>	Minimum resistance
T <sub>A</sub>	Ambient temperature
t <sub>r</sub>	Duration of equivalent rectangular wave
UCT	Upper category temperature
v	Voltage
$V_{clamp}$	Clamping voltage
V <sub>c, max</sub>	Maximum clamping voltage at specified current $i_{\rm c}$
V <sub>DC</sub>	DC operating voltage
$V_{jump}$	Maximum jump start voltage
V <sub>max</sub>	Maximum voltage
V <sub>op</sub>	Operating voltage
V <sub>RMS</sub>	AC operating voltage, root-mean-square value
$V_{\text{RMS, op, max}}$	Root-mean-square value of max. DC operating voltage incl. ripple current
V <sub>surge</sub>	Super imposed surge voltage
Vv	Varistor voltage
$\Delta V_V$	Tolerance of varistor voltage
$W_{LD}$	Maximum load dump
W <sub>max</sub>	Maximum energy absorption
e	Lead spacing

All dimensions are given in mm.

The commas used in numerical values denote decimal points.



The following applies to all products named in this publication:

- 1. Some parts of this publication contain statements about the suitability of our products for certain areas of application. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application. As a rule we are either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether a product with the properties described in the product specification is suitable for use in a particular customer application.
- 2. We also point out that in individual cases, a malfunction of electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified. In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health (e.g. in accident prevention or life-saving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of an electronic component.
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Important notes

8. The trade names EPCOS, CeraCharge, CeraDiode, CeraLink, CeraPad, CeraPlas, CSMP, CTVS, DeltaCap, DigiSiMic, ExoCore, FilterCap, FormFit, LeaXield, MiniBlue, MiniCell, MKD, MKK, MotorCap, PCC, PhaseCap, PhaseCube, PhaseMod, PhiCap, PowerHap, PQSine, PQvar, SIFERRIT, SIFI, SIKOREL, SilverCap, SIMDAD, SiMic, SIMID, SineFormer, SIOV, ThermoFuse, WindCap are trademarks registered or pending in Europe and in other countries. Further information will be found on the Internet at www.tdk-electronics.tdk.com/trademarks.

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