

SOT23 P-CHANNEL ENHANCEMENT MODE VERTICAL DMOS FET
Product Summary

BV_{DSS}	$R_{DS(ON)}$ max	I_D max
-100V	20 Ω @ $V_{GS} = -10V$	-75mA

Description and Applications

This MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Load Switching

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)**
- An Automotive-Compliant Part is Available Under Separate Datasheet ([ZVP3310FQ](#))**

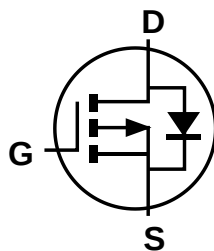
Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)

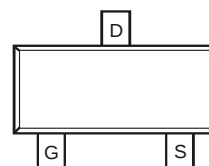
SOT23



Top View



Internal Schematic

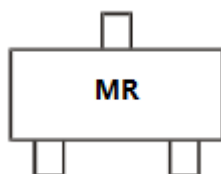


Top View

Ordering Information (Note 4)

Part Number	Case	Packaging
ZVP3310FTA	SOT23	3000/Tape & Reel

- Notes:
- No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 - See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 - Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 - For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information


MR = Product Type Marking Code

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	-100	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current	I _D	-75	mA
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	-1.2	A
Pulsed Source Current (10µs Pulse, Duty Cycle = 1%)	I _{SM}	-1.2	A

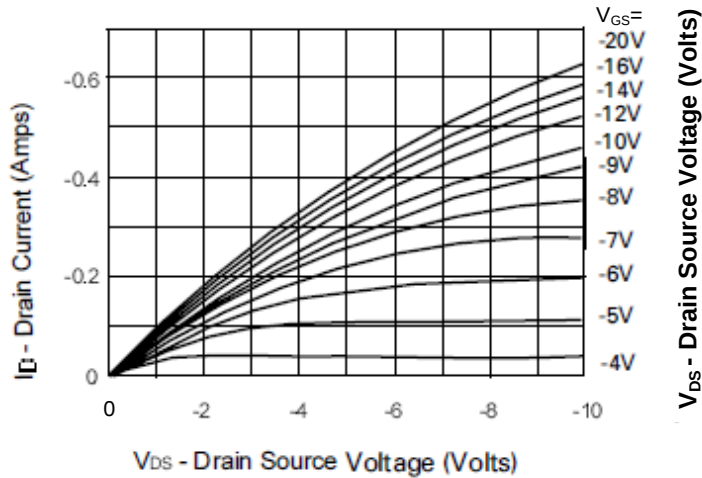
Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (@T _A = +25°C)	P _D	330	mW
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

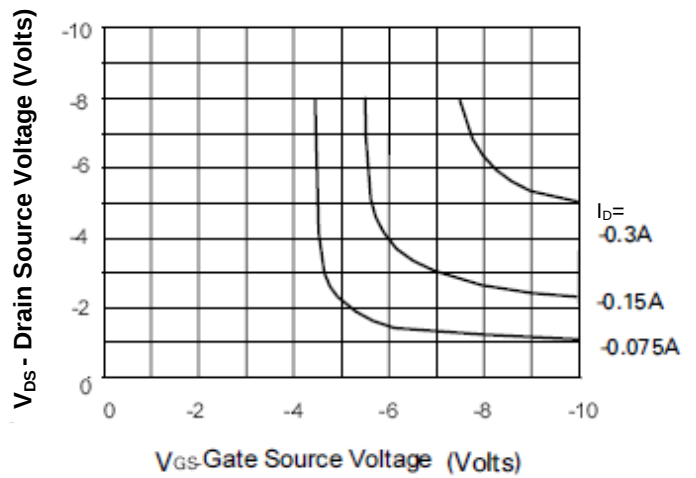
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BV _{DSS}	-100	—	—	V	V _{GS} = 0V, I _D = -1mA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	-1	µA	V _{DS} = -100V, V _{GS} = 0V
		—	—	-50	µA	V _{DS} = -80V, V _{GS} = 0V, T = +125°C
Gate-Source Leakage	I _{GSS}	—	—	±20	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	V _{GS(TH)}	-1.5	—	-3.5	V	V _{DS} = V _{GS} , I _D = -1mA
Static Drain-Source On-Resistance (Note 5)	R _{DS(ON)}	—	—	20	Ω	V _{GS} = -10V, I _D = -150mA
On-State Drain Current (Note 5)	I _{D(ON)}	-300	—	—	mA	V _{DS} = -25V, V _{GS} = -10V
Forward Transconductance (Note 5)	g _{fs}	50	—	—	mS	V _{DS} = -25V, I _D = -150mA
DYNAMIC CHARACTERISTICS (Note 7)						
Input Capacitance	C _{iss}	—	—	50	pF	V _{DS} = -25V, V _{GS} = 0V, f = 1MHz
Output Capacitance	C _{oss}	—	—	15		
Reverse Transfer Capacitance	C _{rss}	—	—	5		
Turn-On Delay Time	t _{D(ON)}	—	—	8	ns	V _{DD} = -25V, I _D = -150mA
Turn-On Rise Time	t _r	—	—	8		
Turn-Off Delay Time	t _{D(OFF)}	—	—	8		
Turn-Off Fall Time	t _f	—	—	8		

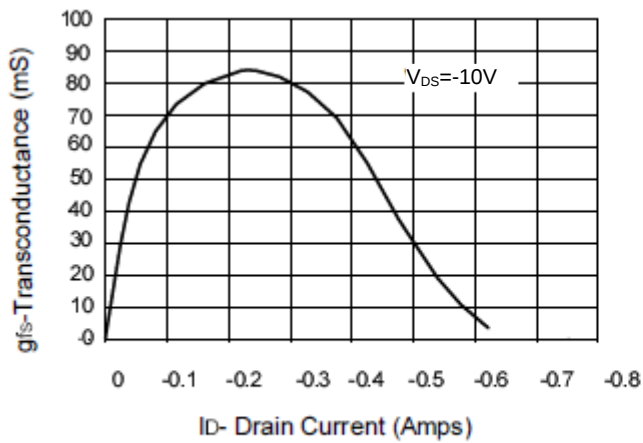
Notes: 5. Measured under pulsed conditions. Width = 300ms. Duty cycle ≤2%.
6. Short duration pulse test used to minimize self-heating effect.
7. Guaranteed by design. Not subject to product testing.



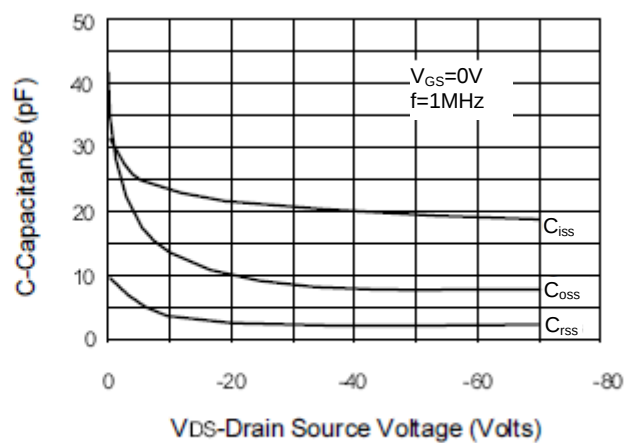
Saturation Characteristics



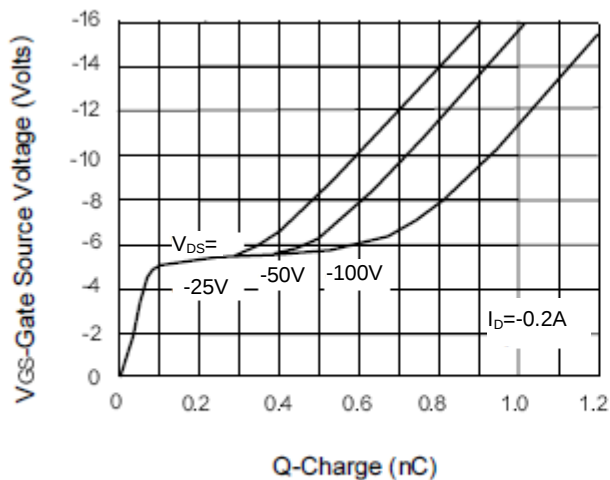
Voltage Saturation Characteristics



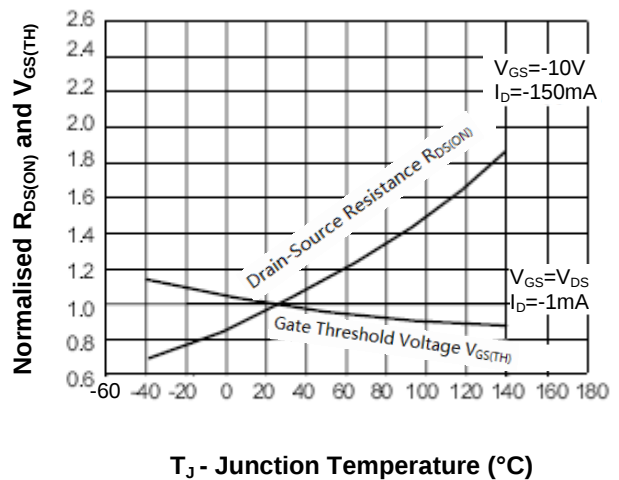
Transconductance v Drain Current



Capacitance v Drain-Source Voltage



Gate Charge v Gate-Source Voltage

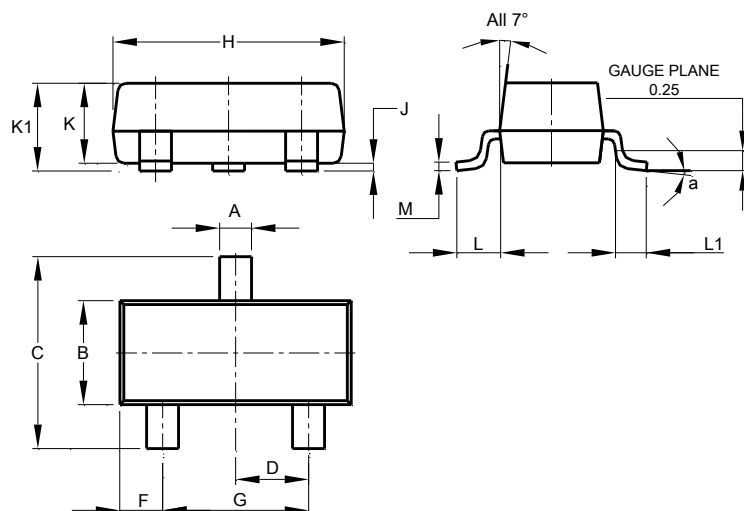


**T_J - Junction Temperature ($^{\circ}C$)
Normalised $R_{DS(on)}$ and $V_{GS(th)}$ v Temperature**

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT23

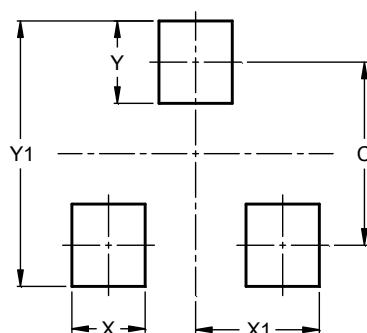


SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT23



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9

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