

Features

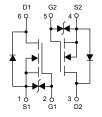
- High Density Cell Design For Low R_{DS(ON)}
- · Voltage Controlled Small Signal Switch
- High Saturation Current Capability
- Epoxy Meets UL 94 V-0 Flammability Rating
- · Moisture Sensitivity Level 1
- Halogen Free Available Upon Request By Adding Suffix "-HF"
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

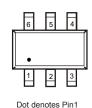
Maximum Ratings

- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature: -55°C to +150°C
- Thermal Resistance: 833°C/W Junction to Ambient

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	60	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current	I _D	340	mA
Total Power Dissipation	P _D	150	mW

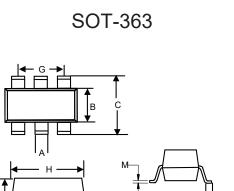
Circuit and Pin Schematic





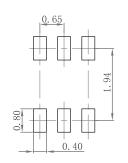
Marking:72K

DUAL N-CHANNEL MOSFET



	DIMENSIONS					
DIM INCHES		HES	MM		NOTE	
DIIVI	MIN	MAX	MIN	MAX	INOTE	
Α	0.006	0.014	0.15	0.35		
В	0.045	0.053	1.15	1.35		
С	0.079	0.096	2.00	2.45		
D	0.026		0.65		TYP.	
G	0.047	0.055	1.20	1.40		
Н	0.071	0.087	1.80	2.20		
J		0.004		0.10		
K	0.031	0.043	0.80	1.10		
L	0.010	0.018	0.26	0.46		
М	0.003	0.006	0.08	0.15		

SUGGESTED SOLDER PAD LAYOUT





ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

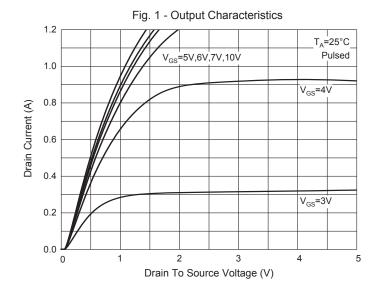
Parameter	Symbol	Test conditions	Min	Тур	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} =0V, I_D =250 μ A	60			V
Gate-Threshold Voltage ⁽¹⁾	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=1$ mA	1.0		2.5	V
Gate-Body Leakage	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±10	μΑ
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =48V, V _{GS} =0V			1	μA
Drain-Source On-Resistance ⁽¹⁾	В	V _{GS} =10V, I _D =500mA			5	
	R _{DS(on)}	V _{GS} =4.5V, I _D =200mA			5.3	Ω
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =300mA			1.5	V
Recovered Charge	Q _r	V_{GS} =0V, I_S =300mA, V_R =25V, dls/dt=-100A/ μ s		30		nC
Dynami Characteristics ⁽²⁾						
Input Capacitance	C _{iss}				40	
Output Capacitance	C _{oss}	V_{DS} =10V, V_{GS} =0V, f=1MHz			30	pF
Reverse Transfer Capacitance	C _{rss}				10	
Switching Characteristics ⁽²⁾						
Turn-On Delay Time	t _{d(on)}	V_{DD} =50V, V_{GS} =10V, R_{L} =250 Ω ,			10	
Turn-Off Delay Time	t _{d(off)}	R_{GS} =50 Ω , R_{GEN} =50 Ω			15	ns
Reverse Recovery Time	t _{rr}	V_{GS} =0V, I_{S} =300mA, V_{R} =25V, dls/dt=-100A/ μ s		30		
Gate-Source Zener Diode						
Gate-Source Breakdown Voltage	BV _{GSO}	I _{gs} =±1mA(Oper Drain)	±21.5		±30	V

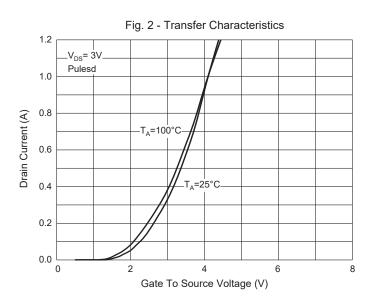
Note: 1. Pulse Test: Pulse Width ${\leqslant}300\mu\text{s},$ Duty Cycle ${\leqslant}2\%.$

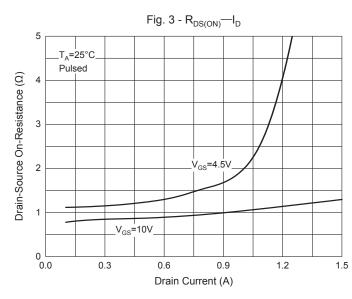
2. These Parameters Have No Way to Verify.

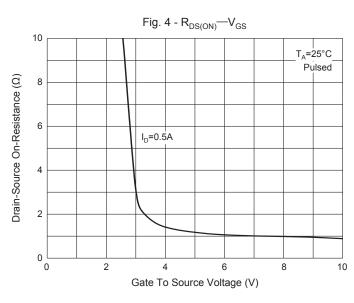


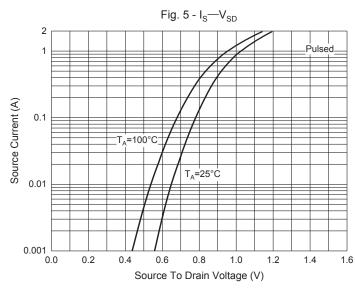
Curve Characteristics

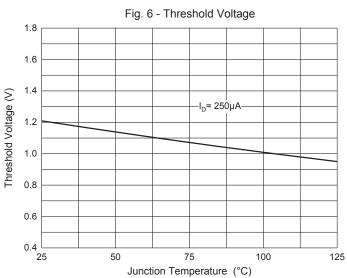














Ordering Information

Device	Packing
Part Number-TP	Tape&Reel:3Kpcs/Reel

Note: Adding "-HF" Suffix For Halogen Free, eg. Part Number-TP-HF

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Rev.3-1-01012019 4/4 MCCSEMI.COM

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