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December 2010

NC7SVL08 TinyLogic[®] Low-I_{CCT} Two-Input AND Gate

Features

- 0.9V to 3.6V V_{CC} Supply Operation
- 3.6V Over-Voltage Tolerant I/Os at V_{CC} from 0.9V to 3.6V
- Power-Off High-Impedance Inputs and Outputs
- Proprietary Quiet Series[™] Noise / EMI Reduction Circuitry
- Ultra-Small MicroPak™ Packages
- Ultra-Low Dynamic Power

Description

The NC7SVL08 is a single two-input AND gate with a low-l_{CCT} input design from Fairchild's Ultra-Low Power (ULP-A) series of TinyLogic $^{\tiny \$}$. The NC7SVL08 features very low quiescent current, even when the input voltage is lower than the V_{CC} supply. This feature services mobile handset applications very well, allowing for direct interface with baseband processor general-purpose I/Os. Since mobile devices rely on a battery supply, the NC7SVL08 facilitates lower power consumption in mixed-voltage rail environments.

This product is designed on an advanced CMOS technology for a wide low-voltage operating range (0.9V to 3.6V $V_{\rm CC}$), high drive needs (up to 24mA), and speed (maximum propagation delay of 3.5ns, $V_{\rm CC}$ =3.3V). It achieves this performance while maintaining low CMOS power dissipation.

Ordering Information

Part Number	Top Mark	Package	Packing Method
NC7SVL08P5X	L08	5-Lead SC70, EIAJ SC-88a, 1.25mm Wide	3000 Units on Tape & Reel
NC7SVL08L6X	CE	6-Lead MicroPak™, 1.00mm Wide	5000 Units on Tape & Reel
NC7SVL08FHX	CE	6-Lead, MicroPak2™, 1x1mm Body, .35mm Pitch	5000 Units on Tape & Reel

Connection Diagrams

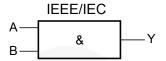


Figure 1. Logic Symbol

Pin Configurations

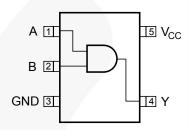


Figure 2. SC70 (Top View)

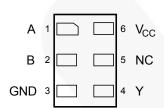


Figure 3. MicroPak™ (Top Through View)

Pin Definitions

Pin # SC70	Pin # MicroPak™	Name	Description
1	1	A	Input
2	2	В	Input
3	3	GND	Ground
4	4	4 Y Output	
	5	NC	No Connect
5	6	Vcc	Supply Voltage

Function Table

Y = AB

Inp	Output			
Α	В	Y		
L	L	L		
L	Н	L		
Н	L	L		
Н	Н	Н		

L = Low Logic Level

H = High Logic Level

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Para	meter	Min.	Max.	Unit
V _{CC}	Supply Voltage		-0.5	4.6	V
V _{IN}	DC Input Voltage		-0.5	4.6	V
V	DC Output Valtage	HIGH or LOW State ⁽¹⁾	-0.5	V _{CC} to +0.5	V
V_{OUT}	DC Output Voltage	V _{CC} =0V	-0.5	4.6	V
I _{IK}	DC Input Diode Current	V _{IN} < 0V		-50	mA
	DO Outrot Diede Ouwent	V _{OUT} < 0V		-50	А
IOK	I _{OK} DC Output Diode Current	V _{OUT} > V _{CC}		+50	mA
I _{OH} / I _{OL}	DC Output Source/Sink Current	t	A	±50	mA
I _{CC} or I _{GND}	DC V _{CC} or Ground Current per	Supply Pin		±50	mA
T _{STG}	Storage Temperature Range		-65	+150	°C
TJ	Junction Temperature Under Bi	as		+150	°C
TL	Junction Lead Temperature (So	oldering, 10 Seconds)		+260	°C
		SC70-5		150	
P_{D}	Power Dissipation at +85°C	MicroPak™-6		130	mW
		MicroPak2™-6	\	120	
ESD	Human Body Model	JEDEC: JESD22-A114		4000	V
ESD	Charged Device Model	JEDEC: JESD22-C101		2000	V

Note:

1. The I_O maximum rating must be observed.

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Conditions	Min.	Max.	Unit	
V _{CC}	Supply Voltage		0.9	3.6	V	
V _{IN}	Input Voltage ⁽²⁾		0	3.6	V	
V	Output Voltage	HIGH or LOW State	0	Vcc	V	
VOUT	V _{OUT} Output Voltage	V _{CC} =0V	0	3.6]	
		V _{CC} =3.0V to 3.6V		±24.0		
		V _{CC} =2.3V to 2.7V		±18.0	mA	
1 /1	Output Current in L / L	V _{CC} =1.65V to 1.95V		±6.0		
I _{OH} / I _{OL}	Output Current in I _{OH} / I _{OL}	V _{CC} =1.40V to 1.60V		±4.0		
		V _{CC} =1.10V to 1.30V		±2.0		
		V _{CC} =0.9V		±0.1	μA	
T _A	Free Air Operating Temperature		-40	+85	°C	
Δt / ΔV	Minimum Input Edge Rate	V _{IN} =0.8V to 2.0V, V _{CC} =3.0V		10	ns/V	
		SC70-5		425		
$\theta_{\sf JA}$	Thermal Resistance	MicroPak™-6		500	°C/W	
		MicroPak2™-6		560	1	

Note:

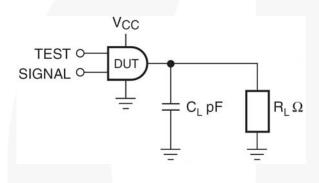
2. Unused inputs must be held HIGH or LOW. They may not float.

DC Electrical Characteristics

Cumbal	Doromotor	V	Conditions	T _A =25°C		T _A =-40 to 85°C		lln!tc
Symbol	Parameter	V _{cc}	Conditions	Min.	Max.	Min.	Max.	Units
		0.90		0.65 x V _{CC}		0.65 x V _{CC}		
		1.10 ≤ V _{CC} ≤ 1.30		0.65 x V _{CC}		0.65 x V _{CC}		
.,	HIGH Level Input	1.40 ≤ V _{CC} ≤ 1.60		0.65 x V _{CC}		0.65 x V _{CC}		٧
V_{IH}	Voltage	1.65 ≤ V _{CC} ≤ 1.95		0.9		0.9		
		2.30 ≤ V _{CC} ≤ 2.70		1.5		1.5		
		2.70 ≤ V _{CC} ≤ 3.60		1.5		1.5		
		0.90			0.25 x V _{CC}		0.25 x V _{CC}	
		1.10 ≤ V _{CC} ≤ 1.30			0.25 x V _{CC}		0.25 x V _{CC}	
	LOW Level Input	1.40 ≤ V _{CC} ≤ 1.60			0.25 x V _{CC}		0.25 x V _{CC}	
V_{IL}	Voltage	1.65 ≤ V _{CC} ≤ 1.95			0.25 x V _{CC}		0.25 x V _{CC}	V
		2.30 ≤ V _{CC} ≤ 2.70			0.7		0.7	
		2.70 ≤ V _{CC} ≤ 3.60			0.8		0.8	
	1	0.90		V _{CC} - 0.1		V _{CC} - 0.1		
		1.10 ≤ V _{CC} ≤ 1.30		V _{CC} - 0.1		V _{CC} - 0.1		
		1.40 ≤ V _{CC} ≤ 1.60	1	V _{CC} - 0.2		V _{CC} - 0.2		
		1.65 ≤ V _{CC} ≤ 1.95	I _{OH} =-100μA	V _{CC} - 0.2		V _{CC} - 0.2		
	9	2.30 ≤ V _{CC} ≤ 2.70	1	V _{CC} - 0.2		V _{CC} - 0.2		
		2.70 ≤ V _{CC} ≤ 3.60		V _{CC} - 0.2		V _{CC} - 0.2		
		1.10 ≤ V _{CC} ≤ 1.30	I _{OH} =-2mA	0.75 x V _{CC}		0.75 x V _{CC}		V
V _{OH}	HIGH Level Output	1.40 ≤ V _{CC} ≤ 1.60	I _{OH} =-4mA	0.75 x V _{CC}		0.75 x V _{CC}		
	Voltage	1.65 ≤ V _{CC} ≤ 1.95		1.25		1.25		
		2.30 ≤ V _{CC} ≤ 2.70	I _{OH} =-6mA	2.0		2.0		
		2.30 ≤ V _{CC} ≤ 2.70		1.8		1.8		
		$2.70 \le V_{CC} \le 3.60$	I _{OH} =-12mA	2.2		2.2		
		2.30 ≤ V _{CC} ≤ 2.70		1.7		1.7		
		2.70 ≤ V _{CC} ≤ 3.60	I _{OH} =-18mA	2.4		2.4		
		$2.70 \le V_{CC} \le 3.60$	I _{OH} =-24mA	2.2		2.2		
		0.90	-011 =		0.10		0.10	
		1.10 ≤ V _{CC} ≤ 1.30			0.10		0.10	
		$1.40 \le V_{CC} \le 1.60$			0.20		0.20	
		1.65 ≤ V _{CC} ≤ 1.95	I _{OL} =100μA		0.20	/	0.20	
		2.30 ≤ V _{CC} ≤ 2.70	1		0.20		0.20	
		2.70 ≤ V _{CC} ≤ 3.60	1		0.20		0.20	
	LOW Level Output	1.10 ≤ V _{CC} ≤ 1.30	I _{OL} =2mA		0.25 x V _{CC}		0.25 x V _{CC}	
V_{OL}	Voltage	1.40 ≤ V _{CC} ≤ 1.60	I _{OL} =4mA		0.25 x V _{CC}		0.25 x V _{CC}	V
		1.65 ≤ V _{CC} ≤ 1.95	I _{OL} =6mA		0.30		0.30	
		2.30 ≤ V _{CC} ≤ 2.70			0.40		0.40	
		2.70 ≤ V _{CC} ≤ 3.60	I _{OL} =12mA		0.40		0.40	
		2.30 ≤ V _{CC} ≤ 2.70			0.60		0.60	
		2.70 ≤ V _{CC} ≤ 3.60	I _{OL} =18mA		0.40		0.40	
		2.70 ≤ V _{CC} ≤ 3.60	I _{OL} =24mA		0.55		0.55	
I _{IN}	Input Leakage Current	0.90 to 3.60	0 ≤ V _{IN} ≤ 3.6V		±0.1		±0.5	μΑ
I _{OFF}	Power Off Leakage Current	0	0 ≤ (V _{IN} , V _O) ≤ 3.6V		0.5		0.5	μΑ
	Quiescent Supply		V _{IN} =V _{CC} or GND		0.9		0.9	
Icc	Current	0.90 to 3.60	$V_{CC} \le V_{IN} \le 3.6V$		0.0		±0.9	μΑ
	Increase in I _{CC} per	1.95	V _{IN} =0.9V		6		8	
I_{CCT}	Input	3.6	V _{IN} =1.5V		6		8	μΑ

AC Electrical Characteristics

Symbol	Parameter	V	V Conditions	T _A =25°C		T _A =-40 to 85°C		linita	Ciaura	
Symbol	Parameter	V _{cc}	Conditions	Min.	Тур.	Max.	Min.	Max.	Units	Figure
		0.90	C_L =15pF, R_L =1M Ω		45.0					
		1.10 ≤ V _{CC} ≤ 1.30	C _L =15pF,	3.5	8.2	17.5	3.0	30.5		
t _{PHL} , t _{PLH}	Propagation Delay	$1.40 \le V_{CC} \le 1.60$	$R_L=2k\Omega$	1.5	4.0	7.0	1.5	7.5	ns	Figure 4, Figure 5
		$1.65 \le V_{CC} \le 1.95$	C _L =30pF, R _I =500Ω	1.1	3.0	5.5	1.0	6.0		i igule 3
		$2.30 \le V_{CC} \le 2.70$		0.6	2.2	4.0	0.6	4.5		
		$2.70 \le V_{CC} \le 3.60$	112 00022	0.5	1.6	3.5	0.5	4.0		
C _{IN}	Input Capacitance	0			3				pF	
C_{PD}	Power Dissipation Capacitance	0.90 to 3.60	V _{IN} =0V or V _{CC} , f=10MHz		5				pF	



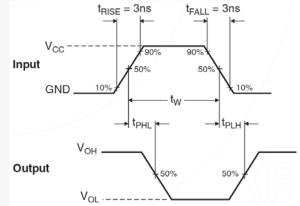


Figure 4. AC Test Circuit

Figure 5. AC Waveforms

Symbol	V _{cc}					
	3.3V ± 0.3V	2.5V ± 0.2V	1.8V ± 0.15V	1.5V ± 0.1V	1.2V ± 0.1V	0.9V
V _{mi}	1.5V	V _{CC} / 2				
V_{mo}	1.5V	V _{CC} / 2				

Physical Dimensions

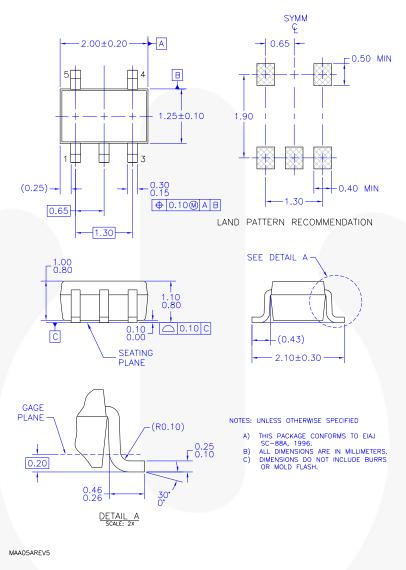


Figure 6. 5-Lead, SC70, EIAJ SC-88a, 1.25mm Wide

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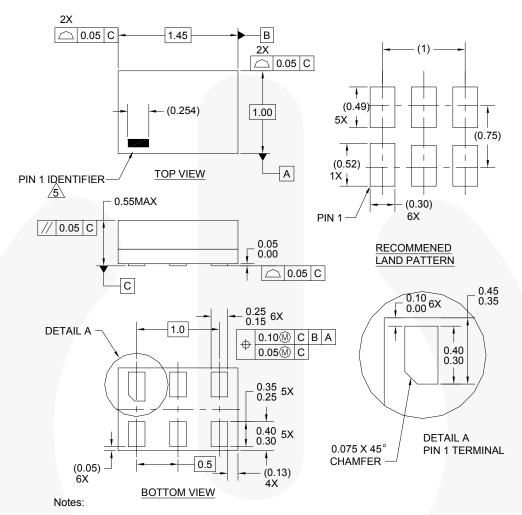
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Tape and Reel Specifications

Please visit Fairchild Semiconductor's online packaging area for the most recent tape and reel specifications: http://www.fairchildsemi.com/products/analog/pdf/sc70-5_tr.pdf.

Package Designator	kage Designator Tape Section		Cavity Status	Cover Type Status
	Leader (Start End)	125 (Typical)	Empty	Sealed
P5X	Carrier	3000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed

Physical Dimensions



- 1. CONFORMS TO JEDEC STANDARD M0-252 VARIATION UAAD 2. DIMENSIONS ARE IN MILLIMETERS
- 3. DRAWING CONFORMS TO ASME Y14.5M-1994
- FILENAME AND REVISION: MAC06AREV4
- 5. PIN ONE IDENTIFIER IS 2X LENGTH OF ANY

OTHER LINE IN THE MARK CODE LAYOUT.

Figure 7. 6-Lead, MicroPak™, 1.0mm Wide

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Package Designator Tape Section		Cavity Number	Cavity Status	Cover Type Status	
	Leader (Start End)	125 (Typical)	Empty	Sealed	
L6X	Carrier	5000	Filled	Sealed	
	Trailer (Hub End)	75 (Typical)	Empty	Sealed	

Physical Dimensions

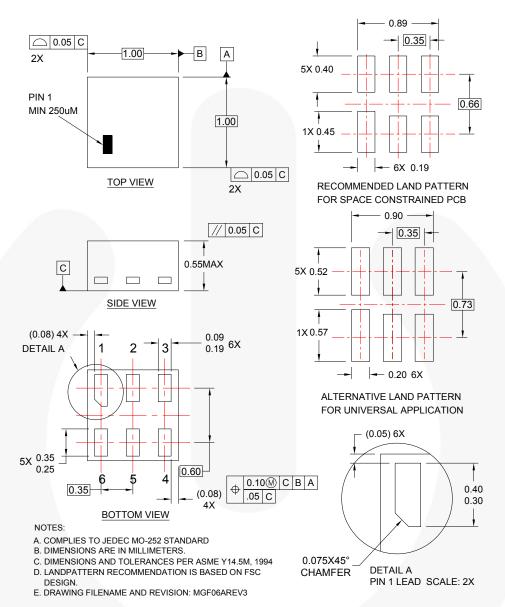


Figure 8. 6-Lead, MicroPak™2, 1x1mm Body, .35mm Pitch

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Tape and Reel Specifications

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Package Designator	Tape Section	Cavity Number	Cavity Status	Cover Type Status
	Leader (Start End)	125 (Typical)	Empty	Sealed
FHX	Carrier	5000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed





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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
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NC7SVL08FHX NC7SVL08L6X NC7SVL08P5X

ПОСТАВКА ЭЛЕКТРОННЫХ КОМПОНЕНТОВ

Общество с ограниченной ответственностью «МосЧип» ИНН 7719860671 / КПП 771901001 Адрес: 105318, г.Москва, ул.Щербаковская д.3, офис 1107

Данный компонент на территории Российской Федерации Вы можете приобрести в компании MosChip.

Для оперативного оформления запроса Вам необходимо перейти по данной ссылке:

http://moschip.ru/get-element

Вы можете разместить у нас заказ для любого Вашего проекта, будь то серийное производство или разработка единичного прибора.

В нашем ассортименте представлены ведущие мировые производители активных и пассивных электронных компонентов.

Нашей специализацией является поставка электронной компонентной базы двойного назначения, продукции таких производителей как XILINX, Intel (ex.ALTERA), Vicor, Microchip, Texas Instruments, Analog Devices, Mini-Circuits, Amphenol, Glenair.

Сотрудничество с глобальными дистрибьюторами электронных компонентов, предоставляет возможность заказывать и получать с международных складов практически любой перечень компонентов в оптимальные для Вас сроки.

На всех этапах разработки и производства наши партнеры могут получить квалифицированную поддержку опытных инженеров.

Система менеджмента качества компании отвечает требованиям в соответствии с ГОСТ Р ИСО 9001, ГОСТ РВ 0015-002 и ЭС РД 009

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