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February 2014



FHR1200

Micro-Power, Ultra Wide Voltage Regulator

Features

- Low Operating Current: 12 μ A Max.
- Option to Direct Drive Feedback Pin on PWM Controllers
- Programmable Output: 7.5 V to 100 V
- Wide Operating Temperature Range: -55°C to +150°C
- Output Voltage Accuracy: $\pm 2\%$
- Excellent Output Voltage Compensation: -30 PPM/°C
- Sink Current Capability: 10 μ A to 50 mA
- Small Package: SC70-6 (SOT363)

Applications

- Primary-Side Regulation in Flyback SMPS
- Secondary-Side Regulation in Flyback SMPS
- High Input Voltage SMPS
 - Smart Meter
 - Industrial Motor Control
 - Wireless Infrastructure
- Industrial and Street Lighting LED Power Supplies

Typical Application

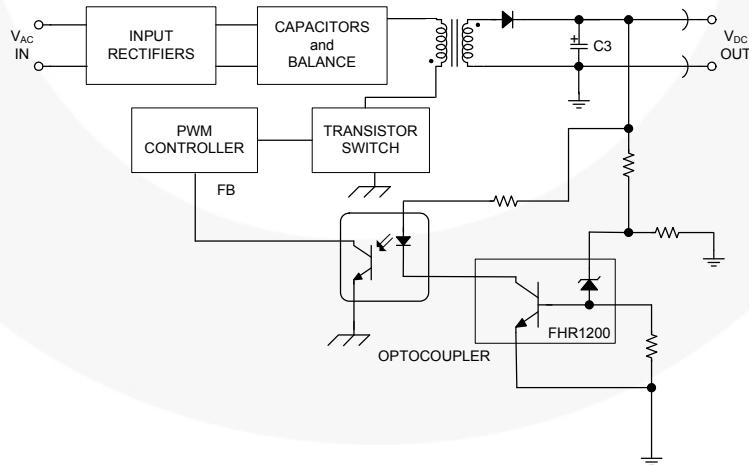


Figure1. Flyback Power Supply Secondary-Side Regulation

Ordering Information

Part Number	Top Mark	Package	Packing Method	Remarks
FHR1200	FH	SC70-6 (SOT363)	Tape and Reel	3000 pcs, Reel Size is 7"

Block Diagram

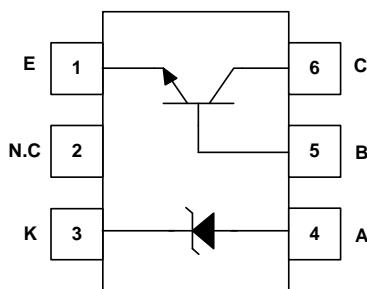


Figure 2. Internal Connection

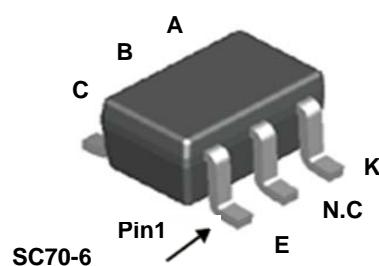


Figure 3. Device Package

Pin Definitions

Pin #	Pin Name	Description
1	E	Ground Connection
2	N.C.	No Connection
3	K	Reference Voltage
4	A	Ref Bias Pin: Tie R4 to ground to bias; Tie cap to ground for lower noise
5	B	Reference Bias Pin
6	C	Regulator Output

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^\circ\text{C}$ unless otherwise noted.

Symbol	Parameter	Value	Unit
V_{OUT}	Regulator Output	100	V
I_{BIAS}	Cathode Current	50	mA
P_D	Power Dissipation	$T_A = 25^\circ\text{C}$	mW
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +150	°C

Thermal Characteristics⁽¹⁾

Symbol	Parameter	Value	Unit
$R_{\theta JA}$	Thermal Resistance, Junction to Air	550	°C/W
Ψ_{JB}	Junction to Board Thermal Characterization Parameter	370	°C/W

Note:

1. PCB Board Size: FR4 76 x 114 x 0.6 T mm³ (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

Ψ_{JB} test method: T-36 gauge thermocouple is soldered directly to the collector lead pin about 1 mm distance from package lead.

Electrical Characteristics

Values are at $T_A = 25^\circ\text{C}$ unless otherwise noted.

Symbol	Parameter	Conditions		Min.	Typ.	Max.	Units
V_{REF}	Reference Voltage (Zener + Base Emitter Voltage)	$V_{\text{OUT}} = V_{\text{REF}}$, Fig. 4, $V_{\text{CC}} = 17.3 \text{ V}$, Accuracy = $\pm 2\%$, $I_Z = 25 \mu\text{A}$	$R3 = 49.9 \text{ k}\Omega$, $I_{\text{REF}} = 1 \mu\text{A}$, $R4 = 23.2 \text{ k}\Omega$, $I_{\text{CC}} = 200 \mu\text{A}$	7.115	7.260	7.405	V
TCV_{REF}	Temperature Coefficient ⁽²⁾	$V_{\text{OUT}} = V_{\text{REF}}$, $T_A = 0$ to $+100^\circ\text{C}$ Fig. 4, Accuracy = $T_A = -40$ to $+125^\circ\text{C}$ $\pm 2\%$,	$R3 = 49.9 \text{ k}\Omega$, $I_{\text{REF}} = 1 \mu\text{A}$, $R4 = 9.53 \text{ k}\Omega$, $I_{\text{CC}} = 200 \mu\text{A}$, $I_Z = 60 \mu\text{A}$, $V_{\text{CC}} = 10 \text{ V}$	29	PPM/ $^\circ\text{C}$		
		$V_{\text{OUT}} = V_{\text{REF}}$, $T_A = 0$ to $+100^\circ\text{C}$ Fig. 4, Accuracy = $T_A = -40$ to $+125^\circ\text{C}$ $\pm 2\%$,	$R3 = 49.9 \text{ k}\Omega$, $I_{\text{REF}} = 1 \mu\text{A}$, $R4 = 9.53 \text{ k}\Omega$, $I_{\text{CC}} = 200 \mu\text{A}$, $I_Z = 60 \mu\text{A}$, $V_{\text{CC}} = 10 \text{ V}$	57			
$\Delta V_{\text{REF}}/\Delta V_{\text{OUT}}$	Ratio of the Change in Reference Voltage to the Change in Cathode Voltage	Fig. 5, $V_{\text{CC}} = V_{\text{OUT}} + 20 \text{ V}$, $I_Z = 25 \mu\text{A}$, $V_{\text{OUT}} = V_{\text{REF}} + 75 \text{ V}$, $R5 =$ ⁽³⁾	$R3 = 100 \text{ k}\Omega$, $R2 = 53.6 \text{ k}\Omega$, $R1 = 0$ $499 \text{ k}\Omega$, $R4 = 23.2 \text{ k}\Omega$, $I_{\text{CC}} = 200 \mu\text{A}$, $I_{\text{REF}} = 150 \mu\text{A}$	0.024	0.200	0.200	mV/V
I_Z	Reference Input Current	$I_{\text{CC}} = 1.0 \text{ mA}$, Fig. 5, $V_{\text{CC}} = 17.3 \text{ V}$, $R1 = 10.0 \text{ k}\Omega$, $R2 =$ ⁽⁴⁾ , $R3 = 100 \text{ k}\Omega$, $R4 = 499 \text{ k}\Omega$, $R5 =$ ⁽⁴⁾		7.7	12.0	12.0	μA
$\Delta I_Z/\Delta T$	Deviation of Reference I_Z Over Temperature			5.45		5.45	$\mu\text{A}/^\circ\text{C}$
$\Delta V_{\text{REF}}/\Delta I_{\text{CC}}$	Output Impedance	Fig. 4, $V_{\text{OUT}} = V_{\text{REF}}$, $V_{\text{CC}} = 15.4$ 19.4 V , $I_Z = 25 \mu\text{A}$, $f = 0 \text{ Hz}$, $R5 =$ ⁽⁵⁾	$I_{\text{CC}} = 160$ $240 \mu\text{A}$, $R3 = 49.9 \text{ k}\Omega$, $R4 = 23.2 \text{ k}\Omega$	154	300	300	Ω
e_n	Output Noise Voltage ⁽⁶⁾	$V_{\text{OUT}} = V_{\text{REF}}$, Fig. 6, $I_{\text{CC}} = 1.0 \text{ mA}$, $V_{\text{CC}} = 17.3 \text{ V}$, $V_{\text{REF}} = 7.35 \text{ V}$, $I_Z = 25 \mu\text{A}$, $R3 = 15.00 \text{ k}\Omega$, $R4 = 28.7 \text{ k}\Omega$, $f = 400 \text{ Hz to } 100 \text{ KHz}$	$C_N = \text{n/a}$, $C_L = \text{n/a}$	141.0	μVrms		
			$C_N = 0.1 \mu\text{F}$, $C_L = \text{n/a}$	8.1			
			$C_N = \text{n/a}$, $C_L = 0.1 \mu\text{F}$	57			
			$C_N = 0.1 \mu\text{F}$, $C_L = 0.1 \mu\text{F}$	8.0			
GBW (3db)	Gain Bandwidth Product	$I_{\text{CC}} = 1.0 \text{ mA}$, $V_{\text{CC}} = 27 \text{ V}_{\text{DC}}$, $V_{\text{IN}} = 2 \text{ V}_{\text{p-p}}$, $I_B1 = 5 \mu\text{A}$, $I_Z = 25 \mu\text{A}$, $C_G C_L =$ ⁽⁷⁾ , $C_N = 0.1 \mu\text{F}$, $R1 = 23.2 \text{ k}\Omega$, $R2 = 39.2 \text{ k}\Omega$, $R3 = 15 \text{ k}\Omega$, $R4 = 28.7 \text{ k}\Omega$, $R5 = 22 \text{ k}\Omega$, $V_{\text{OUT}} = 12 \text{ V}$, $I_{\text{REF}} = 200 \mu\text{A}$, Gain = -1, Fig. 7		4.47		4.47	MHz
SR	Slew Rate	$I_{\text{CC}} = 1.0 \text{ mA}$, $V_{\text{CC}} = 27 \text{ V}_{\text{DC}}$, $V_{\text{IN}} = 2 \text{ V}_{\text{p-p}}$, $I_B1 = 5 \mu\text{A}$, $I_Z = 25 \mu\text{A}$, $C_G C_L =$ ⁽⁷⁾ , $C_N = 0.1 \mu\text{F}$, $R1 = 23.2 \text{ k}\Omega$, $R2 = 39.2 \text{ k}\Omega$, $R3 = 15 \text{ k}\Omega$, $R4 = 28.7 \text{ k}\Omega$, $R5 = 22 \text{ k}\Omega$, $V_{\text{OUT}} = 12 \text{ V}$, $I_{\text{REF}} = 200 \mu\text{A}$, Gain = -1, Fig. 7		18.8		18.8	$\text{V}/\mu\text{s}$

Notes:

2. The deviation parameters $V_{\text{REF(dev)}}$ and $I_{\text{REF(dev)}}$ are defined as the differences between the maximum and minimum values obtained over the rated temperature range. The average full-range temperature coefficient of the reference input voltage, TCV_{REF} , is defined as:

$$TCV_{\text{REF}}(\frac{\text{ppm}}{^\circ\text{C}}) = \frac{\left(\frac{V_{\text{REF(dev)}}}{V_{\text{REF}}(T_A=25^\circ\text{C})} \right) * 10^6}{\Delta T} \quad (T_A): \text{Ambient Temperature} \\ V_{\text{REF(dev)}}: V_{\text{REF}} \text{ deviation over full temperature range}$$

where ΔT is the rated operating free-air temperature range of the device.

TCV_{REF} can be positive or negative, depending on whether minimum V_{REF} or maximum V_{REF} , respectively, occurs at the lower temperature.

3.
$$\frac{\Delta V_{\text{REF}}}{\Delta V_{\text{OUT}}} = \text{ABS} \left| \frac{V_{\text{REF1}} - V_{\text{REF2}}}{V_{\text{OUT1}} - V_{\text{OUT2}}} \right|$$

4.
$$I_Z = \frac{V_{\text{REF}} - V_{\text{OUT}}}{R1}$$

5.
$$Z_{\text{OUT}} = \frac{V_{\text{REF2}} - V_{\text{REF1}}}{I_{\text{CC2}} - I_{\text{CC1}}}$$

6. For testing: a) hfe typical ~200; b) all resistors are metal film; c) all capacitors are plastic film.

Test Circuit

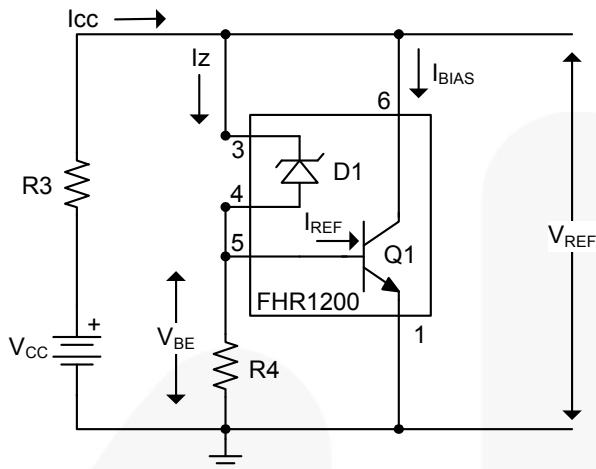


Figure 4. Test Configured: $V_{REF} = V_{OUT}$

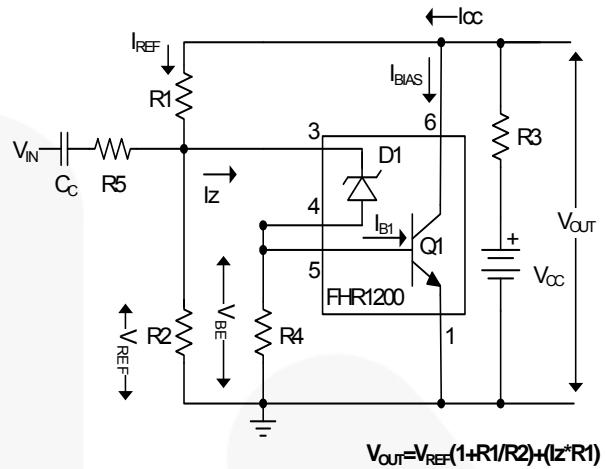


Figure 5. Test Configured: $V_{REF} < V_{OUT}$

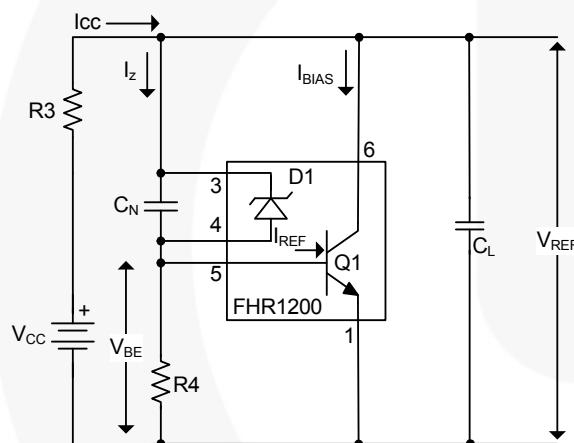


Figure 6. Test Configured: $V_{REF} = V_{OUT}$ with Capacitance

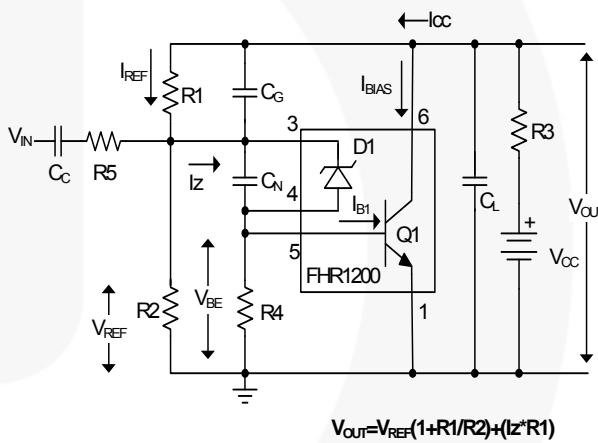


Figure 7. Test Configured: $V_{REF} < V_{OUT}$ with Capacitance

Typical Characteristics: V_{REF}

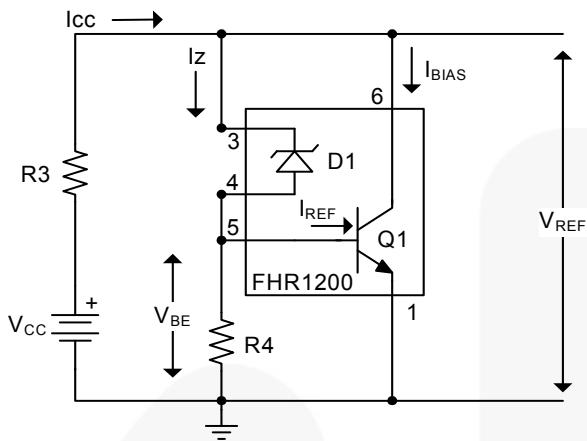


Figure 8. Test Diagram: $V_{REF} = V_{OUT}$ (Fixed Value of R_4)

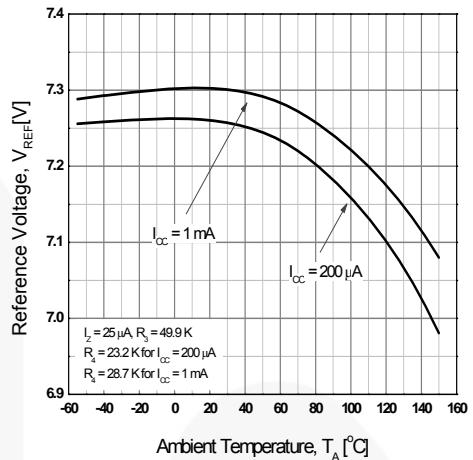


Figure 9. Reference Voltage vs. Ambient Temperature (Fixed Value R_4 , $I_Z \sim 25 \mu A$ at $25^\circ C$)

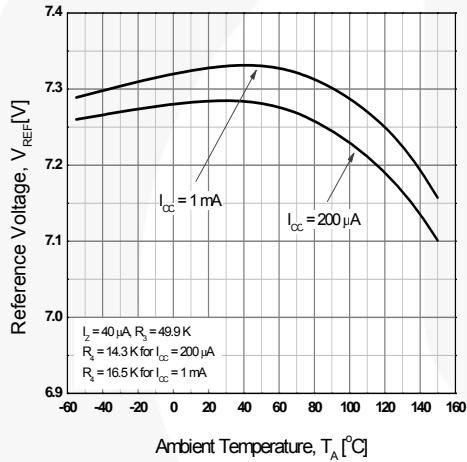


Figure 10. Reference Voltage vs. Ambient Temperature (Fixed Value R_4 , $I_Z \sim 40 \mu A$ at $25^\circ C$)

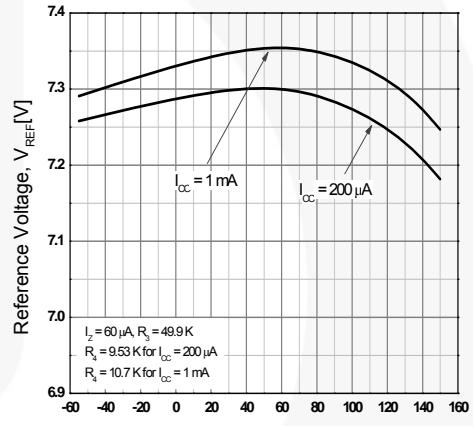


Figure 11. Reference Voltage vs. Ambient Temperature (Fixed Value R_4 , $I_Z \sim 60 \mu A$ at $25^\circ C$)

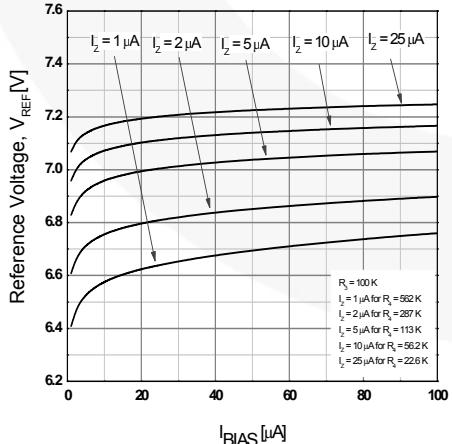


Figure 12. Minimum Cathode Current for Regulation

Typical Characteristics (Continued)

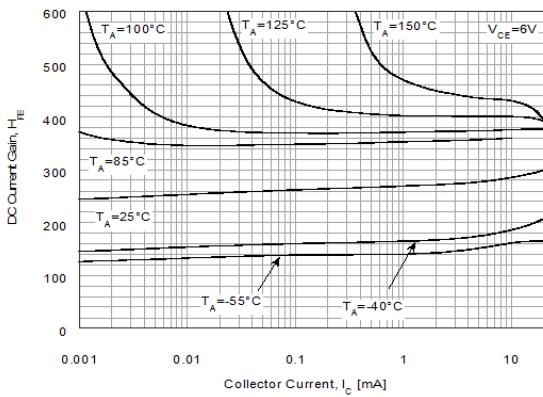


Figure 13. DC Current Gain vs. Collector Current

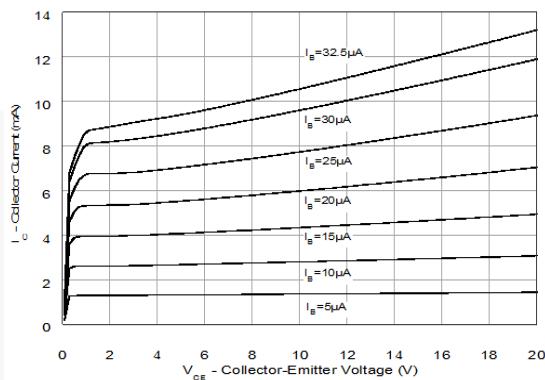


Figure 14. Common Emitter Output Characteristics

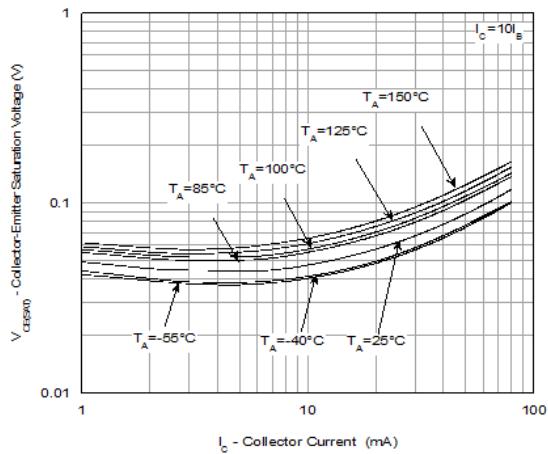


Figure 15. Collection-Emitter Saturation Voltage vs. Collector Current

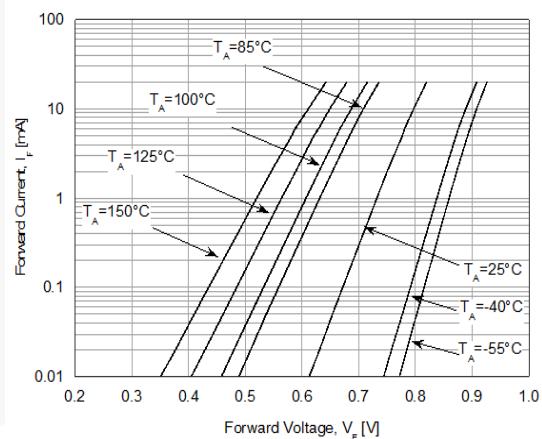


Figure 16. Typical Forward Voltage

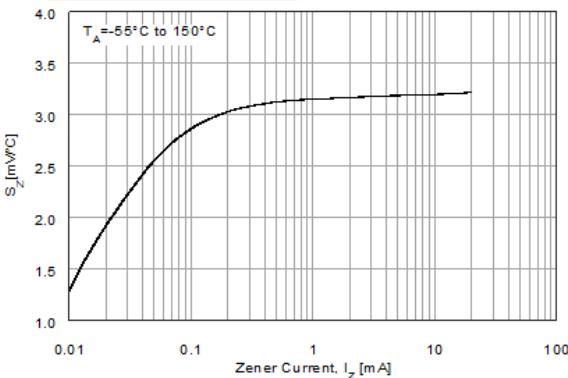


Figure 17. Typical Temperature Coefficient as Function of Working Current

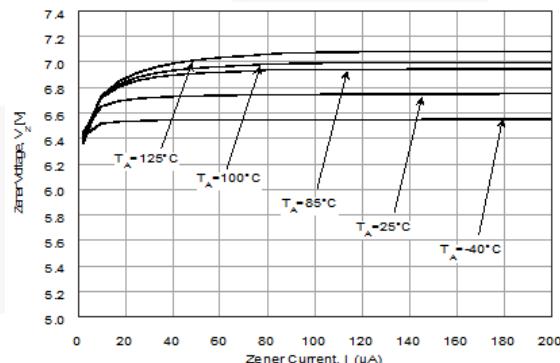


Figure 18. Zener Voltage vs. Applied Current

Typical Characteristics (Continued)

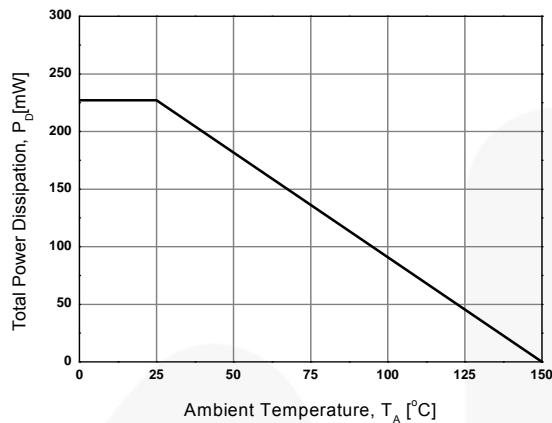


Figure 19. Power Derating

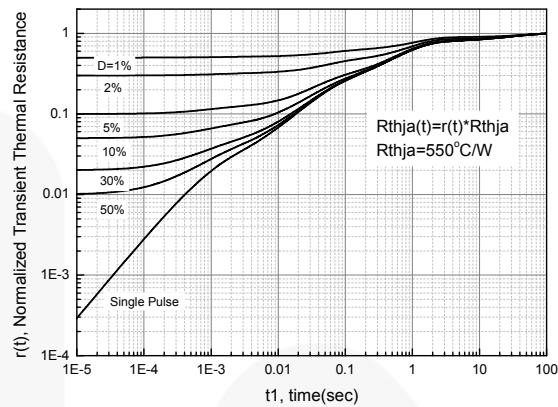


Figure 20. Transient Thermal Resistance

Typical Applications (Continued)

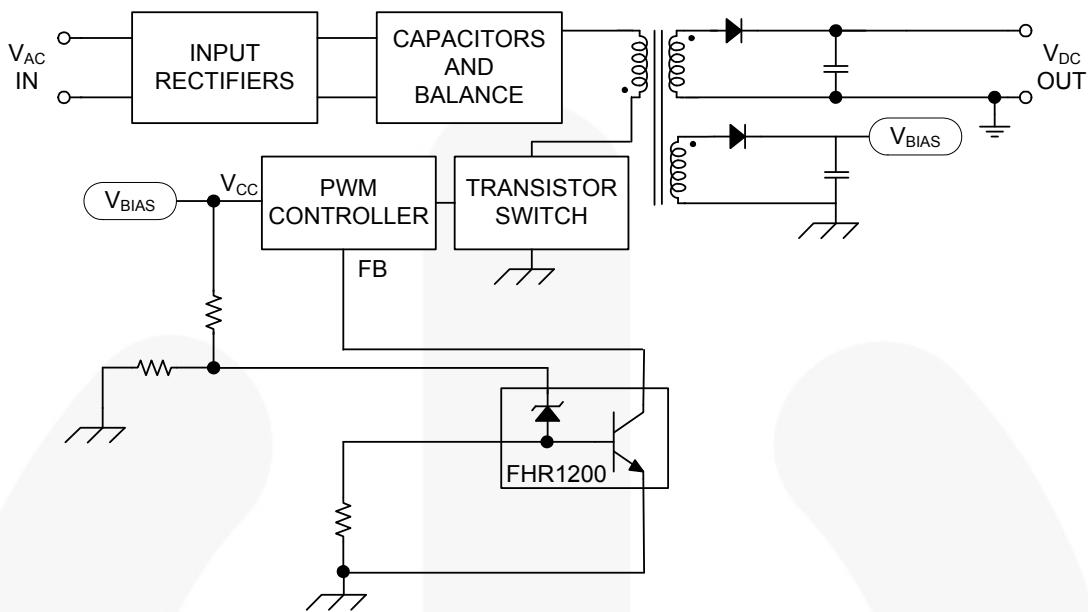


Figure 21. Flyback Power Supply Primary-Side Regulation

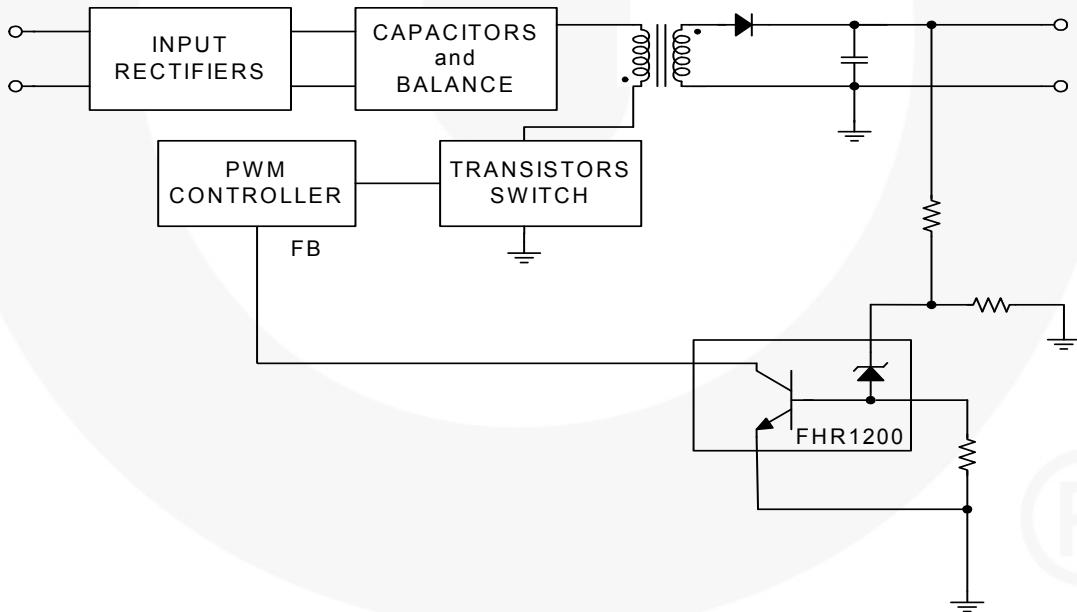


Figure 22. Flyback Power Supply Non-Isolated Secondary-Side Regulation

Physical Dimensions

SC70-6 (SOT363)

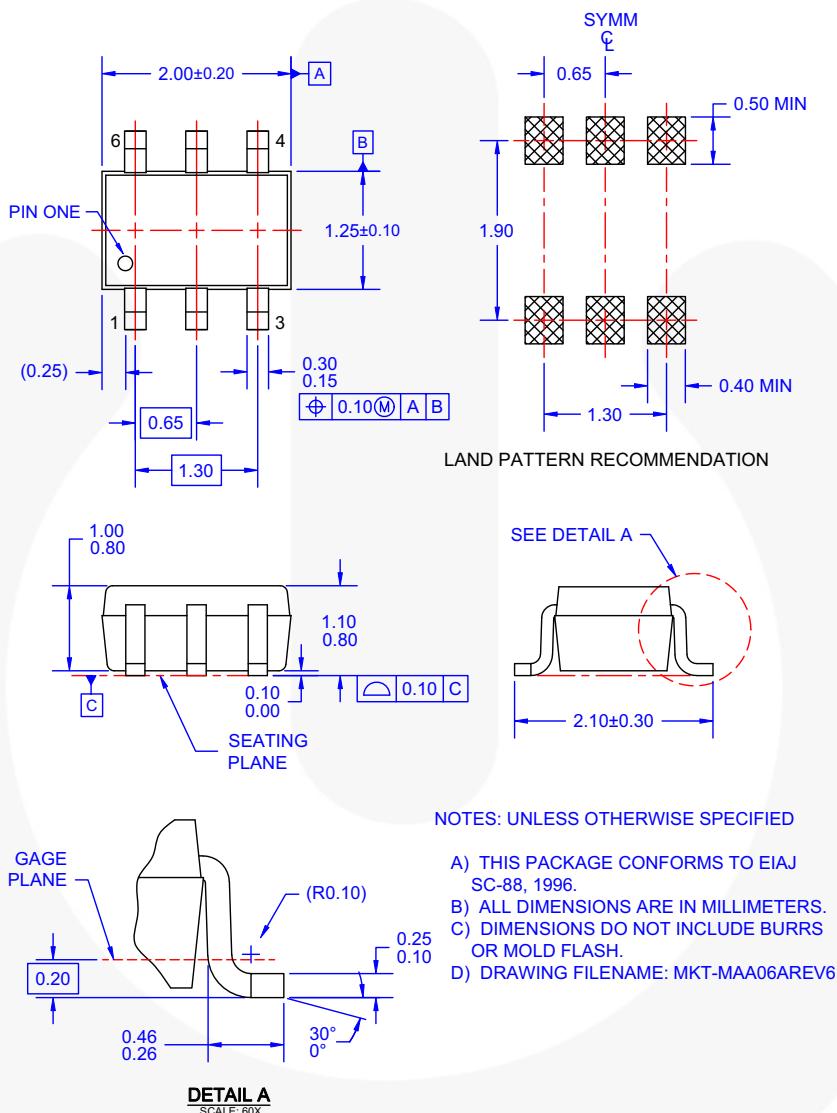


Figure 23. 6-Lead, SC-70, EIAJ SC-88, 1.25 MM WIDE

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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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