

### 200 mA 36 V Input Ultra Low Supply Current VR Evaluation Board

No. EEV-332-N033B-181225

**R1524N033B-EV is the evaluation board for R1524 which has the below features, benefits and specifications.**

#### OUTLINE

The R1524N is an ultra-low supply current voltage regulator featuring 200 mA output current and 36 V input voltage. This device consists of an Output Short-circuit Protection Circuit, an Over-current Protection Circuit, and a Thermal Shutdown Circuit in addition to the basic regulator circuits. The operating temperature range is from  $-40^{\circ}\text{C}$  to  $105^{\circ}\text{C}$ , and the maximum input voltage is 36 V. All these features allow the R1524N to become an ideal power source of electric home appliances.

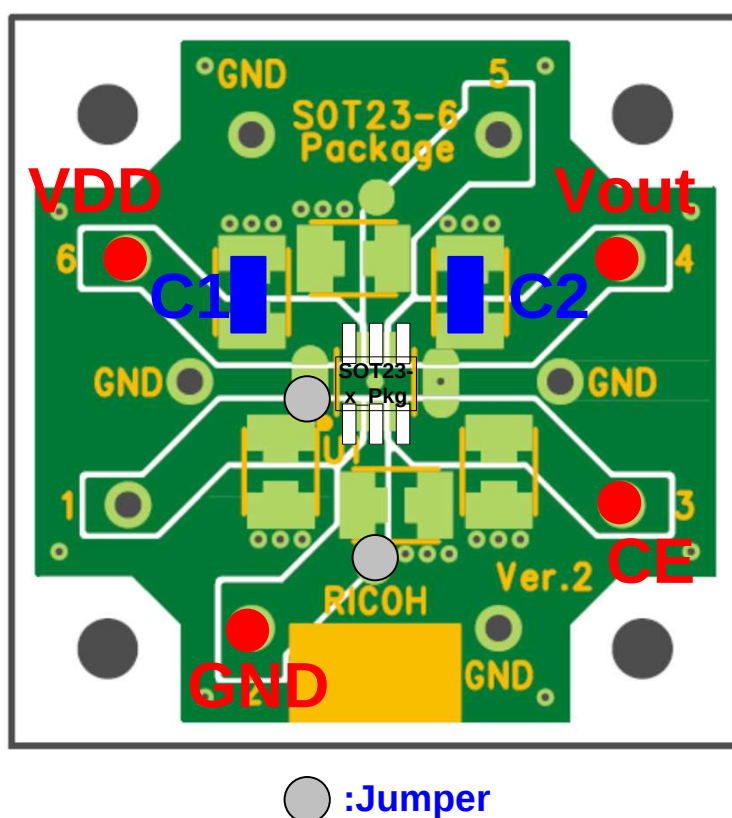
The output voltages are internally fixed at either of the following: 1.8 V, 2.5 V, 2.8 V, 3.0 V, 3.3 V, 3.4 V, 5.0 V, 5.5 V, 6.0 V, 6.4 V, 8.0 V, 8.5 V and 9.0 V. The output voltage accuracy is  $\pm 0.6\%$ .

#### KEY SPECIFICATIONS

- Input Voltage Range (Maximum Rating) ..... 3.5 V to 36 V (50 V)
- Operating Temperature Range .....  $-40^{\circ}\text{C}$  to  $105^{\circ}\text{C}$
- Supply Current ..... Typ. 2.2  $\mu\text{A}$
- Standby Current ..... Typ. 0.1  $\mu\text{A}$
- Dropout Voltage ..... Typ. 0.6 V ( $I_{\text{OUT}} = 200 \text{ mA}$ ,  $V_{\text{OUT}} = 5.0 \text{ V}$ )
- Output Voltage Range ..... 1.8 V / 2.5 V / 2.8 V / 3.0 V / 3.3 V / 3.4 V /  
5.0 V / 5.5 V / 6.0 V / 6.4 V / 8.0 V / 8.5 V / 9.0 V  
\*Contact Ricoh sales representatives for other voltages.
- Output Voltage Accuracy .....  $\pm 0.6\%$  ( $T_a = 25^{\circ}\text{C}$ )
- Output Voltage Temperature-Drift Coefficient ..... Typ.  $\pm 60 \text{ ppm}/^{\circ}\text{C}$
- Line Regulation ..... Typ. 0.01%/V ( $V_{\text{SET}} + 1 \text{ V} \leq V_{\text{IN}} \leq 36 \text{ V}$ )
- Built-in Output Short-circuit Protection Circuit ..... Typ. 80 mA
- Built-in Over-current Protection Circuit ..... Typ. 350 mA
- Built-in Thermal Shutdown Circuit ..... Thermal Shutdown Temperature: Typ.  $160^{\circ}\text{C}$
- Ceramic capacitors are recommended  
to be used with this device .....  $C_{\text{OUT}} = 0.1 \mu\text{F}$  or more
- Package ..... SOT-23-5
- For more details on R1524 IC, please refer to  
[https://www.e-devices.ricoh.co.jp/en/products/power/vr\\_ldo/r1524/r1524-ea.pdf](https://www.e-devices.ricoh.co.jp/en/products/power/vr_ldo/r1524/r1524-ea.pdf).

## PCB LAYOUT

R1524N (Package: SOT-23-5) PCB Layout



○ :Jumper

## ABSOLUTE MAXIMUM RATINGS

### Absolute Maximum Ratings

| Symbol    | Item                                                                   |          | Rating                                  | Unit |
|-----------|------------------------------------------------------------------------|----------|-----------------------------------------|------|
| $V_{IN}$  | Input Voltage                                                          |          | -0.3 to 50                              | V    |
| $V_{IN}$  | Peak Input Voltage <sup>(1)</sup>                                      |          | 60                                      | V    |
| $V_{CE}$  | Input Voltage (CE Pin)                                                 |          | -0.3 to 50                              | V    |
| $V_{OUT}$ | Output Voltage                                                         |          | $-0.3 \text{ to } V_{IN} + 0.3 \leq 50$ | V    |
| $I_{OUT}$ | Output Current                                                         |          | 300                                     | mA   |
| $P_D$     | Power Dissipation <sup>(2)</sup><br>(JEDEC STD.51-7 Test Land Pattern) | SOT-23-5 | 660                                     | mW   |
| $T_j$     | Junction Temperature Range                                             |          | -40 to 125                              | °C   |
| $T_{stg}$ | Storage Temperature Range                                              |          | -55 to 125                              | °C   |

### ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the lifetime and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings are not assured.

## RECOMMENDED OPERATING CONDITIONS

### Recommended Operating Conditions

| Symbol   | Item                        | Rating     | Unit |
|----------|-----------------------------|------------|------|
| $V_{IN}$ | Input Voltage               | 3.5 to 36  | V    |
| $T_a$    | Operating Temperature Range | -40 to 105 | °C   |

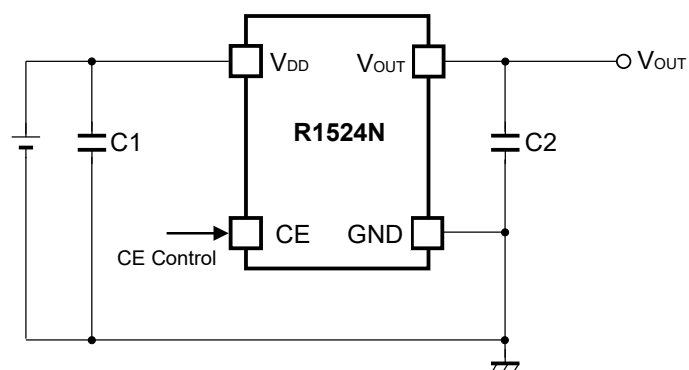
### RECOMMENDED OPERATING CONDITIONS

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

<sup>(1)</sup> Duration time: 200 ms

<sup>(2)</sup> Refer to *POWER DISSIPATION* for detailed information.

## TYPICAL APPLICATION



**R1524N Typical Application**

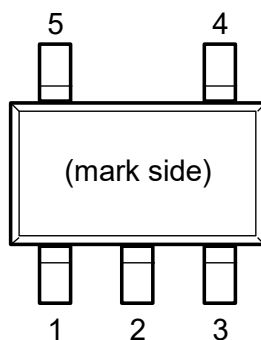
※Although C2 operates even at 0.1  $\mu\text{F}$ , C2 = 10  $\mu\text{F}$  is recommended to improve transient characteristics.

### Recommended External Components\*1

| Symbol | Value             |
|--------|-------------------|
| C1     | 0.1 $\mu\text{F}$ |
| C2     | 10 $\mu\text{F}$  |

\*1 The bill of materials will be attached on the shipment of each purchased evaluation board.

## PIN DESCRIPTION



### SOT-23-5 Pin Configuration

#### SOT-23-5 Pin Description

| Pin No. | Symbol             | Description                   |
|---------|--------------------|-------------------------------|
| 1       | GND <sup>(1)</sup> | Ground Pin                    |
| 2       | GND <sup>(1)</sup> | Ground Pin                    |
| 3       | CE                 | Chip Enable Pin (Active-high) |
| 4       | V <sub>OUT</sub>   | Output Pin                    |
| 5       | V <sub>DD</sub>    | Input Pin                     |

<sup>(1)</sup> The GND pin must be wired together when it is mounted on board.

## TECHNICAL NOTES

### Phase Compensation

In the R1524N, phase compensation is provided to secure stable operation even when the load current is varied. For this purpose, make sure to use 0.1  $\mu$ F or more of a capacitor (C2).

In case of using a tantalum type capacitor and the ESR (Equivalent Series Resistance) value of the capacitor is large, the output might be unstable. Evaluate the circuit including consideration of frequency characteristics. Connect 0.1  $\mu$ F or more of a capacitor (C1) between  $V_{DD}$  and GND, and as close as possible to the pins.

### PCB Layout

For SOT-23-5 package type, wire the following GND pins together: No. 1 and No. 2.



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##### **Shin-Yokohama Office (International Sales)**

2-3, Shin-Yokohama 3-chome, Kohoku-ku, Yokohama-shi, Kanagawa, 222-8530, Japan  
Phone: +81-50-3814-7687 Fax: +81-45-474-0074

##### **Ricoh Americas Holdings, Inc.**

675 Campbell Technology Parkway, Suite 200 Campbell, CA 95008, U.S.A.  
Phone: +1-408-610-3105

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##### **Semiconductor Support Centre**

Prof. W.H. Keesomlaan 1, 1183 DJ Amstelveen, The Netherlands  
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##### **Ricoh Electronic Devices Korea Co., Ltd.**

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Phone: +82-2-2135-5700 Fax: +82-2-2051-5713

##### **Ricoh Electronic Devices Shanghai Co., Ltd.**

Room 403, No.2 Building, No.690 Bibo Road, Pu Dong New District, Shanghai 201203,  
People's Republic of China  
Phone: +86-21-5027-3200 Fax: +86-21-5027-3299

##### **Ricoh Electronic Devices Shanghai Co., Ltd.**

##### **Shenzhen Branch**

1205, Block D(Jinlong Building), Kingkey 100, Hongbao Road, Luohu District,  
Shenzhen, China  
Phone: +86-755-8348-7600 Ext 225

##### **Ricoh Electronic Devices Co., Ltd.**

##### **Taipei office**

Room 109, 10F-1, No.51, Hengyang Rd., Taipei City, Taiwan  
Phone: +886-2-2313-1621/1622 Fax: +886-2-2313-1623

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105318, г.Москва, ул.Щербаковская д.3, офис 1107, 1118, ДЦ «Щербаковский»

Телефон: +7 495 668-12-70 (многоканальный)

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E-mail: [info@moschip.ru](mailto:info@moschip.ru)

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