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May 2014

### FGB3040G2\_F085 / FGD3040G2\_F085 FGP3040G2\_F085 / FGI3040G2\_F085

## EcoSPARK®2 300mJ, 400V, N-Channel Ignition IGBT

#### **Features**

- SCIS Energy = 300mJ at T<sub>J</sub> = 25°C
- Logic Level Gate Drive
- Qualified to AEC Q101

JEDEC TO-252AA

D-Pak

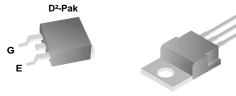
■ RoHS Compliant

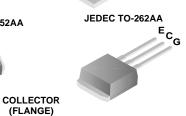
#### **Applications**

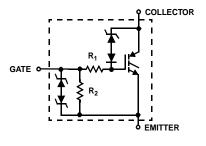
- Automotive Ignition Coil Driver Circuits
- Coil On Plug Applications



# Package JEDEC TO-263AB D²-Pak JEDEC TO-220AB ECG







#### **Device Maximum Ratings** T<sub>A</sub> = 25°C unless otherwise noted

Symbol	Parameter	Ratings	Units
BV <sub>CER</sub>	Collector to Emitter Breakdown Voltage (I <sub>C</sub> = 1mA)	400	V
BV <sub>ECS</sub>	Emitter to Collector Voltage - Reverse Battery Condition (I <sub>C</sub> = 10mA)	28	V
E <sub>SCIS25</sub>	Self Clamping Inductive Switching Energy (Note 1)	300	mJ
	Self Clamping Inductive Switching Energy (Note 2)	170	mJ
I <sub>C25</sub>	Collector Current Continuous, at V <sub>GE</sub> = 5.0V, T <sub>C</sub> = 25°C	41	Α
I <sub>C110</sub>	Collector Current Continuous, at V <sub>GE</sub> = 5.0V, T <sub>C</sub> = 110°C	25.6	Α
$V_{GEM}$	Gate to Emitter Voltage Continuous	±10	V
D	Power Dissipation Total, at T <sub>C</sub> = 25°C	150	W
$P_D$	Power Dissipation Derating, for T <sub>C</sub> > 25°C	1	W/°C
$T_{J}$	Operating Junction Temperature Range	-55 to +175	°C
T <sub>STG</sub>	Storage Junction Temperature Range	-55 to +175	°C
$T_L$	Max. Lead Temp. for Soldering (Leads at 1.6mm from case for 10s)	300	°C
T <sub>PKG</sub>	PKG Reflow soldering according to JESD020C		°C
ESD	HBM-Electrostatic Discharge Voltage at 100pF, 1500 $\Omega$	4	kV
EOD	CDM-Electrostatic Discharge Voltage at $1\Omega$	2	kV

#### **Package Marking and Ordering Information**

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FGB3040G2	FGB3040G2_F085	TO-263AB	330mm	24mm	800
FGD3040G2	FGD3040G2_F085	TO-252AA	330mm	16mm	2500
FGP3040G2	FGP3040G2_F085	TO-220AB	Tube	N/A	50
FGI3040G2	FGI3040G2_F085	TO-262AA	Tube	N/A	50

#### Electrical Characteristics T<sub>A</sub> = 25°C unless otherwise noted

Symbol Parameter Test Conditions Min Typ Max Ur
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#### **Off State Characteristics**

BV <sub>CER</sub>	Collector to Emitter Breakdown Voltage	$I_{CE}$ = 2mA, $V_{GE}$ = 0, tage $R_{GE}$ = 1KΩ, $T_{J}$ = -40 to 150°C		370	400	430	V
BV <sub>CES</sub>	Collector to Emitter Breakdown Voltage	$T_{J} = -40 \text{ to } 150^{\circ}\text{C}$		390	420	450	٧
BV <sub>ECS</sub>	Emitter to Collector Breakdown Voltage	ge $I_{CE}$ = -20mA, $V_{GE}$ = 0V, $T_{J}$ = 25°C		28	-	1	V
BV <sub>GES</sub>	Gate to Emitter Breakdown Voltage	nitter Breakdown Voltage I <sub>GES</sub> = ±2mA		±12	±14	-	V
	Collector to Emitter Leakage Current	$V_{CE}$ = 250V, $R_{GE}$ = 1K $\Omega$	$T_J = 25^{\circ}C$	-	-	25	μΑ
I <sub>CER</sub>	ICER Collector to Emitter Leakage Current		$T_{J} = 150^{\circ}C$	-	-	1	mA
	Emitter to Collector Leakage Current	V <sub>EC</sub> = 24V,	$T_{J} = 25^{\circ}C$	-	-	1	mA
I <sub>ECS</sub>	Emilier to Collector Leakage Current		$T_{J} = 150^{\circ}C$	-	-	40	IIIA
R <sub>1</sub>	Series Gate Resistance			-	120	-	Ω
R <sub>2</sub>	Gate to Emitter Resistance			10K	-	30K	Ω

#### **On State Characteristics**

$V_{CE(SAT)}$	Collector to Emitter Saturation Voltage	$I_{CE} = 6A, V_{GE} = 4V,$	$T_{J} = 25^{\circ}C$	-	1.15	1.25	V
V <sub>CE(SAT)</sub>	Collector to Emitter Saturation Voltage	$I_{CE}$ = 10A, $V_{GE}$ = 4.5V,	$T_J = 150^{\circ}C$	-	1.35	1.50	V
V <sub>CE(SAT)</sub>	Collector to Emitter Saturation Voltage	$I_{CE} = 15A, V_{GE} = 4.5V,$	$T_J = 150^{\circ}C$	-	1.68	1.85	V
E <sub>SCIS</sub>	Self Clamped Inductive Switching	L = 3.0 mHy,RG = 1KΩ,	TJ = 25°C	1	-	300	mJ
0010	·	VGE = 5V, (Note 1)					

#### **Thermal Characteristics**

R <sub>0</sub> JC Thermal Resistance Junction to Case	-	-	1	°C/W
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#### Notes:

1: Self Clamping Inductive Switching Energy ( $E_{SCIS25}$ ) of 300 mJ is based on the test conditions that starting Tj=25°C; L=3mHy,  $I_{SCIS}$ =14.2A, $V_{CC}$ =100V during inductor charging and  $V_{CC}$ =0V during the time in clamp.

2: Self Clamping Inductive Switching Energy ( $E_{SCIS150}$ ) of 170 mJ is based on the test conditions that starting Tj=150°C; L=3mHy,  $I_{SCIS}$ =10.8A, $V_{CC}$ =100V during inductor charging and  $V_{CC}$ =0V during the time in clamp.

Min Typ Max Units

## **Electrical Characteristics** $T_A = 25^{\circ}C$ unless otherwise noted

Parameter

Dynamic Characteristics							
Q <sub>G(ON)</sub>	Gate Charge	I <sub>CE</sub> = 10A, V <sub>CE</sub> = 12V, V <sub>GE</sub> = 5V		-	21	-	nC
V <sub>GE(TH)</sub>	Gate to Emitter Threshold Voltage $I_{CE} = 1$ mA, $V_{CE} = V_{GE}$ ,	lor = 1mA Vor = Vor	$T_{J} = 25^{\circ}C$	1.3	1.7	2.2	V
▼GE(TH)		I'CE IIII'N, VCE VGE,	$T_{J} = 150^{\circ}C$	0.75	1.2	1.8	
$V_{GEP}$	Gate to Emitter Plateau Voltage	V <sub>CE</sub> = 12V, I <sub>CE</sub> = 10A		-	2.8	-	V

**Test Conditions** 

#### **Switching Characteristics**

Symbol

t <sub>d(ON)R</sub>	Current Turn-On Delay Time-Resistive	<u> </u>	-	0.9	4	μS
t <sub>rR</sub>	Current Rise Time-Resistive	$V_{GE} = 5V, R_G = 1K\Omega$ $T_J = 25^{\circ}C,$	1	1.9	7	μS
t <sub>d(OFF)L</sub>	Current Turn-Off Delay Time-Inductive	V <sub>CE</sub> = 300V, L = 1mH,	-	4.8	15	μS
t <sub>fL</sub>	Current Fall Time-Inductive	$V_{GE} = 5V, R_{G} = 1K\Omega$ $I_{CE} = 6.5A, T_{J} = 25^{\circ}C,$	-	2.0	15	μS

#### **Typical Performance Curves**

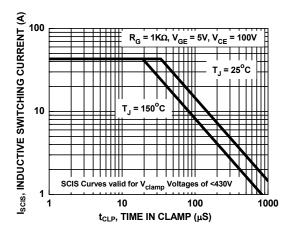
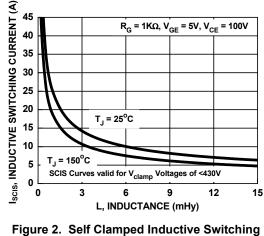


Figure 1. Self Clamped Inductive Switching Current vs. Time in Clamp



Current vs. Inductance

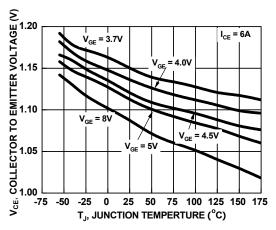


Figure 3. Collector to Emitter On-State Voltage vs. Junction Temperature

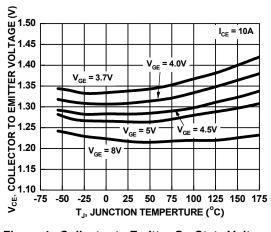


Figure 4. Collector to Emitter On-State Voltage vs. Junction Temperature

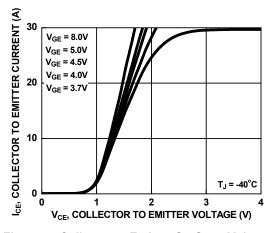


Figure 5. Collector to Emitter On-State Voltage vs. Collector Current

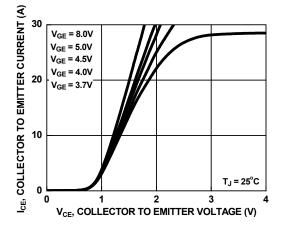


Figure 6. Collector to Emitter On-State Voltage vs. Collector Current

#### **Typical Performance Curves** (Continued)

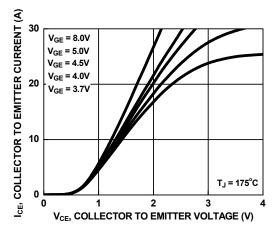


Figure 7. Collector to Emitter On-State Voltage vs. Collector Current

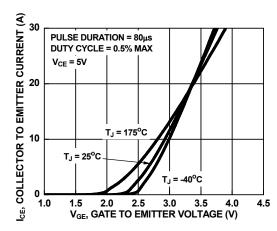


Figure 8. Transfer Characteristics

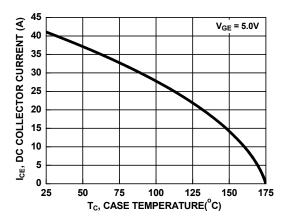


Figure 9. DC Collector Current vs. Case Temperature

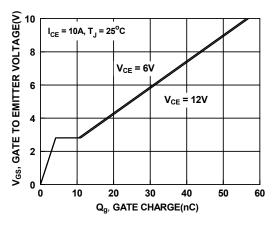


Figure 10. Gate Charge

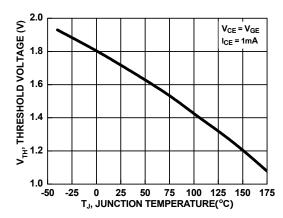


Figure 11. Threshold Voltage vs. Junction Temperature

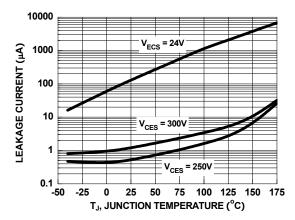
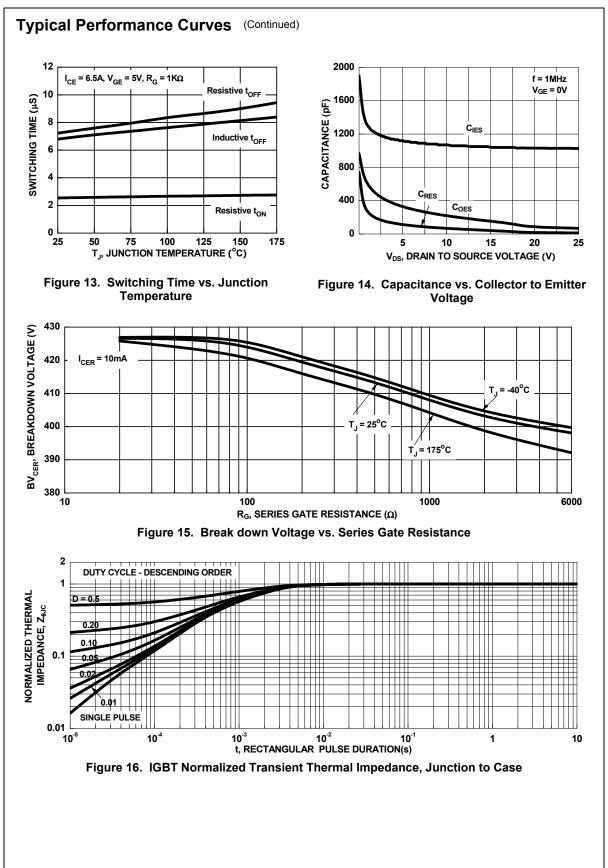
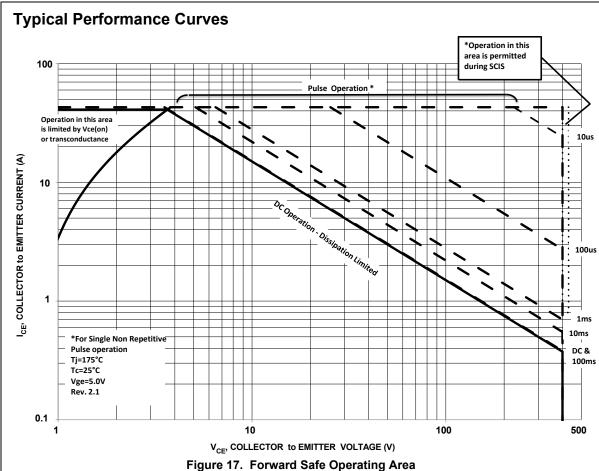
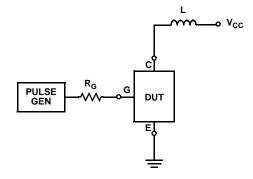


Figure 12. Leakage Current vs. Junction Temperature





#### **Test Circuit and Waveforms**



 $R_{G} = 1K\Omega$  DUT  $V_{CC}$ 

Figure 18. Inductive Switching Test Circuit

Figure 19.  $t_{ON}$  and  $t_{OFF}$  Switching Test Circuit

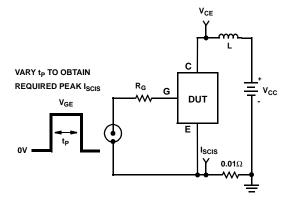


Figure 20. Energy Test Circuit

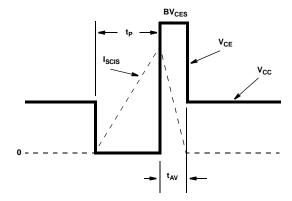


Figure 21. Energy Waveforms





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