

# MBR1100

Preferred Device

## Axial Lead Rectifier

These rectifiers employ the Schottky Barrier principle in a large area metal-to-silicon power diode. State-of-the-art geometry features epitaxial construction with oxide passivation and metal overlap contact. Ideally suited for use as rectifiers in low-voltage, high-frequency inverters, free wheeling diodes, and polarity protection diodes.

### Features

- Low Reverse Current
- Low Stored Charge, Majority Carrier Conduction
- Low Power Loss/High Efficiency
- Highly Stable Oxide Passivated Junction
- Guard-Ring for Stress Protection
- Low Forward Voltage
- 175°C Operating Junction Temperature
- High Surge Capacity
- These are Pb-Free Devices\*

### Mechanical Characteristics:

- Case: Epoxy, Molded
- Weight: 0.4 Gram (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Polarity: Cathode Indicated by Polarity Band

### MAXIMUM RATINGS

| Rating                                                                                                                                                                            | Symbol                          | Value       | Unit |
|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------|-------------|------|
| Peak Repetitive Reverse Voltage<br>Working Peak Reverse Voltage<br>DC Blocking Voltage                                                                                            | $V_{RRM}$<br>$V_{RWM}$<br>$V_R$ | 100         | V    |
| Average Rectified Forward Current<br>( $V_{R(equiv)} \leq 0.2 V_R$ (dc), $R_{\theta JA} = 50^\circ\text{C/W}$ ,<br>P.C. Board Mounting, [see Note 3], $T_A = 120^\circ\text{C}$ ) | $I_O$                           | 1.0         | A    |
| Non-Repetitive Peak Surge Current<br>(Surge Applied at Rated Load Conditions<br>Halfwave, Single Phase, 60 Hz)                                                                    | $I_{FSM}$                       | 50          | A    |
| Operating and Storage Junction Temperature<br>Range (Note 1)                                                                                                                      | $T_J, T_{stg}$                  | -65 to +175 | °C   |
| Voltage Rate of Change (Rated $V_R$ )                                                                                                                                             | $dv/dt$                         | 10          | V/ns |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. The heat generated must be less than the thermal conductivity from Junction-to-Ambient:  $dP/dT_J < 1/R_{\theta JA}$ .

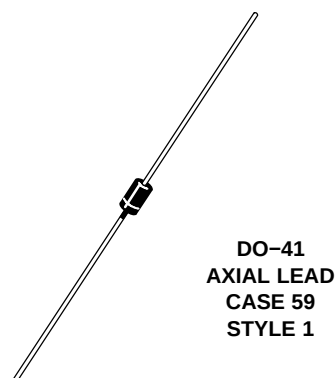
\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



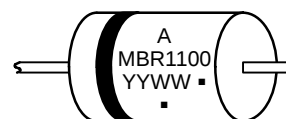
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## SCHOTTKY BARRIER RECTIFIER 1.0 AMPERE, 100 VOLTS



### MARKING DIAGRAM



A = Assembly Location  
Y = Year  
WW = Work Week  
▪ = Pb-Free Package  
(Note: Microdot may be in either location)

### ORDERING INFORMATION

| Device     | Package     | Shipping†        |
|------------|-------------|------------------|
| MBR1100    | Axial Lead* | 1000 Units/Bag   |
| MBR1100G   | Axial Lead* | 1000 Units/Bag   |
| MBR1100RL  | Axial Lead* | 5000/Tape & Reel |
| MBR1100RLG | Axial Lead* | 5000/Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

# MBR1100

## THERMAL CHARACTERISTICS (See Note 4)

| Characteristic                          | Symbol          | Max        | Unit                 |
|-----------------------------------------|-----------------|------------|----------------------|
| Thermal Resistance, Junction-to-Ambient | $R_{\theta JA}$ | See Note 3 | $^{\circ}\text{C/W}$ |

## ELECTRICAL CHARACTERISTICS ( $T_L = 25^{\circ}\text{C}$ unless otherwise noted)

| Characteristic                                                                                                                                                | Symbol | Max          | Unit |
|---------------------------------------------------------------------------------------------------------------------------------------------------------------|--------|--------------|------|
| Maximum Instantaneous Forward Voltage (Note 2)<br>( $i_F = 1\text{ A}$ , $T_L = 25^{\circ}\text{C}$ )<br>( $i_F = 1\text{ A}$ , $T_L = 100^{\circ}\text{C}$ ) | $V_F$  | 0.79<br>0.69 | V    |
| Maximum Instantaneous Reverse Current @ Rated dc Voltage (Note 2)<br>( $T_L = 25^{\circ}\text{C}$ )<br>( $T_L = 100^{\circ}\text{C}$ )                        | $i_R$  | 0.5<br>5.0   | mA   |

2. Pulse Test: Pulse Width = 300  $\mu\text{s}$ , Duty Cycle  $\leq 2.0\%$ .

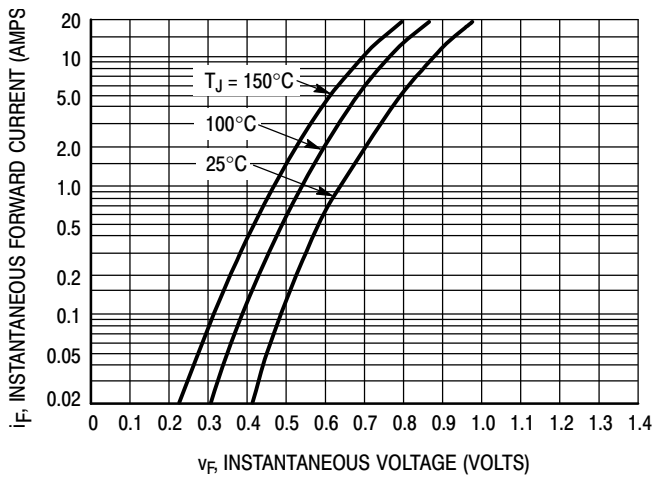


Figure 1. Typical Forward Voltage

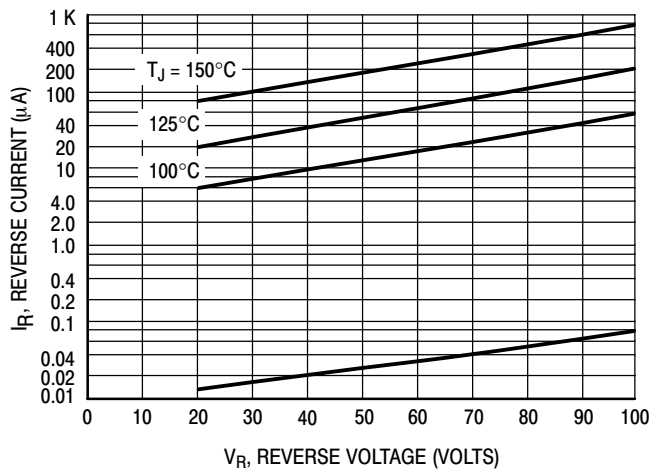


Figure 2. Typical Reverse Current †

† The curves shown are typical for the highest voltage device in the voltage grouping. Typical reverse current for lower voltage selections can be estimated from these same curves if  $V_R$  is sufficiently below rated  $V_R$ .

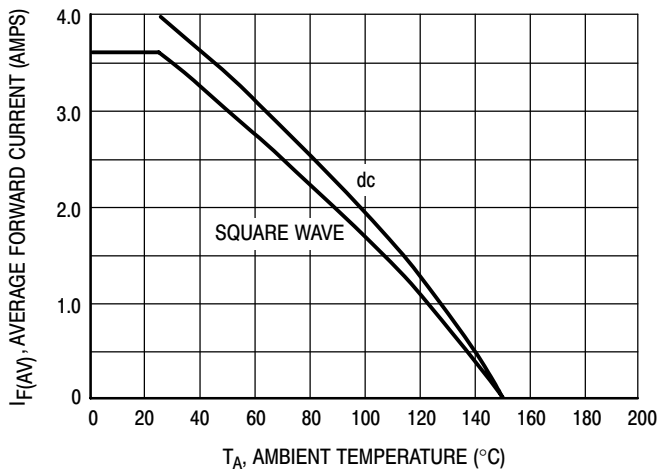


Figure 3. Current Derating  
(Mounting Method 3 per Note 3)

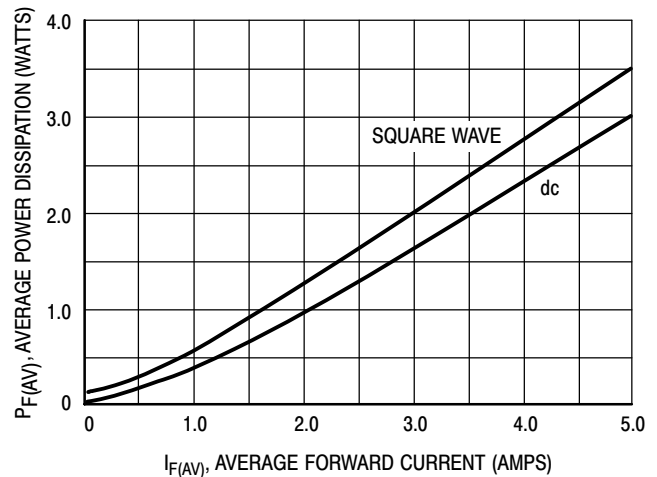


Figure 4. Power Dissipation

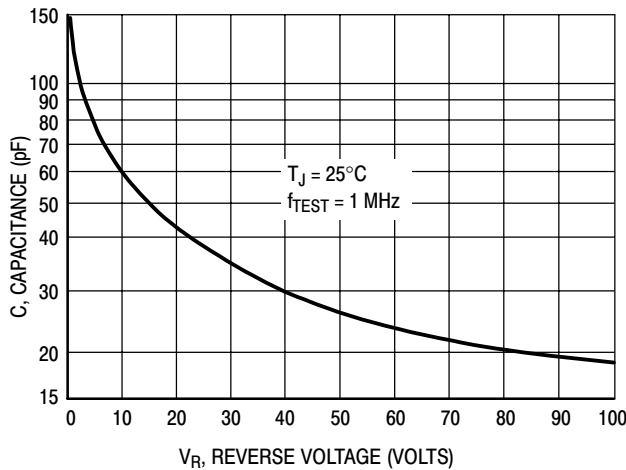


Figure 5. Typical Capacitance

**NOTE 3 — MOUNTING DATA:**

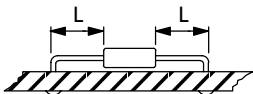
Data shown for thermal resistance junction-to-ambient ( $R_{\theta JA}$ ) for the mounting shown is to be used as a typical guideline values for preliminary engineering or in case the tie point temperature cannot be measured.

Typical Values for  $R_{\theta JA}$  in Still Air

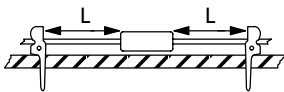
| Mounting Method | Lead Length, L (in) |     |     |     | $R_{\theta JA}$      |
|-----------------|---------------------|-----|-----|-----|----------------------|
|                 | 1/8                 | 1/4 | 1/2 | 3/4 |                      |
| 1               | 52                  | 65  | 72  | 85  | $^{\circ}\text{C/W}$ |
| 2               | 67                  | 80  | 87  | 100 | $^{\circ}\text{C/W}$ |
| 3               | —                   | 50  |     |     | $^{\circ}\text{C/W}$ |

Mounting Method 1

P.C. Board with  
1-1/2" x 1-1/2"  
copper surface.

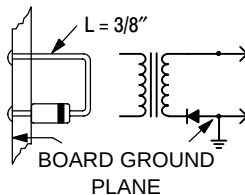


Mounting Method 2

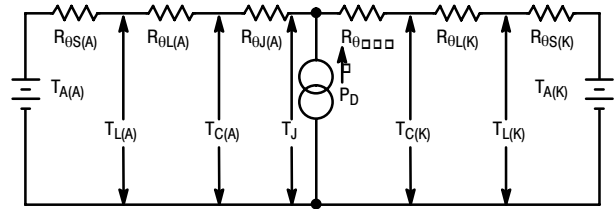


Mounting Method 3

P.C. Board with  
1-1/2" x 1-1/2"  
copper surface.



**NOTE 4 — THERMAL CIRCUIT MODEL:**  
(For heat conduction through the leads)



Use of the above model permits junction to lead thermal resistance for any mounting configuration to be found. For a given total lead length, lowest values occur when one side of the rectifier is brought as close as possible to the heat sink. Terms in the model signify:

$T_A$  = Ambient Temperature     $T_C$  = Case Temperature  
 $T_L$  = Lead Temperature     $T_J$  = Junction Temperature  
 $R_{\theta S}$  = Thermal Resistance, Heat Sink to Ambient  
 $R_{\theta L}$  = Thermal Resistance, Lead to Heat Sink  
 $R_{\theta J}$  = Thermal Resistance, Junction to Case  
 $P_D$  = Power Dissipation

(Subscripts A and K refer to anode and cathode sides, respectively.) Values for thermal resistance components are:  
 $R_{\theta L} = 100^{\circ}\text{C/W/in}$  typically and  $120^{\circ}\text{C/W/in}$  maximum.  
 $R_{\theta J} = 36^{\circ}\text{C/W}$  typically and  $46^{\circ}\text{C/W}$  maximum.

**NOTE 5 — HIGH FREQUENCY OPERATION:**

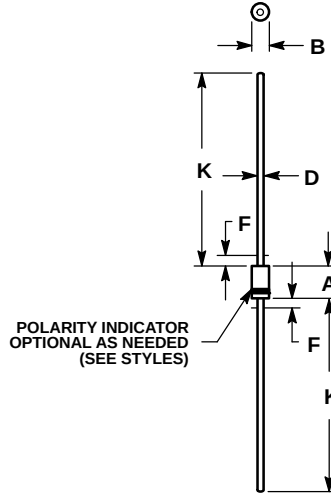
Since current flow in a Schottky rectifier is the result of majority carrier conduction, it is not subject to junction diode forward and reverse recovery transients due to minority carrier injection and stored charge. Satisfactory circuit analysis work may be performed by using a model consisting of an ideal diode in parallel with a variable capacitance. (See Figure 5)

Rectification efficiency measurements show that operation will be satisfactory up to several megahertz. For example, relative waveform rectification efficiency is approximately 70 percent at 2 MHz, e.g., the ratio of dc power to RMS power in the load is 0.28 at this frequency, whereas perfect rectification would yield 0.406 for sine wave inputs. However, in contrast to ordinary junction diodes, the loss in waveform efficiency is not indicative of power loss: it is simply a result of reverse current flow through the diode capacitance, which lowers the dc output voltage.

# MBR1100

## PACKAGE DIMENSIONS

### AXIAL LEAD CASE 59-10 ISSUE U



#### NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. ALL RULES AND NOTES ASSOCIATED WITH JEDEC DO-41 OUTLINE SHALL APPLY
4. POLARITY DENOTED BY CATHODE BAND.
5. LEAD DIAMETER NOT CONTROLLED WITHIN F DIMENSION.

| DIM | INCHES |       | MILLIMETERS |      |
|-----|--------|-------|-------------|------|
|     | MIN    | MAX   | MIN         | MAX  |
| A   | 0.161  | 0.205 | 4.10        | 5.20 |
| B   | 0.079  | 0.106 | 2.00        | 2.70 |
| D   | 0.028  | 0.034 | 0.71        | 0.86 |
| F   | ---    | 0.050 | ---         | 1.27 |
| K   | 1.000  | ---   | 25.40       | ---  |

#### STYLE 1:

- PIN 1. CATHODE (POLARITY BAND)
- ANODE

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