



Hall IC Series Omnipolar Detection Hall ICs (Polarity detection for both S and N features dual outputs)

No.10045EDT01

Description

The BU52004GUL and BU52014HFV are bipolar Hall ICs incorporating a polarity determination circuit that enables operation (output) on both the S- and N-poles, with the polarity judgment based on the output processing configuration. These Hall IC products can be in with movie, mobile phone and other applications involving crystal panels to detect the (front-back) location or determine the rotational direction of the panel.

Features

- 1) Omnipolar detection (polarity detection for both S and N features dual outputs)
- 2) Micropower operation (small current using intermittent operation method)
- 3) Ultra-compact CSP4 package(BU52004GUL)
- 4) Small outline package (BU52014HFV)
- 5) Line up of supply voltage

BU52004GUL, BU52014HFV

- For 1.8V Power supply voltage (BU52014HFV)
- For 3.0V Power supply voltage (BU52004GUL)
- 6) Polarity judgment and output on both poles (OUT1: S-pole output; OUT2: N-pole output)
- 7) High ESD resistance 8kV(HBM)

Applications

Mobile phones, notebook computers, digital video camera, digital still camera, etc.

Product Lineup

$\begin{array}{c c c c c c c c c c c c c c c c c c c $	Package
BU52004GUL 2.40~3.30 +/-3.7 * 0.8 50 8.0 CMOS VC	CSP50L1
BU52014HFV 1.65~3.30 +/-3.0 * 0.9 50 5.0 CMOS H	HVSOF5

 $\ensuremath{\Re}\xspace$ Plus is expressed on the S-pole; minus on the N-pole

Absolute Maximum Ratings

BU52004GUL (Ta=25°C)

PARAMETERS	SYMBOL	LIMIT	UNIT
Power Supply Voltage	Vdd	-0.1 ~ +4.5 ^{%1}	V
Output Current	Ιουτ	±1	mA
Power Dissipation	Pd	420 ^{**2}	mW
Operating Temperature Range	Topr	-40 ~ +85	°C
Storage Temperature Range	Tstg	-40 ~ +125	S

%1. Not to exceed Pd

%2. Reduced by 4.20mW for each increase in Ta of 1°C over 25°C

(mounted on 50mm × 58mm Glass-epoxy PCB)

BU52014 HFV (Ta=25°C)

PARAMETERS	SYMBOL	LIMIT	UNIT
Power Supply Voltage	Vdd	-0.1 ~ +4.5 ^{%3}	V
Output Current	Ιουτ	±0.5	mA
Power Dissipation	Pd	536 ^{**4}	mW
Operating Temperature Range	Topr	-40 ~ +85	C°
Storage Temperature Range	Tstg	-40 ~ +125	°C

※3. Not to exceed Pd

%4. Reduced by 5.36mW for each increase in Ta of 1°C $\,$ over 25°C $\,$

(mounted on 70mm × 70mm × 1.6mm Glass-epoxy PCB)

Magnetic, Electrical Characteristics

BU52004GUL (Unless otherwise specified, VDD=3.0V, Ta=25°C)

DADAMETEDS	PARAMETERS SYMBOL LIMIT		UNIT	CONDITIONS		
FARAMETERS	STNIBUL	MIN	TYP	MAX	UNIT	CONDITIONS
Power Supply Voltage	V _{DD}	2.4	3.0	3.3	V	
Onerete Deint	B _{opS}	-	3.7	5.5	mT	OUTPUT : OUT1 (respond the south pole)
Operate Point	B _{opN}	-5.5	-3.7	-	1111	OUTPUT : OUT2 (respond the north pole)
Release Point	B _{rpS}	0.8	2.9	-	mT	OUTPUT : OUT1 (respond the south pole)
Release Point	B _{rpN}	-	-2.9	-0.8		OUTPUT : OUT2 (respond the north pole)
Liveteracia	B _{hysS}	-	0.8	-	mT	
Hysteresis	B _{hysN}	-	0.8	-		
Period	Tp	-	50	100	ms	
Output High Voltage	V _{OH}	V _{DD} -0.4	-	-	V	B _{rpN} <b<b<sub>rpS ^{*5} I_{OUT} =-1.0mA</b<b<sub>
Output Low Voltage	V _{OL}	-	-	0.4	V	B <b<sub>opN, B_{opS}<b <sup="">*5 I_{OUT} =+1.0mA</b<sub>
Supply Current	I _{DD(AVG)}	-	8	12	μA	Average
Supply Current During Startup Time	I _{DD(EN)}	-	4.7	-	mA	During Startup Time Value
Supply Current During Standby Time	I _{DD(DIS)}	-	3.8	-	μA	During Standby Time Value

※5. B = Magnetic flux density

1mT=10Gauss

Positive ("+") polarity flux is defined as the magnetic flux from south pole which is direct toward to

the branded face of the sensor.

After applying power supply, it takes one cycle of period (T_{P}) to become definite output.

Radiation hardiness is not designed.

BU52014HFV (Unless otherwise specified, VDD=1.80V, Ta=25°C)

PARAMETERS	SYMBOL	LIMIT			CONDITIONS	
PARAMETERS	STMBOL	MIN	TYP	MAX	UNIT	CONDITIONS
Power Supply Voltage	V_{DD}	1.65	1.80	3.30	V	
Operate Point	B _{opS}	-	3.0	5.0		OUTPUT : OUT1 (respond the south pole)
Operate Point	B _{opN}	-5.0	-3.0	-	mT	OUTPUT : OUT2 (respond the north pole)
Delegee Deint	B _{rpS}	0.6	2.1	-	mT.	OUTPUT : OUT1 (respond the south pole)
Release Point	B _{rpN}	-	-2.1	-0.6	mT	OUTPUT : OUT2 (respond the north pole)
	B _{hysS}	-	0.9	-	T	
Hysteresis	B _{hysN}	-	0.9	-	mT	
Period	T _p	-	50	100	ms	
Output High Voltage	V _{он}	V _{DD} -0.2	-	-	V	B _{rpN} <b<b<sub>rpS ^{%6} I_{OUT} =-0.5mA</b<b<sub>
Output Low Voltage	V _{OL}	-	-	0.2	V	B <b<sub>opN, B_{opS}<b <sup="">%6 I_{OUT} =+0.5mA</b<sub>
Supply Current 1	I _{DD1(AVG)}	-	5	8	μA	V _{DD} =1.8V, Average
Supply Current During Startup Time 1	I _{DD1(EN)}	-	2.8	-	mA	V _{DD} =1.8V, During Startup Time Value
Supply Current During Standby Time 1	I _{DD1(DIS)}	-	1.8	-	μA	V _{DD} =1.8V, During Standby Time Value
Supply Current 2	I _{DD2(AVG)}	-	8	12	μA	V _{DD} =2.7V, Average
Supply Current During Startup Time 2	I _{DD2(EN)}	-	4.5	-	mA	V _{DD} =2.7V, During Startup Time Value
Supply Current During Standby Time 2	I _{DD2(DIS)}	-	4.0	-	μA	V _{DD} =2.7V, During Standby Time Value

%6. B = Magnetic flux density

1mT=10Gauss

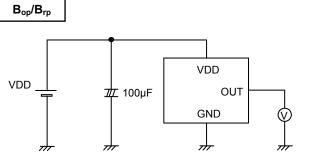
Positive ("+") polarity flux is defined as the magnetic flux from south pole which is direct toward to

the branded face of the sensor.

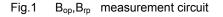
After applying power supply, it takes one cycle of period (T_P) to become definite output.

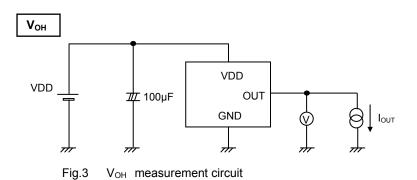
Radiation hardiness is not designed.

•Figure of measurement circuit



Bop and Brp are measured with applying the magnetic field from the outside.





T_p 200Ω VDD VDD VDD OUT O GND GND

The period is monitored by Oscilloscope.

Fig.2 T_p measurement circuit

Product Name	I _{OUT}
BU52004GUL	1.0mA
BU52014HFV	0.5mA

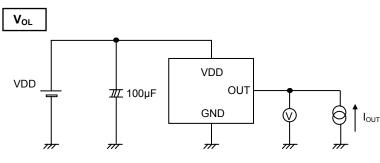


Fig.4 V_{OL} measurement circuit

I_{DD}

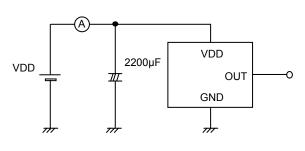
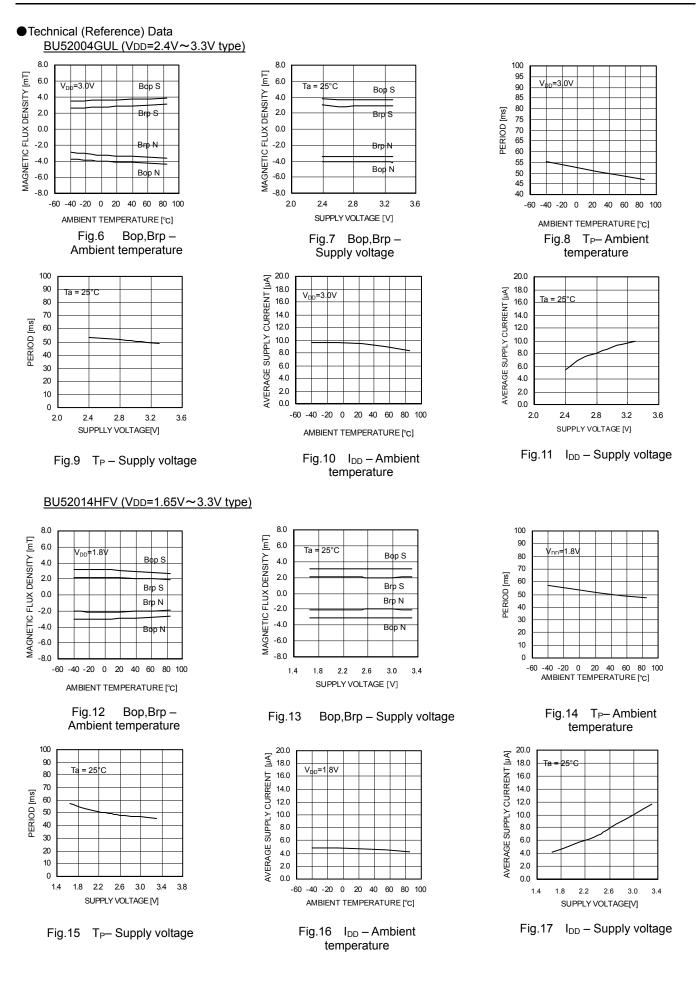
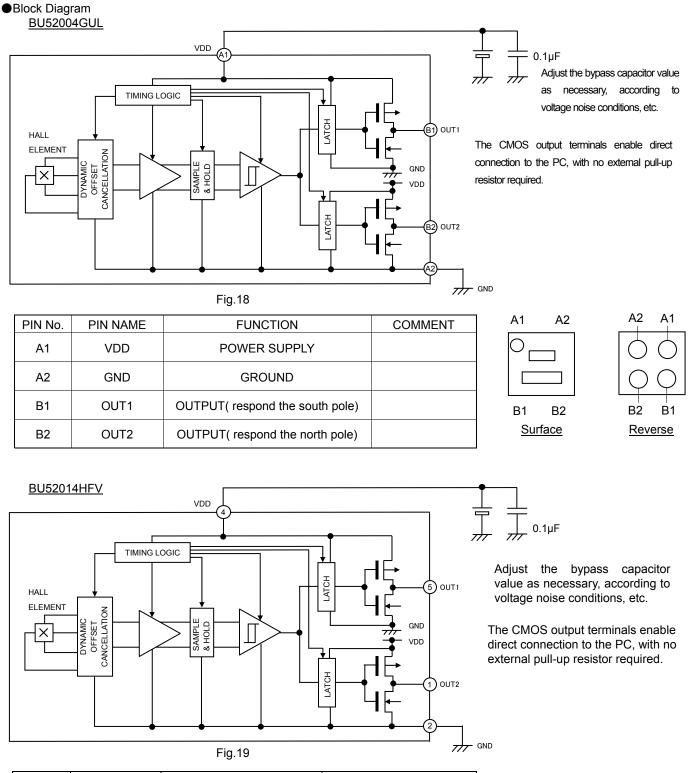


Fig.5 I_{DD} measurement circuit

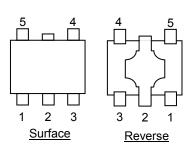
Product Name	I _{OUT}
BU52004GUL	1.0mA
BU52014HFV	0.5mA

BU52004GUL, BU52014HFV





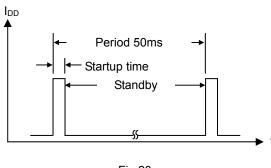
PIN No.	PIN NAME	FUNCTION	COMMENT
1	OUT2	OUTPUT (respond the north pole)	
2	GND	GROUND	
3	N.C.		OPEN or Short to GND.
4	VDD	POWER SUPPLY	
5 OUT1		OUTPUT (respond the south pole)	



• Description of Operations

(Offset Cancelation)

Micropower Operation (Small current using intermittent action)





Hall Voltage

 V_{DD}

В⊗

卅 GND

Fig.21

The dual output bipolar detection Hall IC adopts an intermittent operation method to save energy. At startup, the Hall elements, amp, comparator and other detection circuits power ON and magnetic detection begins. During standby, the detection circuits power OFF, thereby reducing current consumption. The detection results are held while standby is active, and then output.

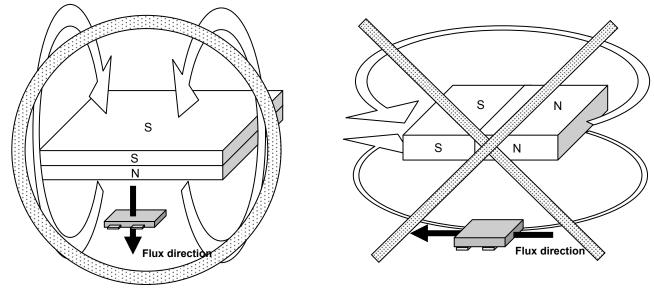
Reference period: 50ms (MAX100ms) Reference startup time: 48µs

The Hall elements form an equivalent Wheatstone (resistor) bridge circuit. Offset voltage may be generated by a differential in this bridge resistance, or can arise from changes in resistance due to package or bonding stress. A dynamic offset cancellation circuit is employed to cancel this offset voltage.

When Hall elements are connected as shown in Fig. 21 and a magnetic field is applied perpendicular to the Hall elements, voltage is generated at the mid-point terminal of the bridge. This is known as Hall voltage.

Dynamic cancellation switches the wiring (shown in the figure) to redirect the current flow to a 90° angle from its original path, and thereby cancels the Hall voltage.

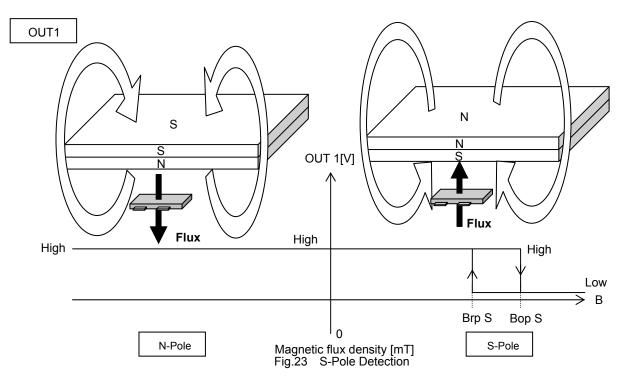
The magnetic signal (only) is maintained in the sample/hold circuit during the offset cancellation process and then released.



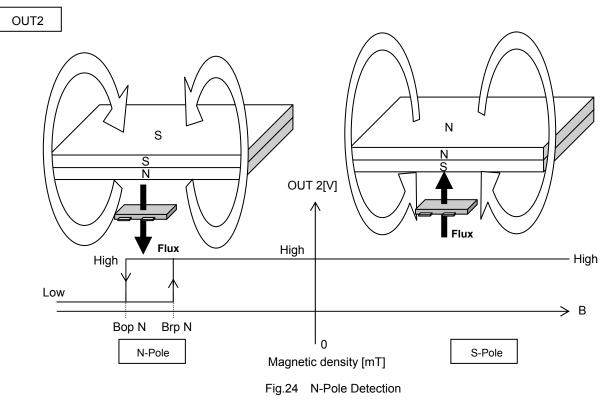
(Magnetic Field Detection Mechanism)

Fig.22

The Hall IC cannot detect magnetic fields that run horizontal to the package top layer. Be certain to configure the Hall IC so that the magnetic field is perpendicular to the top layer.



The OUT1 pin detects and outputs for the S-pole only. Since it is unipolar, it does not recognize the N-pole.



The OUT2 pin detects and outputs for the N-pole only. Since it is unipolar, it does not recognize the S-pole. The dual output Omnipolar detection Hall IC detects magnetic fields running perpendicular to the top surface of the package. There is an inverse relationship between magnetic flux density and the distance separating the magnet and the Hall IC: when distance increases magnetic density falls. When it drops below the operate point (Bop), output goes HIGH. When the magnet gets closer to the IC and magnetic density rises, to the operate point, the output switches LOW. In LOW output mode, the distance from the magnet to the IC increases again until the magnetic density falls to a point just below Bop, and output returns HIGH. (This point, where magnetic flux density restores HIGH output, is known as the release point, Brp.) This detection and adjustment mechanism is designed to prevent noise, oscillation and other erratic system operation.

Intermittent Operation at Power ON

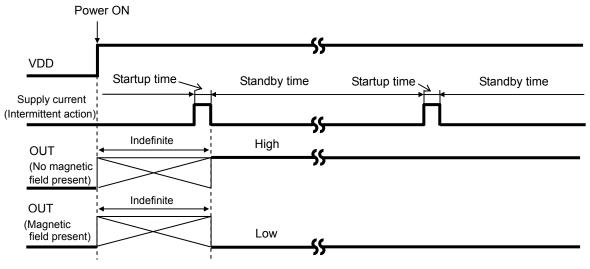
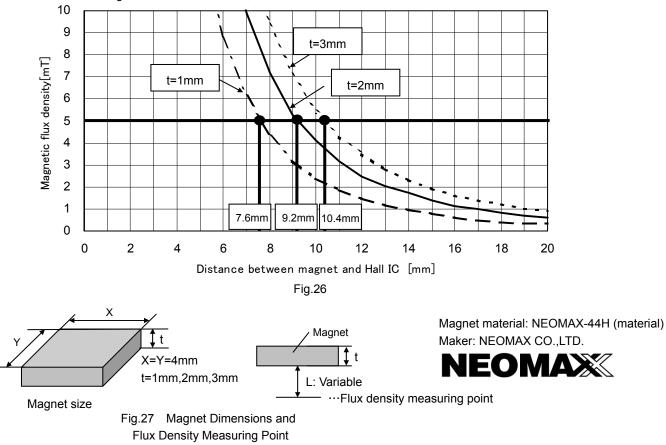


Fig.25

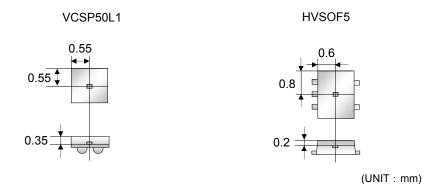
The dual output Omnipolar detection Hall IC adopts an intermittent operation method in detecting the magnetic field during startup, as shown in Fig. 25. It outputs to the appropriate terminal based on the detection result and maintains the output condition during the standby period. The time from power ON until the end of the initial startup period is an indefinite interval, but it cannot exceed the maximum period, 100ms. To accommodate the system design, the Hall IC output read should be programmed within 100ms of power ON, but after the time allowed for the period ambient temperature and supply voltage.

Magnet Selection

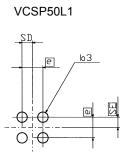
Of the two representative varieties of permanent magnet, neodymium generally offers greater magnetic power per volume than ferrite, thereby enabling the highest degree of miniaturization, Thus, neodymium is best suited for small equipment applications. Fig. 26 shows the relation between the size (volume) of a neodymium magnet and magnetic flux density. The graph plots the correlation between the distance (L) from three versions of a 4mm X 4mm cross-section neodymium magnet (1mm, 2mm, and 3mm thick) and magnetic flux density. Fig. 27 shows Hall IC detection distance – a good guide for determining the proper size and detection distance of the magnet. Based on the BU52014HFV operating point max 5.0 mT, the minimum detection distance for the 1mm, 2mm and 3mm magnets would be 7.6mm, 9.22mm, and 10.4mm, respectively. To increase the magnet's detection distance, either increase its thickness or sectional area.



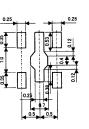
Position of the Hall Effect IC(Reference)



•Footprint dimensions (Optimize footprint dimensions to the board design and soldering condition)



HVSOF5



(UNIT : mm)

Strings	Size(Typ)
е	0.50
b3	0.25
SD	0.25
SE	0.25

Terminal Equivalent Circuit Diagram

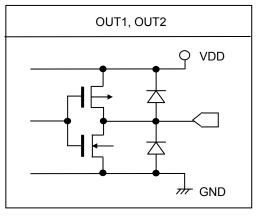


Fig.28

Because they are configured for CMOS (inverter) output, the output pins require no external resistance and allow direct connection to the PC. This, in turn, enables reduction of the current that would otherwise flow to the external resistor during magnetic field detection, and supports overall low current (micropower) operation.

Operation Notes

1) Absolute maximum ratings

Exceeding the absolute maximum ratings for supply voltage, operating conditions, etc. may result in damage to or destruction of the IC. Because the source (short mode or open mode) cannot be identified if the device is damaged in this way, it is important to take physical safety measures such as fusing when implementing any special mode that operates in excess of absolute rating limits.

2) GND voltage

Make sure that the GND terminal potential is maintained at the minimum in any operating state, and is always kept lower than the potential of all other pins.

3) Thermal design

Use a thermal design that allows for sufficient margin in light of the power dissipation (Pd) in actual operating conditions.

4) Pin shorts and mounting errors

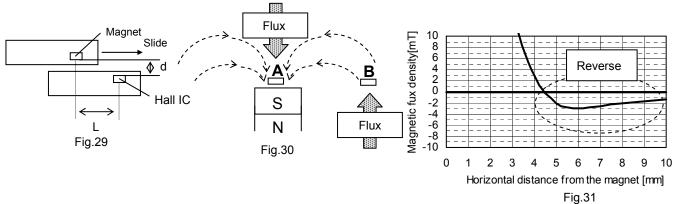
Use caution when positioning the IC for mounting on printed circuit boards. Mounting errors, such as improper positioning or orientation, may damage or destroy the device. The IC may also be damaged or destroyed if output pins are shorted together, or if shorts occur between the output pin and supply pin or GND.

5) Positioning components in proximity to the Hall IC and magnet

Positioning magnetic components in close proximity to the Hall IC or magnet may alter the magnetic field, and therefore the magnetic detection operation. Thus, placing magnetic components near the Hall IC and magnet should be avoided in the design if possible. However, where there is no alternative to employing such a design, be sure to thoroughly test and evaluate performance with the magnetic component(s) in place to verify normal operation before implementing the design.

6) Slide-by position sensing

Fig.29 depicts the slide-by configuration employed for position sensing. Note that when the gap (d) between the magnet and the Hall IC is narrowed, the reverse magnetic field generated by the magnet can cause the IC to malfunction. As seen in Fig.30, the magnetic field runs in opposite directions at Point A and Point B. Since the dual output Omnipolar detection Hall IC can detect the S-pole at Point A and the N-pole at Point B, it can wind up switching output ON as the magnet slides by in the process of position detection. Fig. 31 plots magnetic flux density during the magnet slide-by. Although a reverse magnetic field was generated in the process, the magnetic flux density decreased compared with the center of the magnet. This demonstrates that slightly widening the gap (d) between the magnet and Hall IC reduces the reverse magnetic field and prevents malfunctions.



7) Operation in strong electromagnetic fields

Exercise extreme caution about using the device in the presence of a strong electromagnetic field, as such use may cause the IC to malfunction.

8) Common impedance

Make sure that the power supply and GND wiring limits common impedance to the extent possible by, for example, employing short, thick supply and ground lines. Also, take measures to minimize ripple such as using an inductor or capacitor.

9) GND wiring pattern

When both a small-signal GND and high-current GND are provided, single-point grounding at the reference point of the set PCB is recommended, in order to separate the small-signal and high-current patterns, and to ensure that voltage changes due to the wiring resistance and high current do not cause any voltage fluctuation in the small-signal GND. In the same way, care must also be taken to avoid wiring pattern fluctuations in the GND wiring pattern of external components.

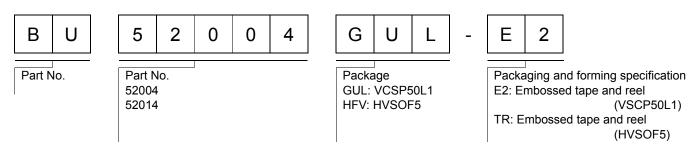
10) Exposure to strong light

Exposure to halogen lamps, UV and other strong light sources may cause the IC to malfunction. If the IC is subject to such exposure, provide a shield or take other measures to protect it from the light. In testing, exposure to white LED and fluorescent light sources was shown to have no significant effect on the IC.

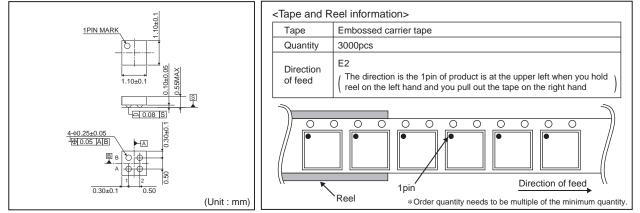
11) Power source design

Since the IC performs intermittent operation, it has peak current when it's ON. Please taking that into account and under examine adequate evaluations when designing the power source.

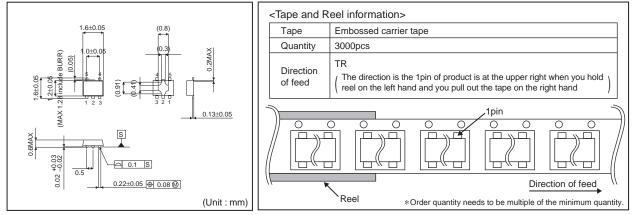
Ordering part number



VCSP50L1(BU52004GUL)



HVSOF5



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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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 - [d] the Products are exposed to high Electrostatic
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- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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Общество с ограниченной ответственностью «МосЧип» ИНН 7719860671 / КПП 771901001 Адрес: 105318, г.Москва, ул.Щербаковская д.З, офис 1107

Данный компонент на территории Российской Федерации

Вы можете приобрести в компании MosChip.

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Вы можете разместить у нас заказ для любого Вашего проекта, будь то серийное производство или разработка единичного прибора.

В нашем ассортименте представлены ведущие мировые производители активных и пассивных электронных компонентов.

Нашей специализацией является поставка электронной компонентной базы двойного назначения, продукции таких производителей как XILINX, Intel (ex.ALTERA), Vicor, Microchip, Texas Instruments, Analog Devices, Mini-Circuits, Amphenol, Glenair.

Сотрудничество с глобальными дистрибьюторами электронных компонентов, предоставляет возможность заказывать и получать с международных складов практически любой перечень компонентов в оптимальные для Вас сроки.

На всех этапах разработки и производства наши партнеры могут получить квалифицированную поддержку опытных инженеров.

Система менеджмента качества компании отвечает требованиям в соответствии с ГОСТ Р ИСО 9001, ГОСТ РВ 0015-002 и ЭС РД 009

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