

SOTiny™ Low Voltage Dual SPDT Analog Switch 2:1 Mux/DeMux Bus Switch

Features

- CMOS Technology for Bus and Analog Applications
- Low On-Resistance: 8Ω at 3.0V
- Wide V_{DD} Range: 1.65V to 5.5V
- Rail-to-Rail Signal Range
- Control Input Overvoltage Tolerance: 5.5V min.
- Fast Transition Speed: 5.2ns max. at 5V
- High Off Isolation: 57dB at 10MHz
- 54dB (10MHz) Crosstalk Rejection Reduces Signal Distortion
- Break-Before-Make Switching
- High Bandwidth: 250 MHz
- Extended Industrial Temperature Range: -40°C to 85°C
- Improved Direct Replacement for NC7SB3157
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. “Green” Device (Note 3)
- Packaging (Pb-free & Green available):
 - 6-pin SC70 (C)

Pin Description

Pin Number	Name	Description
1	B1	Data port
2	GND	Ground
3	B ₀	Data port (Normally Closed)
4	A	Common Output/Data port
5	V _{CC}	Positive Power Supply
6	S	Logic Controll

Logic Function Table

Logic Input(s)	Function
0	B ₀ Connection to A
1	B ₁ Connected to A

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Description

The PI5A3157 is a high-bandwidth, fast single-pole double-throw (SPDT) CMOS switch. It can be used as an analog switch or as a low-delay bus switch. Specified over a wide operating power supply voltage range, 1.65V to 5.5V, the PI5A3157 has a maximum ON resistance of 12-ohms at 1.65V, 9-ohms at 2.3V & 6-ohms at 4.5V.

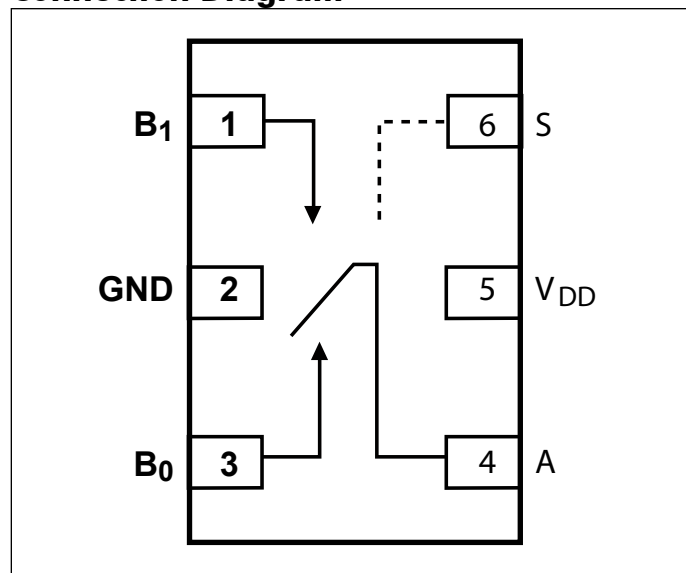
Break-before-make switching prevents both switches being enabled simultaneously. This eliminates signal disruption during switching. The control input, S, tolerates input drive signals up to 5.5V, independent of supply voltage.

PI5A3157 is an improved direct replacement for the NC7SB3157.

Applications

- Cell Phones
- PDAs
- Portable Instrumentation
- Battery Powered Communications
- Computer Peripherals

Connection Diagram



Absolute Maximum Ratings⁽¹⁾

Supply Voltage V_{DD}	−0.5V to +7V
DC Switch Voltage (V_S) ⁽²⁾	−0.5V to V_{DD} +0.5V
DC Input Voltage (V_{IN}) ⁽²⁾	−0.5V to +7.0V
DC Output Current (V_{OUT})	128mA
DC V_{DD} or Ground Current (I_{CC}/I_{GND})	±100mA
Storage Temperature Range (T_{STG})	−65°C to +150°C
Junction Temperature under Bias (T_J)	150°C
Junction Lead Temperature (T_L) (Soldering, 10 seconds)	260°C
Power Dissipation (P_D) @ +85°C	180mW

Recommended Operating Conditions⁽³⁾

Supply Voltage Operating (V_{DD})	1.65V to 5.5V
Control Input Voltage (V_{IN})	0V to V_{DD}
Switch Input Voltage (V_{IN})	0V to V_{DD}
Output Voltage (V_{OUT})	0V to V_{DD}
Operating Temperature (T_A)	−40°C to +85°C
Input Rise and Fall Time (t_r, t_f)	
Control Input V_{DD} = 2.3V - 3.6V	0ns/V to 10ns/V
Control Input V_{DD} = 4.5V - 5.5V	0ns/V to 5ns/V
Thermal Resistance (θ_{JA})	350°C/W

Notes:

1. Absolute Maximum Ratings” may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied.
2. The input and output negative voltage ratings may be exceeded if the input and output diode current ratings are observed.
3. Control input must be held HIGH or LOW; it must not float.

DC Electrical Characteristics (Over the Operating temperature range, T_A = −40°C to 85°C)

Parameters	Description	Test Conditions	Supply Voltage	Temp	Min.	Typ.	Max.	Units
V _{IAR}	Analog Input Signal Range		V _{DD}	T _A = 25°C & -40°C to 85°C	0		V _{DD}	V
R _{ON}	On-Resistance ⁽⁴⁾	I _O = 30mA, V _{IN} = 0V	4.5V	T _A = 25°C		4	6	Ω
		I _O = -30mA, V _{IN} = 2.4V				5	8	
		I _O = -30mA, V _{IN} = 4.5V				8	13	
R _{ON}		I _O = 30mA, V _{IN} = 0V	4.5V	T _A = -40°C to 85°C			6	
		I _O = -30mA, V _{IN} = 2.4V					8	
		I _O = -30mA, V _{IN} = 4.5V					13	
R _{ON}		I _O = 24mA, V _{IN} = 0V	3.0V	T _A = 25°C		5	8	
		I _O = -24mA, V _{IN} = 3.0V				12	19	
R _{ON}		I _O = 24mA, V _{IN} = 0V	3.0V	T _A = -40°C to 85°C			8	
		I _O = -24mA, V _{IN} = 3.0V					19	
R _{ON}		I _O = 24mA, V _{IN} = 0V	2.3V	T _A = 25°C		6	9	
		I _O = -24mA, V _{IN} = 2.3V				16	24	
R _{ON}		I _O = 24mA, V _{IN} = 0V	2.3V	T _A = -40°C to 85°C			9	
		I _O = -24mA, V _{IN} = 2.4V					24	
R _{ON}		I _O = 24mA, V _{IN} = 0V	1.65V	T _A = 25°C		8	12	
		I _O = -24mA, V _{IN} = 1.65V				27	39	
R _{ON}		I _O = 24mA, V _{IN} = 0V	1.65V	T _A = -40°C to 85°C			12	
		I _O = -24mA, V _{IN} = 1.65V					39	

PI5A3157
DC Electrical Characteristics Cont. (Over the Operating temperature range, $T_A = -40^{\circ}\text{C}$ to 85°C)

Parameters	Description	Test Conditions	Supply Voltage	Temp	Min.	Typ.	Max.	Units
ΔR_{ON}	On-Resistance Match Between Channels ^(4, 5, 6)	$I_A = -30\text{mA}$, $V_{BN} = 3.15\text{V}$	4.5V	$T_A = 25^{\circ}\text{C}$		0.15		Ω
		$I_A = -24\text{mA}$, $V_{BN} = 2.1\text{V}$	3.0V			0.2		
		$I_A = -8\text{mA}$, $V_{BN} = 1.6\text{V}$	2.3V			0.3		
		$I_A = -4\text{mA}$, $V_{BN} = 1.15\text{V}$	1.65V			0.3		
R_{ONF}	On-Resistance Flatness ^(4, 5, 7)	$I_A = -30\text{mA}$, $0 \leq V_{BN} \leq V_{DD}$	5.0V	$T_A = 25^{\circ}\text{C}$		6		Ω
		$I_A = -24\text{mA}$, $0 \leq V_{BN} \leq V_{DD}$	3.3V			12		
		$I_A = -8\text{mA}$, $0 \leq V_{BN} \leq V_{DD}$	2.5V			22		
		$I_A = -4\text{mA}$, $0 \leq V_{BN} \leq V_{DD}$	1.8V			90		
V_{IH}	Input High Voltage	Logic High Level	$V_{CC} = 1.65\text{V}$ to 1.95V	$T_A = 25^{\circ}\text{C}$ & -40°C to 85°C	$0.75 V_{CC}$			V
			$V_{CC} = 2.3\text{V}$ to 5.5V		$0.7 V_{CC}$			
V_{IL}	Input Low Voltage	Logic LowLevel	$V_{CC} = 1.65\text{V}$ to 1.95V				$0.25 V_{CC}$	V
			$V_{CC} = 2.3\text{V}$ to 5.5V				$0.25 V_{CC}$	
	Input Leakage Curent	$0 \leq V_{IN} \leq 5.5\text{V}$	$V_{CC} \leq 0\text{V} \leq 5.5\text{V}$	$T_A = 25^{\circ}\text{C}$			± 0.1	μA
				$T_A = -40^{\circ}\text{C}$ to 85°C			± 1.0	
I_{OFF}	OFF State Leak-age Current	$0 \leq V_{IN} \leq 5.5\text{V}$	$V_{CC} \leq 1.65\text{V} \leq 5.5\text{V}$	$T_A = 25^{\circ}\text{C}$			± 0.1	
				$T_A = -40^{\circ}\text{C}$ to 85°C			± 10	
I_{CC}	Quiescent Supply Current	All Channels ON or OFF, $V_{IN} = V_{DD}$ or GND, $I_{OUT} = 0$	$V_{CC} = 5.5\text{V}$	$T_A = 25^{\circ}\text{C}$			1	
				$T_A = -40^{\circ}\text{C}$ to 85°C			10	

Notes:

- Measured by voltage drop between A and B pins at the indicated current through the device. On-Resistance is determined by the lower of the voltages on two ports (A or B).
- Parameter is characterized but not tested in production.
- $\Delta R_{ON} = R_{ON \text{ max}} - R_{ON \text{ min}}$, measured at identical V_{DD} , temperature and voltage levels.
- Flatness is defined as difference between maximum and minimum value of On-Resistance over the specified range of conditions.
- Guaranteed by design.

Capacitance⁽¹²⁾

Parameters	Description	Test Conditions	Supply Voltage	Temp	Min.	Typ.	Max.	Units
C_{IN}	Controll Input	$f = 1 \text{ MHz}^{(12)}$	$V_{CC} = 5.0\text{V}$	$T_A = 25^{\circ}\text{C}$		2.3		pF
C_{IO-B}	For B Port, Switch OFF					6.5		
C_{IOA-ON}	For A Port, Switch ON					18.5		

Switch and AC Characteristics

Parameters	Description	Test Conditions	Supply Voltage	Temp	Min.	Typ.	Max.	Units
t _{PLH} t _{PHL}	Propagation Delay: A to Bn	See test circuit diagram 1 and 2 V _I Open ⁽¹⁰⁾	V _{CC} = 2.3V to 2.7V	T _A = 25°C & -40°C to 85°C		1.2		ns
			V _{CC} = 3.0V to 3.6V			0.8		
			V _{CC} = 4.5V to 5.5V			0.3		
t _{PZL} t _{PZH}	Output Enable Turn ON Time: A to Bn	See test circuit diagram 1 and 2 V _I = 2 V _{CC} for t _{PZL} , V _I = 0V for t _{PZH}	V _{CC} = 1.65V to 1.95V	T _A = 25°C	7		23	
			V _{CC} = 2.3V to 2.7V		3.5		13	
			V _{CC} = 3.0V to 3.6V		2.5		6.9	
			V _{CC} = 4.5V to 5.5V		1.7		5.2	
t _{PZL} t _{PHZ}	Output Enable Turn ON Time: A to Bn	See test circuit diagram 1 and 2 V _I = 2 V _{CC} for t _{PZL} , V _I = 0V for t _{PZH}	V _{CC} = 2.5V	T _A = 25°C & -40°C to 85°C			24	
			V _{CC} = 3.3V				14	
			V _{CC} = 3.0V to 3.6V				7.6	
			V _{CC} = 4.5V to 5.5V				5.7	
t _{PLZ} t _{PHZ}	Output Disable-Turn OFF Time: A to Bn	See test circuit diagram 1 and 2 V _I = 2 V _{CC} for t _{PZL} , V _I = 0V for t _{PZH}	V _{CC} = 1.65V to 1.95V	T _A = 25°C	3		12.5	
			V _{CC} = 2.3V to 2.7V		2		7	
			V _{CC} = 3.0V to 3.6V		1.5		5	
			V _{CC} = 4.5V to 5.5V		0.8		3.5	
t _{PLZ} t _{PHZ}	Output Disable-Turn OFF Time: A to Bn	See test circuit diagram 1 and 2 V _I = 2 V _{CC} for t _{PZL} , V _I = 0V for t _{PZH}	V _{CC} = 2.5V	T _A = 25°C & -40°C to 85°C			13	
			V _{CC} = 3.3V				7.5	
			V _{CC} = 3.0V to 3.6V				5.3	
			V _{CC} = 4.5V to 5.5V				3.8	
t _{BM}	Break Before Make Time	See Test Circuit diagram 9. ⁽⁹⁾	V _{CC} = 2.5V	T _A = 25°C & -40°C to 85°C	0.5			
			V _{CC} = 3.3V		0.5			
			V _{CC} = 3.0V to 3.6V		0.5			
			V _{CC} = 4.5V to 5.5V		0.5			
Q	Charge Injection	C _L = 0.1nF, V _{GEN} = 0V, R _{GEN} = 0Ω, See test circuit 4	V _{CC} = 5.0V	T _A = 25°C		7		pC
			V _{CC} = 3.3V			3		
O _{IRR}	Off Isolation	R _L = 50Ω, V _{GEN} = 0V, R _{GEN} = 0Ω, See test circuit 5 ⁽¹¹⁾	V _{CC} = 1.65V to 5.5V	T _A = 25°C		-57		dB
X _{TALK}	Crosstalk Isolation	See test circuit 6	V _{CC} = 1.65V to 5.5V	T _A = 25°C		-54		
f _{3dB}	-3dB Bandwidth	See test circuit 9	V _{CC} = 1.65V to 5.5V	T _A = 25°C		250		MHz

Notes:

9. Guaranteed by design.
10. Guaranteed by design but not production tested. The device contributes no other propagation delay other than the RC delay of the switch On-Resistance and the 50pF load capacitance, when driven by an ideal voltage source with zero output impedance.
11. Off Isolation = 20 Log₁₀ [V_A / V_{Bn}] and is measured in dB.
12. T_A = 25°C, f = 1MHz. Capacitance is characterized but not tested in production.

Test Circuits and Timing Diagrams

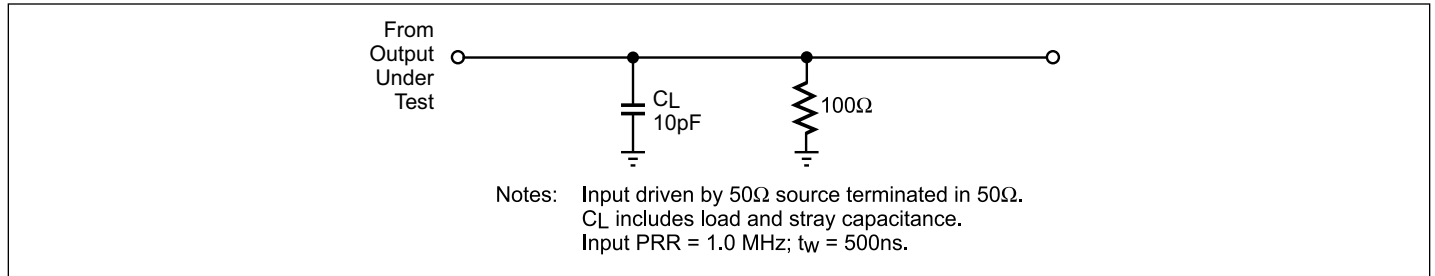


Figure 1. AC Test Circuit

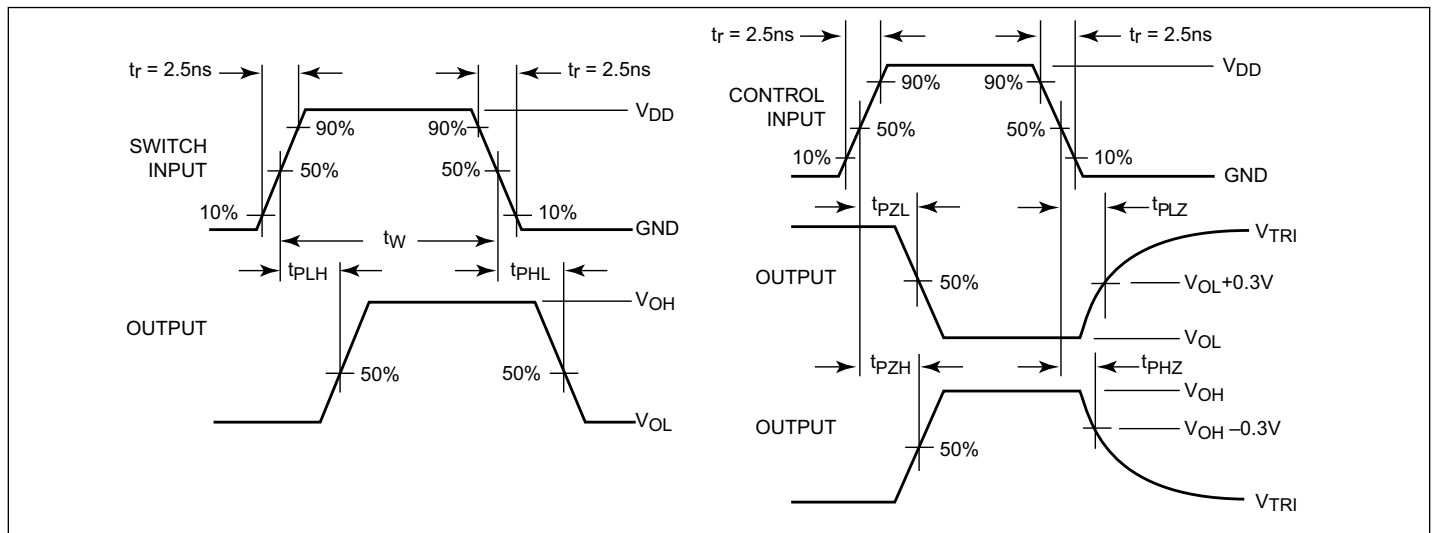


Figure 2. AC Waveforms

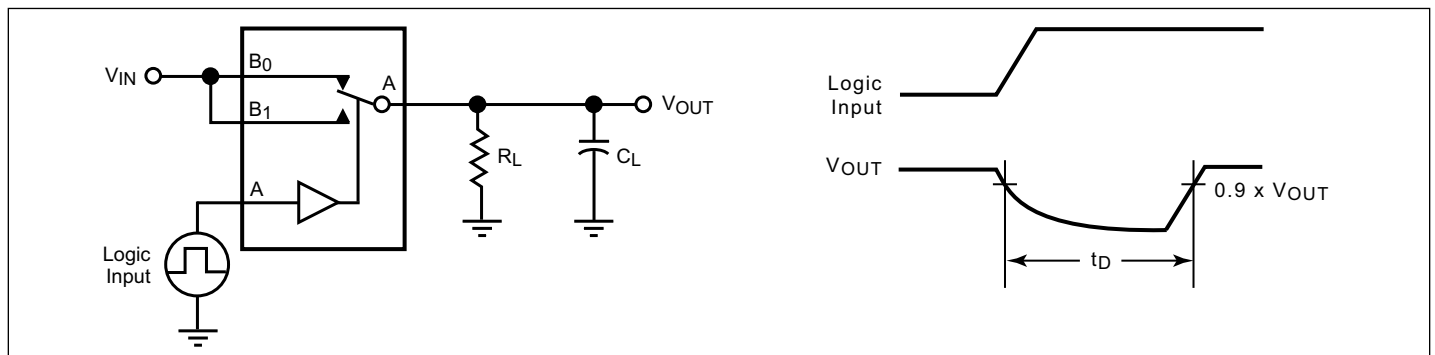


Figure 3. Break Before Make Interval Timing

PI5A3157

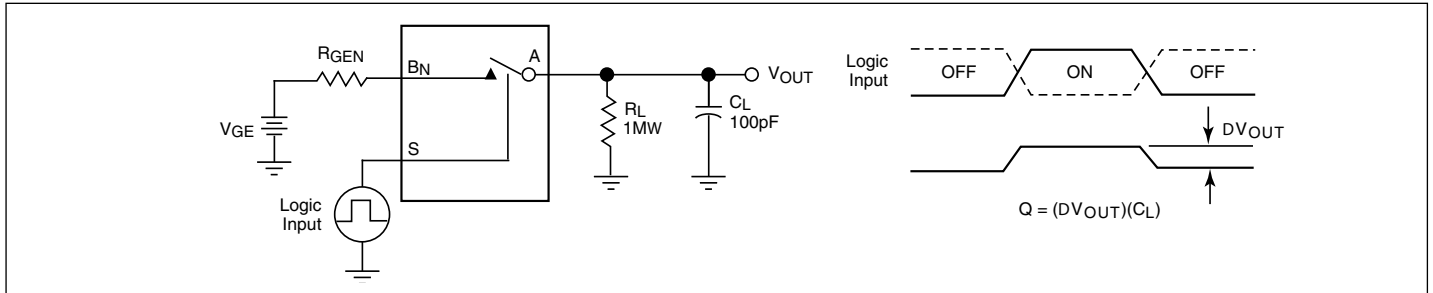


Figure 4. Charge Injection Test

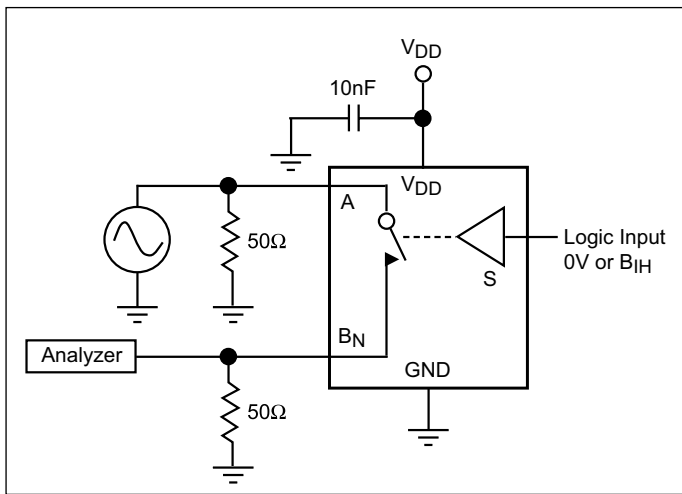


Figure 5. Off Isolation

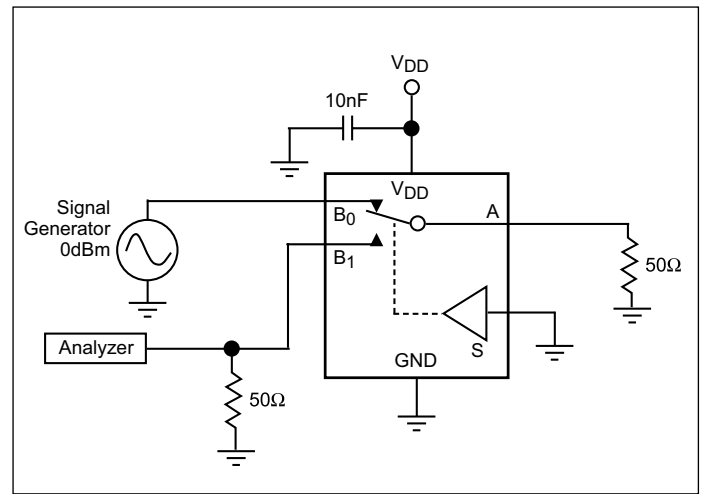


Figure 6. Crosstalk

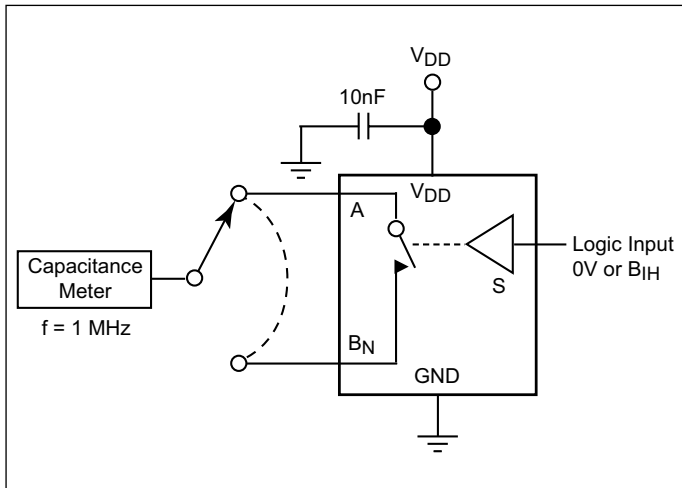


Figure 7. Channel Off Capacitance

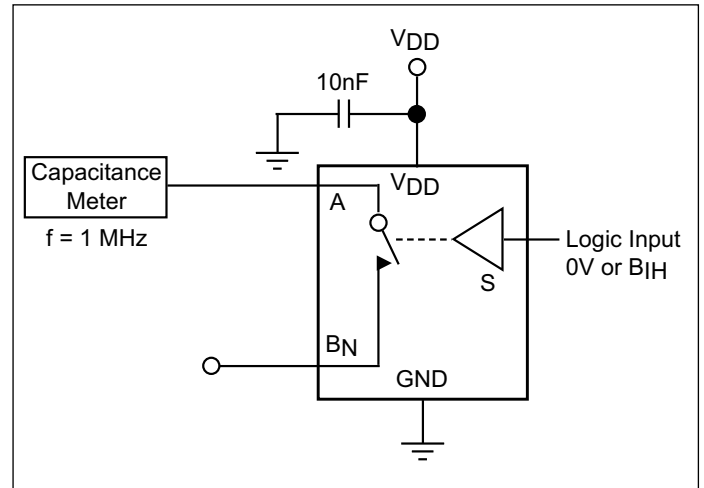


Figure 8. Channel On Capacitance

PI5A3157

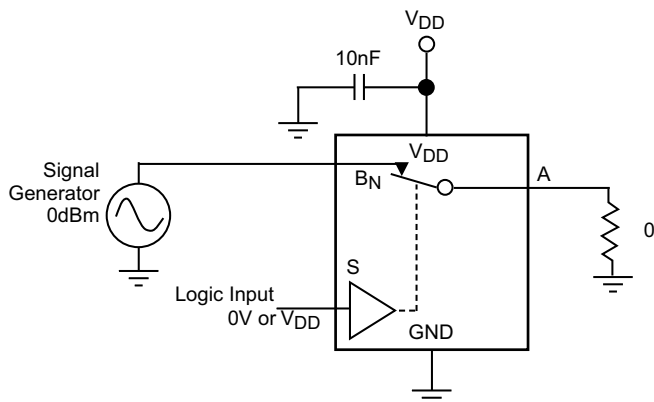


Figure 9. Bandwidth

Part Marking

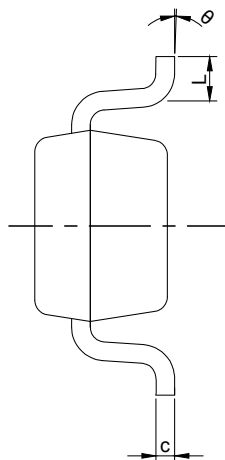
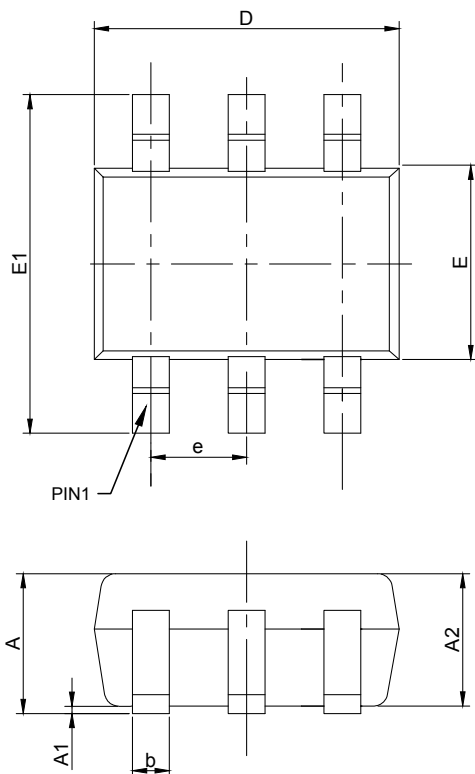
C Package



kD: PI5A3157BC6E

XX: Date Code (Year & Workweek)

The Bar of "D" means Fab3 of Magnachip

PI5A3157
Packaging Mechanical: 6-SC70 (C)


PKG. DIMENSIONS(MM)		
SYMBOL	Min	Max
A	-	1.10
A1	0.00	0.10
A2	0.70	1.00
b	0.15	0.40
c	0.08	0.22
D	1.80	2.20
E	1.10	1.40
E1	1.80	2.40
e	0.65 BSC	
L	0.26	0.46
θ	0°	8°

Notes:

1. Comply with MO-203C/AB, except b Max, D Min and D Max.
2. PACKAGE OUTLINE DIMENSIONS DO NOT INCLUDE MOLD FLASH AND METAL BURR



DATE: 03/29/16

DESCRIPTION: 6-Pin, SOT363 (SC70)
PACKAGE CODE: C (C6)
DOCUMENT CONTROL#: PD-1902
REVISION: B

16-0078

For latest package info.

 please check: <http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/>
Ordering Information

Ordering Code	Packaging Code	Package Description	Top Mark
PI5A3157CEX	C	6-pin, SOT363 (SC70)	ZM

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. E = Pb-free and Green
5. X suffix = Tape/Reel

IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.

Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

A. Life support devices or systems are devices or systems which:

1. are intended to implant into the body, or
2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.

B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2016, Diodes Incorporated
www.diodes.com

Данный компонент на территории Российской Федерации

Вы можете приобрести в компании MosChip.

Для оперативного оформления запроса Вам необходимо перейти по данной ссылке:

<http://moschip.ru/get-element>

Вы можете разместить у нас заказ для любого Вашего проекта, будь то серийное производство или разработка единичного прибора.

В нашем ассортименте представлены ведущие мировые производители активных и пассивных электронных компонентов.

Нашей специализацией является поставка электронной компонентной базы двойного назначения, продукции таких производителей как XILINX, Intel (ex.ALTERA), Vicor, Microchip, Texas Instruments, Analog Devices, Mini-Circuits, Amphenol, Glenair.

Сотрудничество с глобальными дистрибьюторами электронных компонентов, предоставляет возможность заказывать и получать с международных складов практически любой перечень компонентов в оптимальные для Вас сроки.

На всех этапах разработки и производства наши партнеры могут получить квалифицированную поддержку опытных инженеров.

Система менеджмента качества компании отвечает требованиям в соответствии с ГОСТ Р ИСО 9001, ГОСТ РВ 0015-002 и ЭС РД 009

Офис по работе с юридическими лицами:

105318, г.Москва, ул.Щербаковская д.3, офис 1107, 1118, ДЦ «Щербаковский»

Телефон: +7 495 668-12-70 (многоканальный)

Факс: +7 495 668-12-70 (доб.304)

E-mail: info@moschip.ru

Skype отдела продаж:

moschip.ru

moschip.ru_4

moschip.ru_6

moschip.ru_9