



**RF360
Europe GmbH**

SAW components

SAW duplexer

Small cell & femtocell
LTE band 28a

Series/type: B8035

Ordering code: B39771B8035P810

Date: May 09, 2018

Version: 2.1

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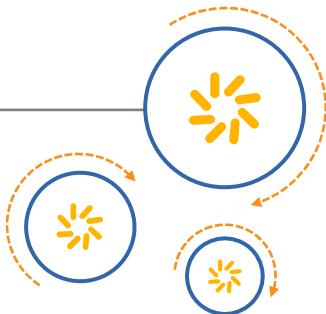
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A Qualcomm – TDK Joint Venture



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SAW components**B8035****SAW duplexer****718 / 773 MHz**

Data sheet

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SAW components**B8035****SAW duplexer****718 / 773 MHz**

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1 Application

- Low-loss SAW duplexer for 3G/LTE small cell & femtocell systems (Band 28a)
- Usable pass band: 30 MHz
- High power durability in downlink
- Rx = uplink = 703-733 MHz
- Tx = downlink = 758-788 MHz

2 Features

- Industrial grade qualified family
- Package size 2.5 ± 0.1 mm \times 2.0 ± 0.1 mm
- Package height 0.5 mm (max.)
- Approximate weight 0.01 g
- RoHS compatible
- Package for Surface Mount Technology (SMT)
- Ni/Au-plated terminals
- Electrostatic Sensitive Device (ESD)
- Moisture Sensitivity Level 2a (MSL2a)

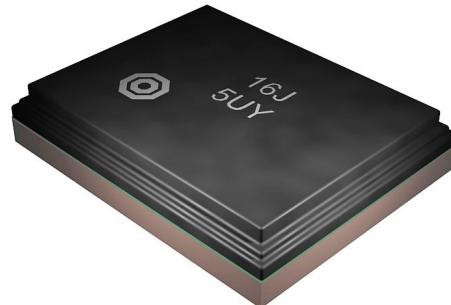


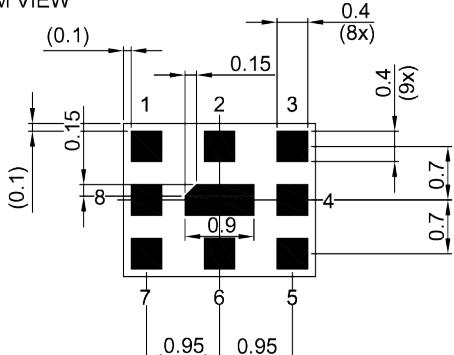
Figure 1: Picture of component with example of product marking.

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3 Package

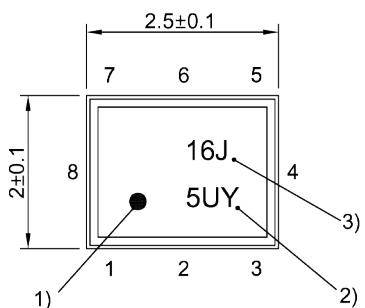
BOTTOM VIEW

Pad and pitch tolerance ± 0.05

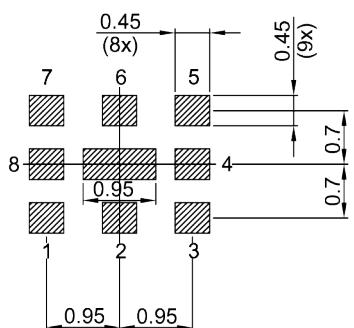
SIDE VIEW



TOP VIEW



- 1) Marking for pad number 1
- 2) Example of encoded lot number
- 3) Example of encoded filter type number

Land pattern
THRU VIEW

Landing pad tolerance -0.02

Figure 2: Drawing of package with package height A = 0.5 mm (max.). See Sec. Package information (p. 29).

4 Pin configuration

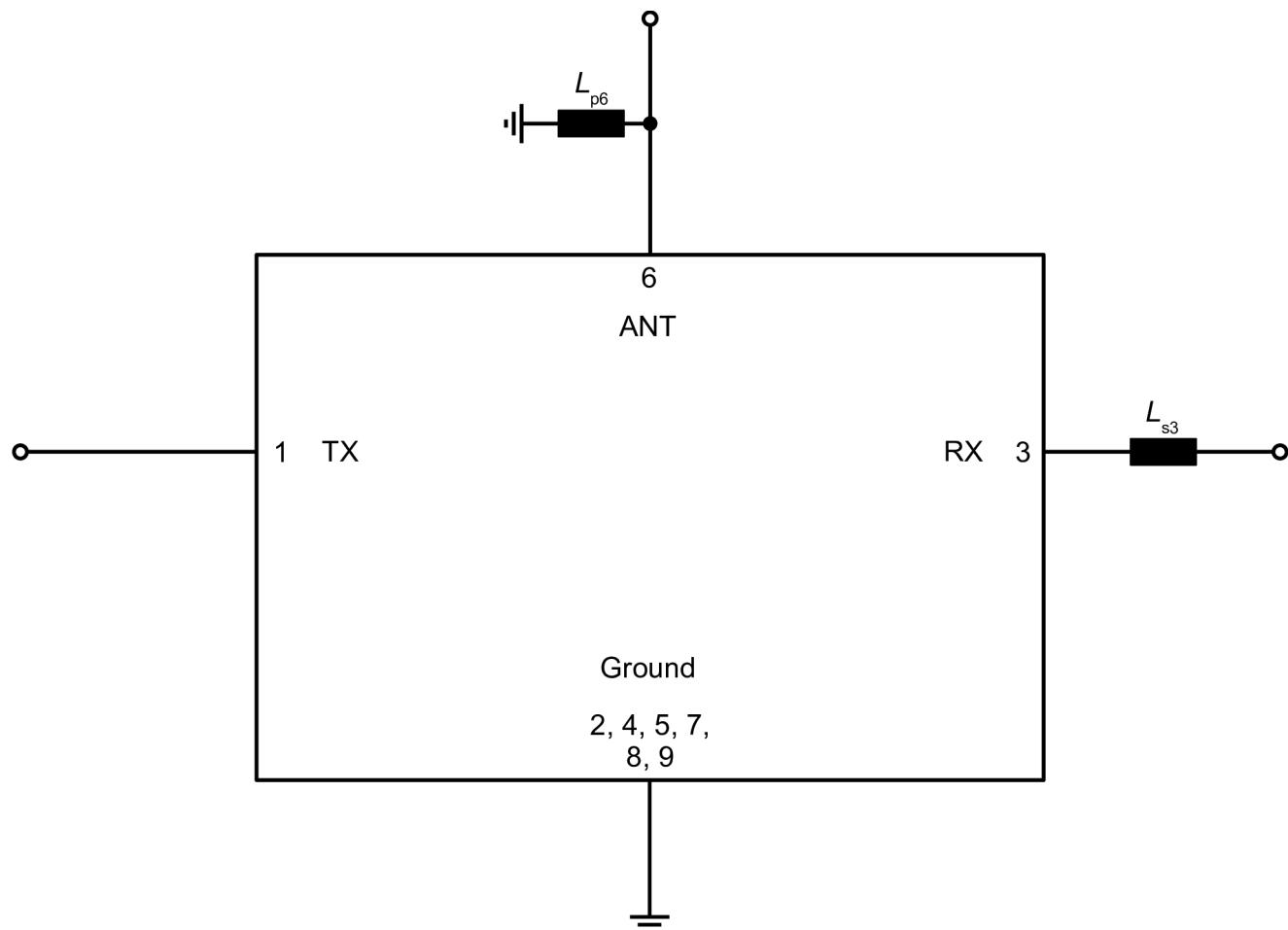
- 1 TX
- 3 RX
- 6 ANT
- 2, 4, 5, 7, 8, 9 Ground

Data sheet

5 Matching circuit

$$\blacksquare L_{p6} = 8.4 \text{ nH}$$

$$\blacksquare L_{s3} = 7.7 \text{ nH}$$

**Figure 3:** Schematic of matching circuit.

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6 Characteristics**6.1 TX – ANT**

Temperature range for specification

 T_{SPEC} = $-10^{\circ}\text{C} \dots +85^{\circ}\text{C}$

TX terminating impedance

 Z_{TX} = 50Ω

ANT terminating impedance

 Z_{ANT} = 50Ω with par. $8.4 \text{ nH}^1)$

RX terminating impedance

 Z_{RX} = 50Ω with ser. $7.7 \text{ nH}^1)$

Characteristics TX – ANT			min. for T_{SPEC}	typ. @ $+25^{\circ}\text{C}$	max. for T_{SPEC}	
Center frequency		f_c	—	773	—	MHz
Average insertion attenuation		$\alpha_{\text{INT,avg}}^2)$				
758 ... 763	MHz		—	1.6	2.5	dB
763 ... 783	MHz		—	1.5	2.1	dB
783 ... 788	MHz		—	1.7	2.5	dB
Maximum insertion attenuation		α_{max}				
758 ... 788	MHz		—	2.0	3.2	dB
Amplitude ripple (p-p)		Δa				
758 ... 788	MHz		—	1.0	2.1	dB
Maximum VSWR		VSWR_{max}				
@ TX port	758 ... 788	MHz	—	1.8	2.2	
@ ANT port	758 ... 788	MHz	—	1.9	2.2	
Maximum error vector magnitude		$\text{EVM}_{\text{max}}^3)$				
760.4 ... 785.6	MHz		—	2.0	4.0	%
Average attenuation		$\alpha_{\text{INT,avg}}^2)$				
703 ... 733	MHz		46	50	—	dB
Minimum attenuation		α_{min}				
50 ... 699	MHz		30	38	—	dB
703 ... 733	MHz		45	48	—	dB
733 ... 748	MHz		23	27	—	dB
803 ... 814	MHz		30	48	—	dB
880 ... 915	MHz		36	42	—	dB
925 ... 960	MHz		36	42	—	dB
1710 ... 1785	MHz		34	36	—	dB
1805 ... 1880	MHz		33	36	—	dB
1920 ... 1980	MHz		33	36	—	dB
2110 ... 2170	MHz		27	34	—	dB
2400 ... 2500	MHz		27	35	—	dB
2500 ... 2570	MHz		24	35	—	dB
2620 ... 2690	MHz		24	31	—	dB
3000 ... 5150	MHz		10	12	—	dB

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5150... 5850 MHz 8 10 — dB

1) See Sec. Matching circuit (p. 6).

2) Integrated attenuation α_{INT} : Averaged power $|S_{ij}|^2$ over the center 4.5 MHz of LTE 5 MHz (25 RB) channels.

3) Error Vector Magnitude (EVM) based on definition in 3GPP TS 25.141.

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Temperature range for specification

 T_{SPEC} = -40 °C ... +95 °C

TX terminating impedance

 Z_{TX} = 50 Ω

ANT terminating impedance

 Z_{ANT} = 50 Ω with par. 8.4 nH¹⁾

RX terminating impedance

 Z_{RX} = 50 Ω with ser. 7.7 nH¹⁾

Characteristics TX – ANT			min. for T_{SPEC}	typ. @ +25 °C	max. for T_{SPEC}	
Center frequency		f_c	—	773	—	MHz
Average insertion attenuation		$\alpha_{\text{INT,avg}}^{2)}$				
	758... 763	MHz	—	1.6	2.6	dB
	763... 783	MHz	—	1.5	2.1	dB
	783... 788	MHz	—	1.7	2.6	dB
Maximum insertion attenuation		α_{max}				
	758... 788	MHz	—	2.0	3.4	dB
Amplitude ripple (p-p)		$\Delta\alpha$				
	758... 788	MHz	—	1.0	2.3	dB
Maximum VSWR		VSWR_{max}				
@ TX port	758... 788	MHz	—	1.8	2.2	
@ ANT port	758... 788	MHz	—	1.9	2.2	
Maximum error vector magnitude		$\text{EVM}_{\text{max}}^{3)}$				
	760.4... 785.6	MHz	—	2.0	5.0	%
Average attenuation		$\alpha_{\text{INT,avg}}^{2)}$				
	703... 733	MHz	46	50	—	dB
Minimum attenuation		α_{min}				
	50... 699	MHz	30	38	—	dB
	703... 733	MHz	45	48	—	dB
	733... 748	MHz	23	27	—	dB
	803... 814	MHz	30	48	—	dB
	880... 915	MHz	36	42	—	dB
	925... 960	MHz	36	42	—	dB
	1710... 1785	MHz	34	36	—	dB
	1805... 1880	MHz	33	36	—	dB
	1920... 1980	MHz	33	36	—	dB
	2110... 2170	MHz	27	34	—	dB
	2400... 2500	MHz	27	35	—	dB
	2500... 2570	MHz	24	35	—	dB
	2620... 2690	MHz	24	31	—	dB
	3000... 5150	MHz	10	12	—	dB
	5150... 5850	MHz	8	10	—	dB

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- 1) See Sec. Matching circuit (p. 6).
- 2) Integrated attenuation α_{INT} : Averaged power $|S_{ij}|^2$ over the center 4.5 MHz of LTE 5 MHz (25 RB) channels.
- 3) Error Vector Magnitude (EVM) based on definition in 3GPP TS 25.141.

Data sheet

6.2 ANT – RX

Temperature range for specification

 T_{SPEC} = -10 °C ... +85 °C

TX terminating impedance

 Z_{TX} = 50 Ω

ANT terminating impedance

 Z_{ANT} = 50 Ω with par. 8.4 nH¹⁾

RX terminating impedance

 Z_{RX} = 50 Ω with ser. 7.7 nH¹⁾

Characteristics ANT – RX			min. for T_{SPEC}	typ. @ +25 °C	max. for T_{SPEC}	
Center frequency		f_c	—	718	—	MHz
Average insertion attenuation		$\alpha_{\text{INT,avg}}$ ²⁾				
	703... 708	MHz	—	1.6	2.8	dB
	708... 728	MHz	—	1.8	2.4	dB
	728... 733	MHz	—	2.0	2.8	dB
Maximum insertion attenuation		α_{max}				
	703... 733	MHz	—	2.2	3.5	dB
Amplitude ripple (p-p)		$\Delta\alpha$				
	703... 733	MHz	—	1.2	2.3	dB
Maximum VSWR		VSWR_{max}				
@ ANT port	703... 733	MHz	—	1.6	2.2	
@ RX port	703... 733	MHz	—	1.5	2.3	
Maximum error vector magnitude		EVM_{max} ³⁾				
	705.4... 730.6	MHz	—	2.9	6.0	%
Average attenuation		$\alpha_{\text{INT,avg}}$ ²⁾				
	758... 788	MHz	51	55	—	dB
Minimum attenuation		α_{min}				
	50... 694	MHz	28	32	—	dB
	694... 695	MHz	22	35	—	dB
	758... 788	MHz	46	50	—	dB
	788... 803	MHz	30	58	—	dB
	791... 821	MHz	30	58	—	dB
	869... 894	MHz	30	62	—	dB
	925... 960	MHz	30	62	—	dB
	1805... 1880	MHz	30	64	—	dB
	1930... 1995	MHz	30	64	—	dB
	2110... 2170	MHz	30	62	—	dB
	2400... 2484	MHz	35	63	—	dB
	2620... 2690	MHz	30	63	—	dB
	5150... 5850	MHz	35	53	—	dB

¹⁾ See Sec. Matching circuit (p. 6).²⁾ Integrated attenuation α_{INT} : Averaged power $|\mathbf{S}_{ij}|^2$ over the center 4.5 MHz of LTE 5 MHz (25 RB) channels.

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³⁾ Error Vector Magnitude (EVM) based on definition in 3GPP TS 25.141.

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Temperature range for specification

 T_{SPEC} = -40 °C ... +95 °C

TX terminating impedance

 Z_{TX} = 50 Ω

ANT terminating impedance

 Z_{ANT} = 50 Ω with par. 8.4 nH¹⁾

RX terminating impedance

 Z_{RX} = 50 Ω with ser. 7.7 nH¹⁾

Characteristics ANT – RX			min. for T_{SPEC}	typ. @ +25 °C	max. for T_{SPEC}	
Center frequency		f_c	—	718	—	MHz
Average insertion attenuation		$\alpha_{\text{INT,avg}}^{2)}$				
	703 ... 708	MHz	—	1.6	3.6	dB
	708 ... 728	MHz	—	1.8	2.4	dB
	728 ... 733	MHz	—	2.0	3.6	dB
Maximum insertion attenuation		α_{max}				
	703 ... 733	MHz	—	2.2	4.5	dB
Amplitude ripple (p-p)		$\Delta\alpha$				
	703 ... 733	MHz	—	1.2	3.2	dB
Maximum VSWR		VSWR_{max}				
@ ANT port	703 ... 733	MHz	—	1.6	4.0	
@ RX port	703 ... 733	MHz	—	1.5	4.0	
Maximum error vector magnitude		$\text{EVM}_{\text{max}}^{3)}$				
	705.4 ... 730.6	MHz	—	2.9	8.0	%
Average attenuation		$\alpha_{\text{INT,avg}}^{2)}$				
	758 ... 788	MHz	51	55	—	dB
Minimum attenuation		α_{min}				
	50 ... 694	MHz	28	32	—	dB
	694 ... 695	MHz	22	35	—	dB
	758 ... 788	MHz	46	50	—	dB
	788 ... 803	MHz	30	58	—	dB
	791 ... 821	MHz	30	58	—	dB
	869 ... 894	MHz	30	62	—	dB
	925 ... 960	MHz	30	62	—	dB
	1805 ... 1880	MHz	30	64	—	dB
	1930 ... 1995	MHz	30	64	—	dB
	2110 ... 2170	MHz	30	62	—	dB
	2400 ... 2484	MHz	35	63	—	dB
	2620 ... 2690	MHz	30	63	—	dB
	5150 ... 5850	MHz	35	53	—	dB

¹⁾ See Sec. Matching circuit (p. 6).²⁾ Integrated attenuation α_{INT} : Averaged power $|\mathbf{S}_{\text{ij}}|^2$ over the center 4.5 MHz of LTE 5 MHz (25 RB) channels.³⁾ Error Vector Magnitude (EVM) based on definition in 3GPP TS 25.141.

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6.3 TX – RX

Temperature range for specification

 T_{SPEC} = -10 °C ... +85 °C

TX terminating impedance

 Z_{TX} = 50 Ω

ANT terminating impedance

 Z_{ANT} = 50 Ω with par. 8.4 nH¹⁾

RX terminating impedance

 Z_{RX} = 50 Ω with ser. 7.7 nH¹⁾

Characteristics TX – RX			min. for T_{SPEC}	typ. @ +25 °C	max. for T_{SPEC}	
Average isolation	$\alpha_{\text{INT,avg}}^{2)}$	703... 733 MHz	49	51	—	dB
		758... 788 MHz	51	53	—	dB
Minimum isolation	α_{min}	703... 733 MHz	48	51	—	dB
		758... 788 MHz	49	51	—	dB

¹⁾ See Sec. Matching circuit (p. 6).²⁾ Integrated attenuation α_{INT} : Averaged power $|S_{ij}|^2$ over the center 4.5 MHz of LTE 5 MHz (25 RB) channels.

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Temperature range for specification

T_{SPEC} = -40 °C ... +95 °C

TX terminating impedance

$$Z_{\text{TX}} = 50 \Omega$$

ANT terminating impedance

$Z_{\text{ANT}} = 50 \Omega$ with par. 8.4 nH^1)

RX terminating impedance

Z_{BY} = 50 Ω with ser. 7.7 nH¹⁾

Characteristics TX – RX			min. for T_{SPEC}	typ. @ +25 °C	max. for T_{SPEC}	
Average isolation		$\alpha_{\text{INT,avg}}^{2)}$				
	703... 733	MHz	49	51	—	dB
Minimum isolation	758... 788	MHz	51	53	—	dB
			α_{min}			
	703... 733	MHz	48	51	—	dB
	758... 788	MHz	49	51	—	dB

1) See Sec. Matching circuit (p. 6).

2) Integrated attenuation c_{INT} : Averaged power $|S_{ii}|^2$ over the center 4.5 MHz of LTE 5 MHz (25 RB) channels.

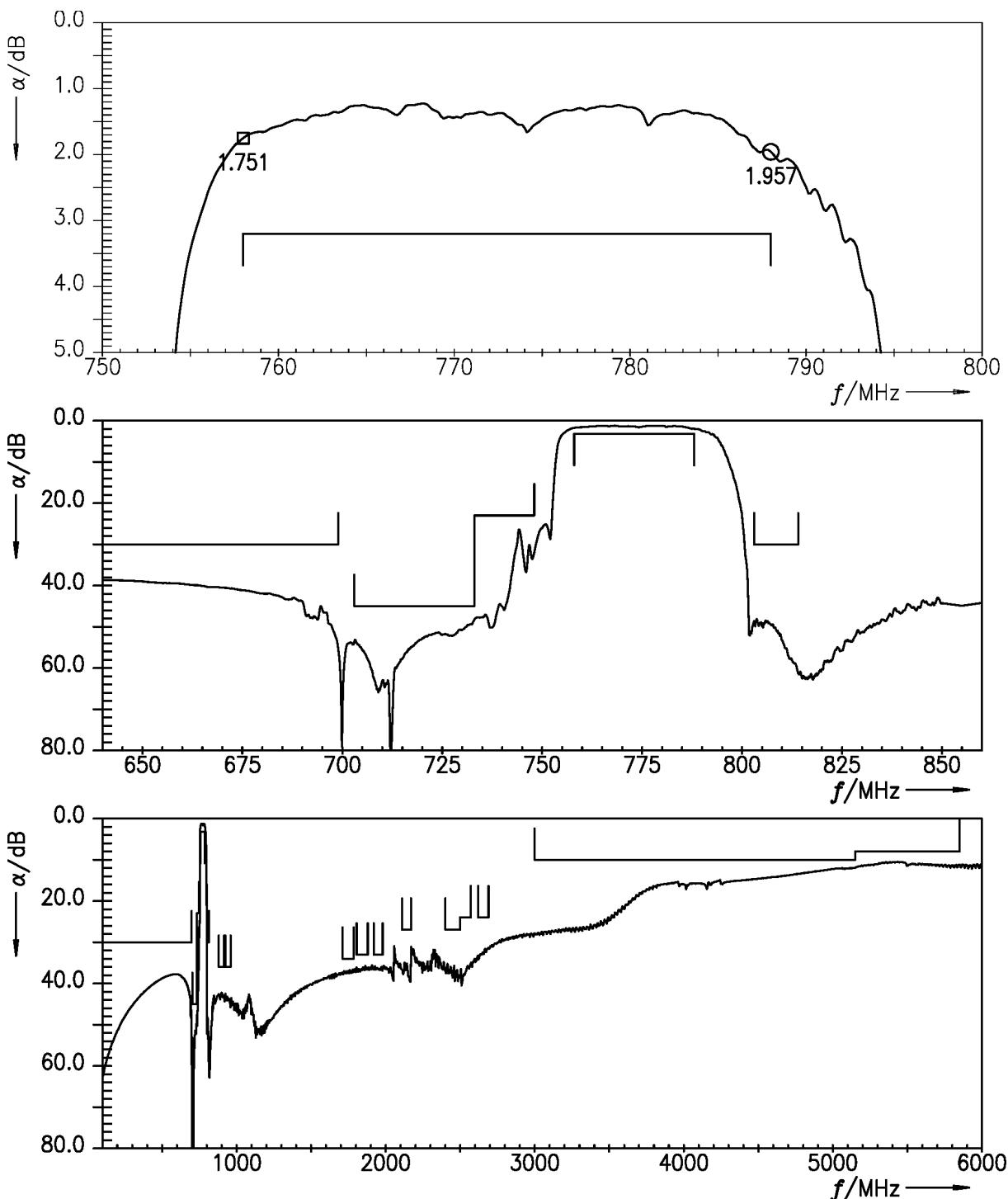
Data sheet

7 Maximum ratings

Operable temperature	$T_{OP} = -40 \text{ }^{\circ}\text{C} \dots +95 \text{ }^{\circ}\text{C}$	
Storage temperature	$T_{STG}^{1)} = -40 \text{ }^{\circ}\text{C} \dots +95 \text{ }^{\circ}\text{C}$	
DC voltage	$ V_{DC} ^{2)} = 0 \text{ V (max.)}$	
ESD voltage		
	$V_{ESD}^{3)} = 100 \text{ V (max.)}$	Machine model.
	$V_{ESD}^{4)} = 225 \text{ V (max.)}$	Human body model.
Input power	P_{IN}	
@ TX port: 758 ... 788 MHz	30 dBm ^{5), 6)}	5 MHz LTE downlink signal (25 RB) for 100000 h @ 55 °C. P_{IN} average – 41 dBm peak. Source and load impedance 50Ω.
@ RX port: 703 ... 733 MHz	27 dBm ⁵⁾	5 MHz LTE uplink signal (25 RB) for 5000 h @ 55 °C. P_{IN} average – 38 dBm peak. Source and load impedance 50Ω.
Operating lifetime with output power at antenna 758 ... 788 MHz	$P_{OUT}^{7)} = 24 \text{ dBm}$	Continuous wave for 100000 h @ 55 °C. Source and load impedance 50Ω.

¹⁾ Not valid for packaging material. Storage temperature for packaging material is –25 °C to +40 °C.²⁾ In case of applied DC voltage blocking capacitors are mandatory.³⁾ According to JESD22-A115B (MM – Machine Model), 10 negative & 10 positive pulses.⁴⁾ According to JESD22-A114F (HBM – Human Body Model), 1 negative & 1 positive pulse.⁵⁾ Expected lifetime according to accelerated power durability test and wear out models.⁶⁾ T_{SPEC} is the ambient temperature of the PCB at component position. Specified min./max values from section 6 "characteristics" for maximum input power 30dBm are valid for temperature up to 56.5°C.⁷⁾ According to accelerated high temperature operating life (HTOL) test.

Data sheet

8 Transmission coefficients**8.1 TX – ANT****Figure 4:** Attenuation TX – ANT.

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8.2 ANT – RX

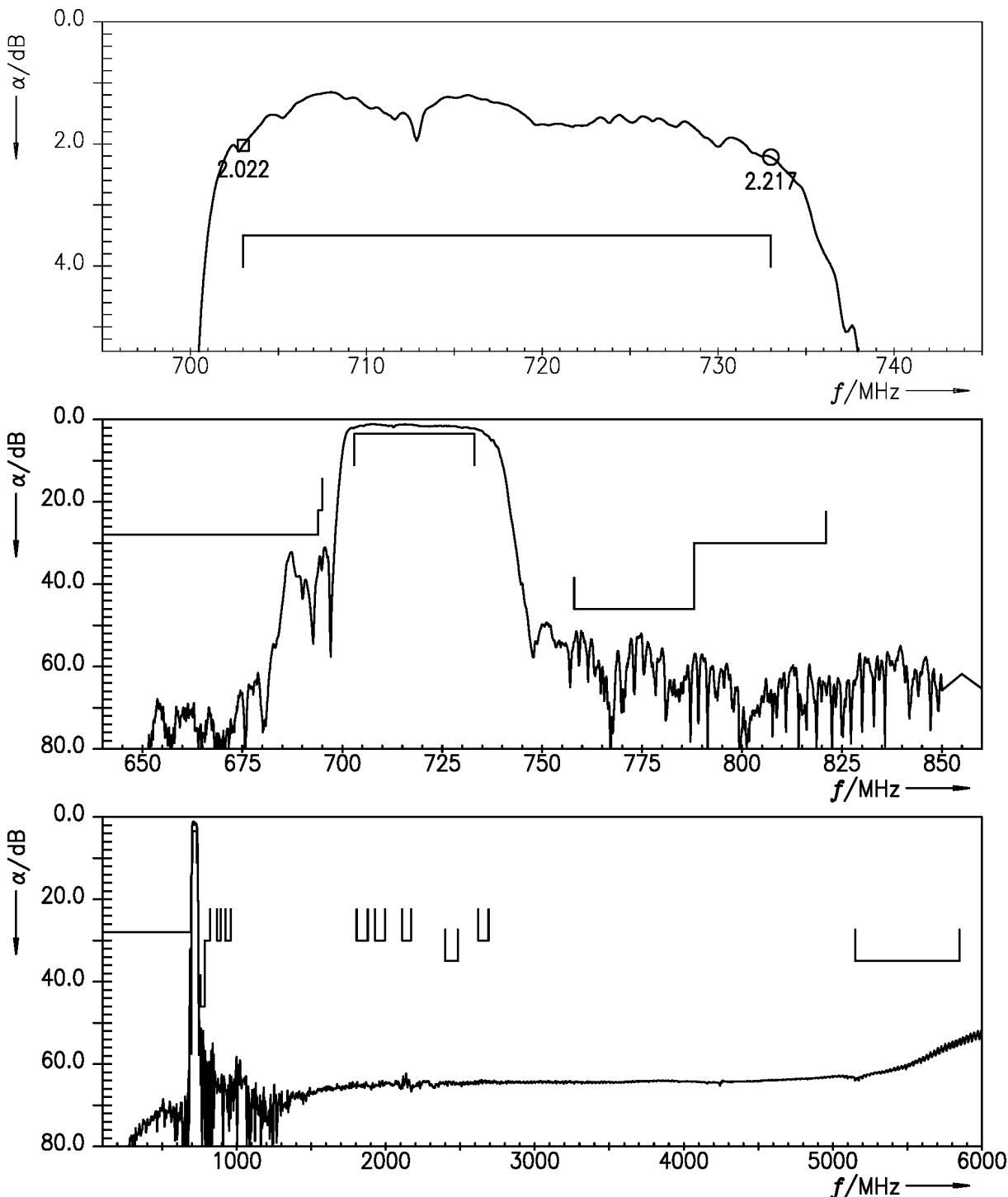


Figure 5: Attenuation ANT – RX.

Data sheet

8.3 TX – RX

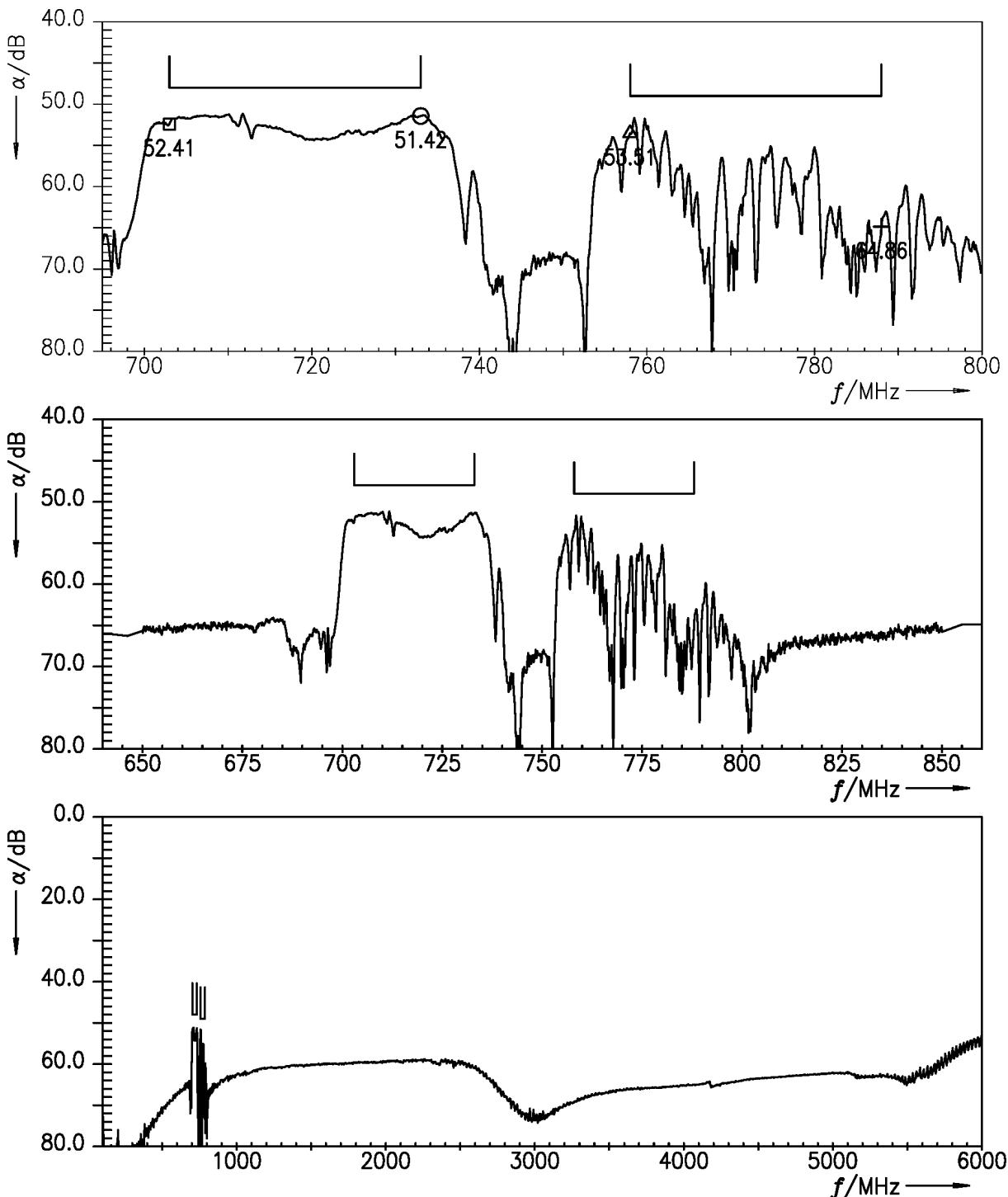
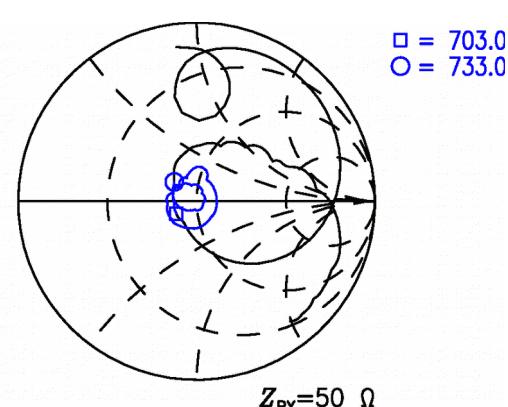
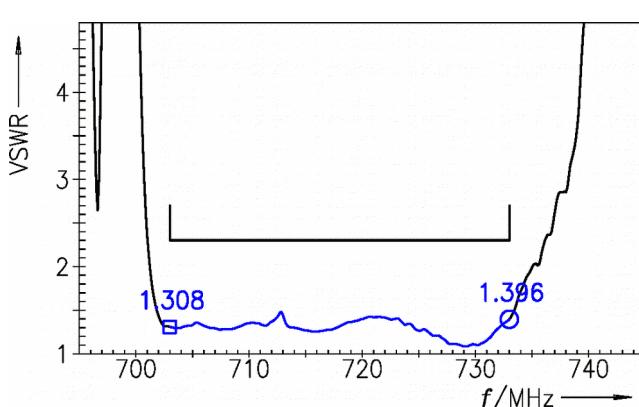
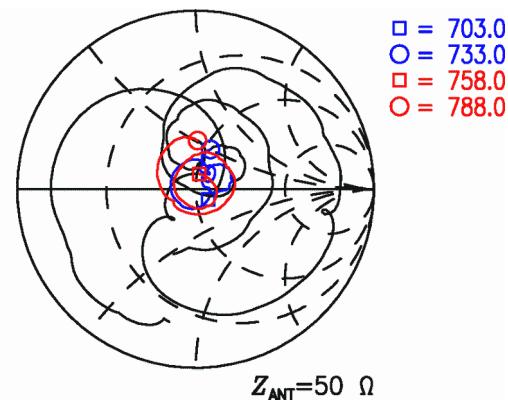
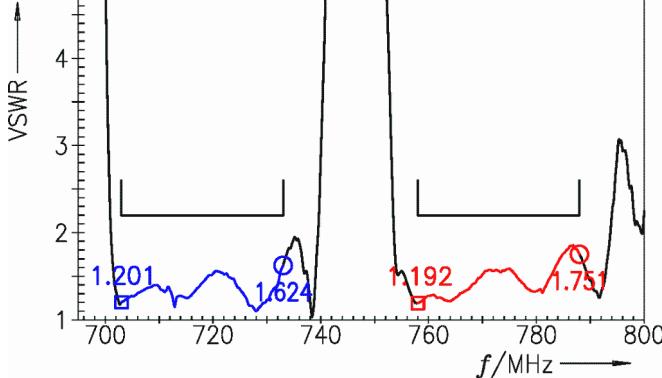
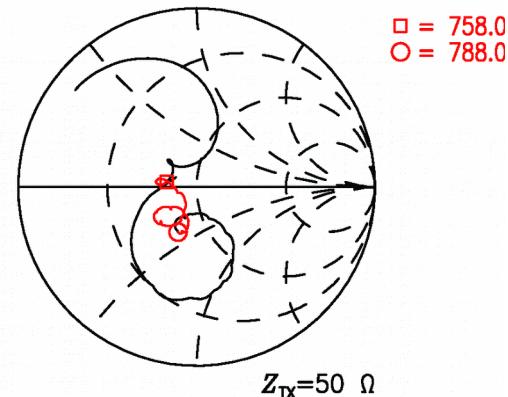
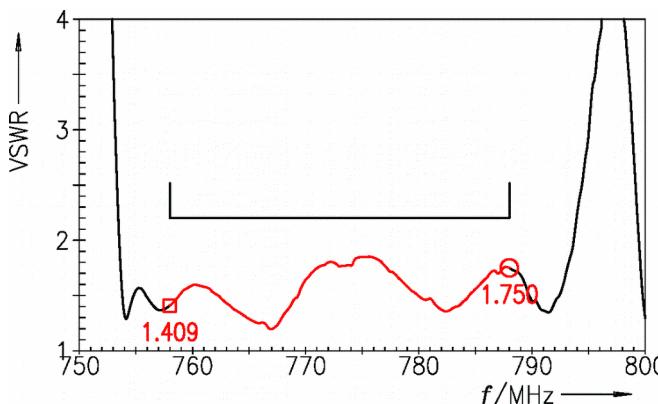


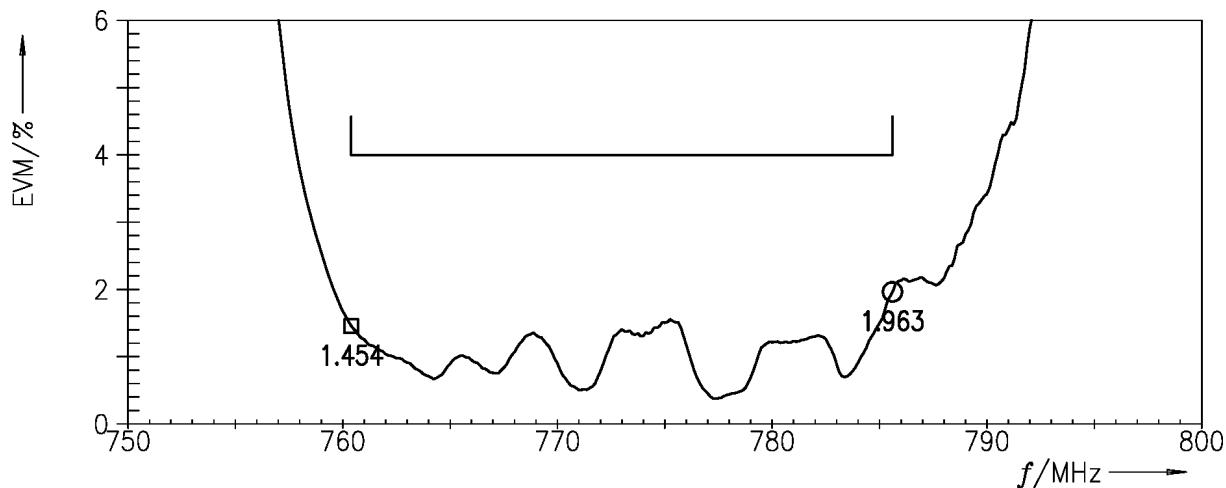
Figure 6: Isolation TX – RX.

Data sheet

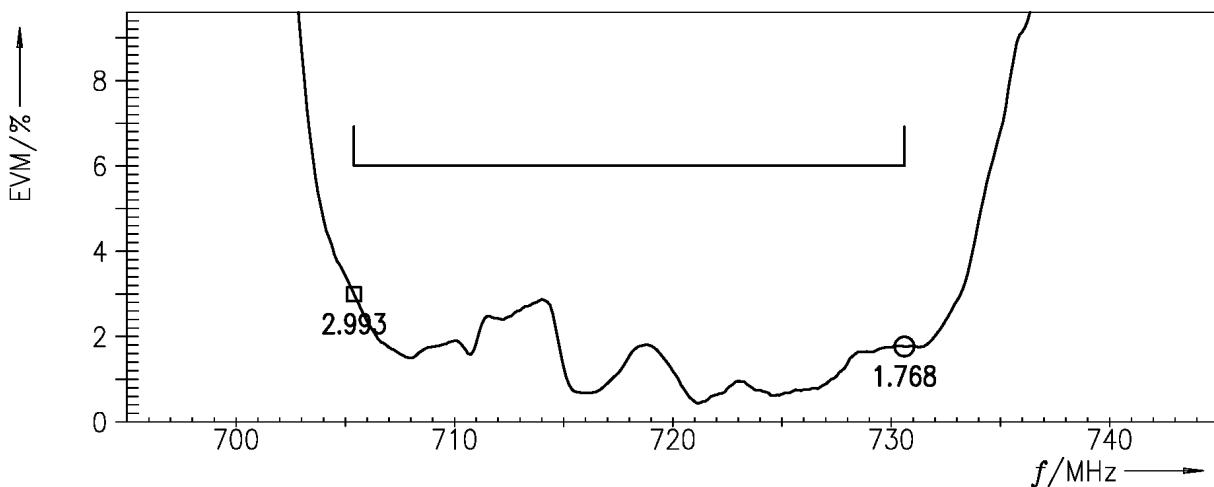
9 Reflection coefficients



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10 EVMs**10.1 TX – ANT****Figure 10:** Error vector magnitude TX – ANT.

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10.2 ANT – RX**Figure 11:** Error vector magnitude ANT – RX.

Data sheet

11 Packing material

11.1 Tape

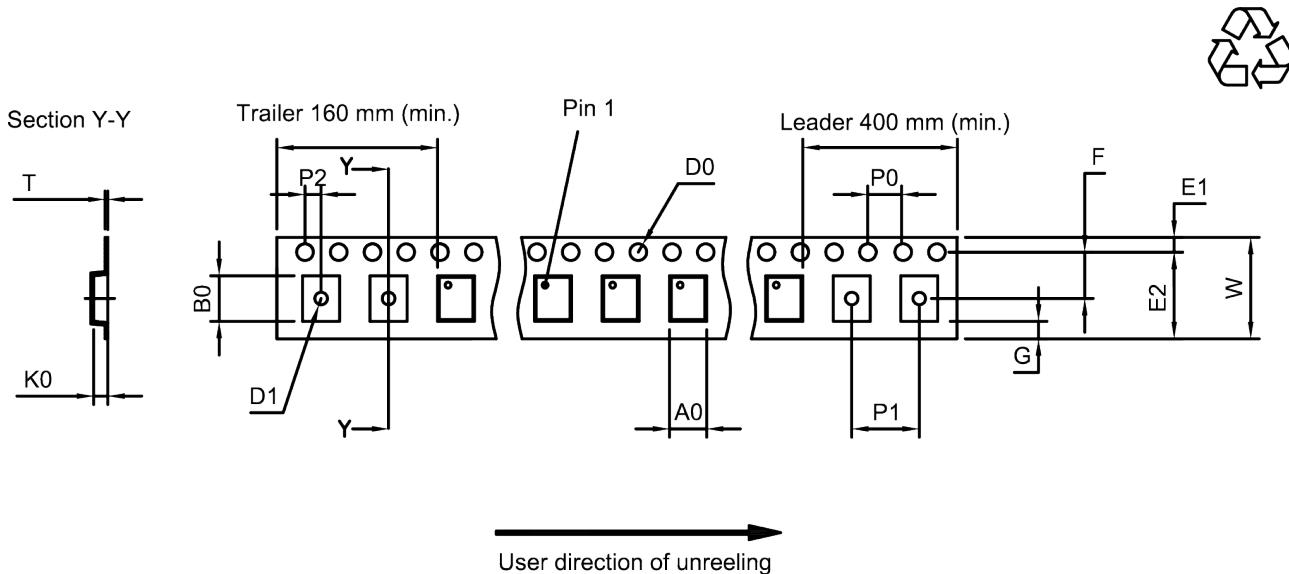


Figure 12: Drawing of tape (first-angle projection) with tape dimensions according to Table 1.

A ₀	2.25 _{±0.05} mm
B ₀	2.75 _{±0.05} mm
D ₀	1.5 _{+0.1/-0} mm
D ₁	1.0 mm (min.)
E ₁	1.75 _{±0.1} mm

E ₂	6.25 mm (min.)
F	3.5 _{±0.05} mm
G	0.75 mm (min.)
K ₀	0.6 _{±0.05} mm
P ₀	4.0 _{±0.1} mm

P ₁	4.0 _{±0.1} mm
P ₂	2.0 _{±0.05} mm
T	0.25 _{±0.03} mm
W	8.0 _{+0.3/-0.1} mm

Table 1: Tape dimensions.

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11.2 Reel with diameter of 180 mm

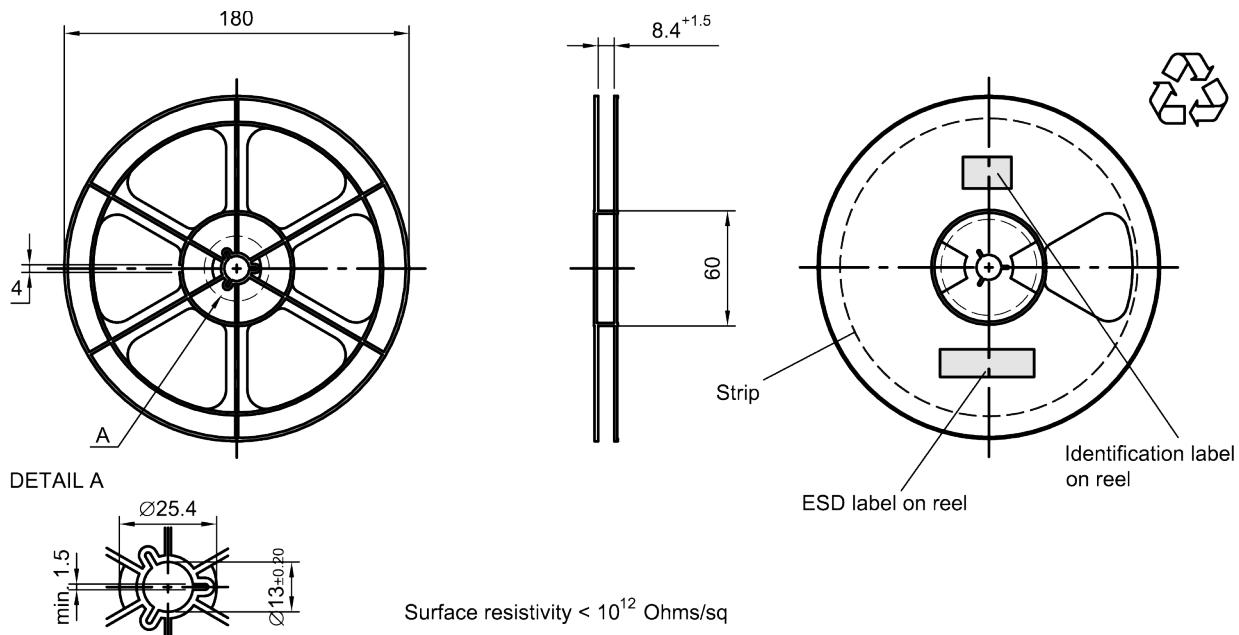


Figure 13: Drawing of reel (first-angle projection) with diameter of 180 mm.

Dimensions [mm]

X = 220±5

Y = 235±5

Sealing area 10±3

Printing on vacuumbag

Sealing area

Drypack in vacuumbag

Vacumbag

Identification label on vacuumbag

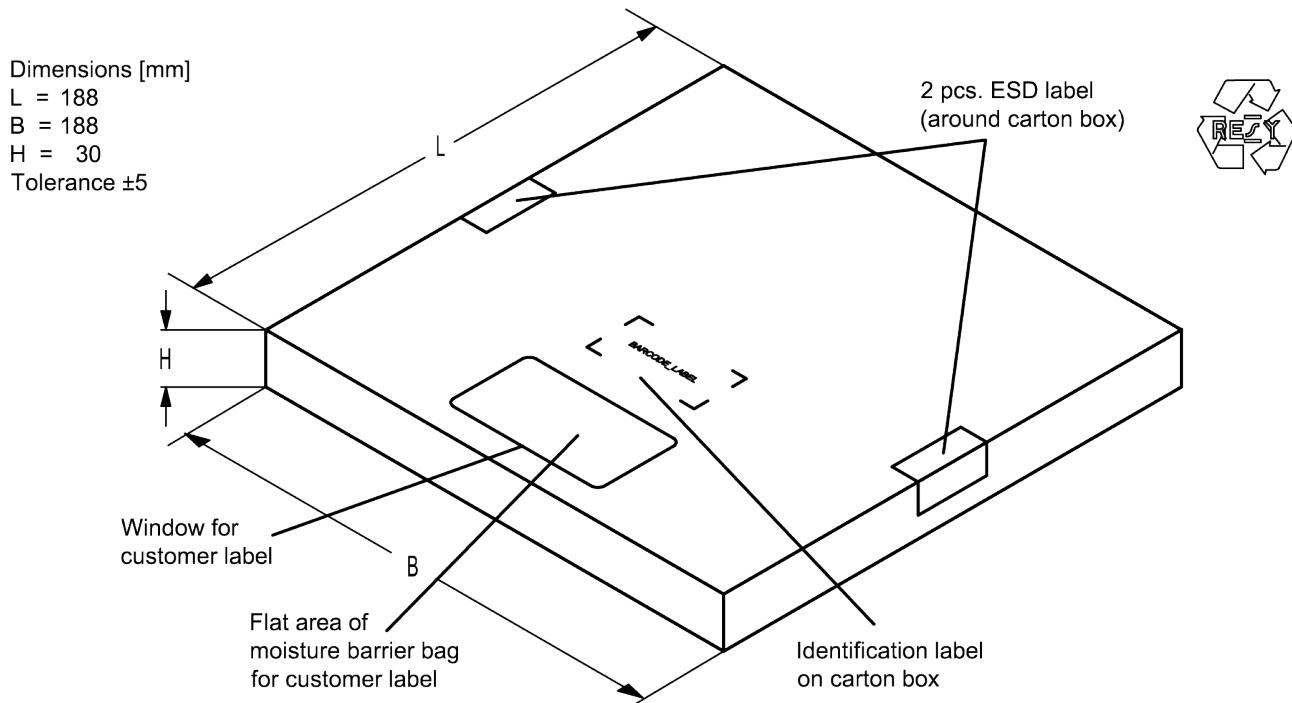
Humidity indicator in vacuumbag



Figure 14: Drawing of moisture barrier bag (MBB) for reel with diameter of 180 mm.

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**Figure 15:** Drawing of folding box for reel with diameter of 180 mm.

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12 Marking

Products are marked with product type number and lot number encoded according to Table 2:

■ Type number:

The 4 digit type number of the ordering code, is encoded by a special BASE32 code into a 3 digit marking.

e.g., B3xxxxB1234xxxx,

Example of decoding type number marking on device in decimal code.

$$\begin{array}{lll}
 \mathbf{16J} & \Rightarrow & \mathbf{1234} \\
 1 \times 32^2 + 6 \times 32^1 + 18 \ (\mathbf{=J}) \times 32^0 & = & \mathbf{1234}
 \end{array}$$

The BASE32 code for product type B8035 is 7V3.

■ Lot number:

The last 5 digits of the lot number, are encoded based on a special BASE47 code into a 3 digit marking.

e.g., **12345**,

Example of decoding lot number marking on device in decimal code.

$$\begin{array}{lll}
 \mathbf{5UY} & \Rightarrow & \mathbf{12345} \\
 5 \times 47^2 + 27 \ (\mathbf{=U}) \times 47^1 + 31 \ (\mathbf{=Y}) \times 47^0 & = & \mathbf{12345}
 \end{array}$$

Adopted BASE32 code for type number			
Decimal value	Base32 code	Decimal value	Base32 code
0	0	16	G
1	1	17	H
2	2	18	J
3	3	19	K
4	4	20	M
5	5	21	N
6	6	22	P
7	7	23	Q
8	8	24	R
9	9	25	S
10	A	26	T
11	B	27	V
12	C	28	W
13	D	29	X
14	E	30	Y
15	F	31	Z

Adopted BASE47 code for lot number			
Decimal value	Base47 code	Decimal value	Base47 code
0	0	24	R
1	1	25	S
2	2	26	T
3	3	27	U
4	4	28	V
5	5	29	W
6	6	30	X
7	7	31	Y
8	8	32	Z
9	9	33	b
10	A	34	d
11	B	35	f
12	C	36	h
13	D	37	n
14	E	38	r
15	F	39	t
16	G	40	v
17	H	41	\
18	J	42	?
19	K	43	{
20	L	44	}
21	M	45	<
22	N	46	>
23	P		

Table 2: Lists for encoding and decoding of marking.

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13 Soldering profile

The recommended soldering process is in accordance with IEC 60068-2-58 – 3rd edit and IPC/JEDEC J-STD-020B.

ramp rate	$\leq 3 \text{ K/s}$
preheat	125 °C to 220 °C, 150 s to 210 s, 0.4 K/s to 1.0 K/s
$T > 220 \text{ }^{\circ}\text{C}$	30 s to 70 s
$T > 230 \text{ }^{\circ}\text{C}$	min. 10 s
$T > 245 \text{ }^{\circ}\text{C}$	max. 20 s
$T \geq 255 \text{ }^{\circ}\text{C}$	–
peak temperature T_{peak}	250 °C +0/-5 °C
wetting temperature T_{min}	230 °C +5/-0 °C for 10 s ± 1 s
cooling rate	$\leq 3 \text{ K/s}$
soldering temperature T	measured at solder pads

Table 3: Characteristics of recommended soldering profile for lead-free solder (Sn95.5Ag3.8Cu0.7).

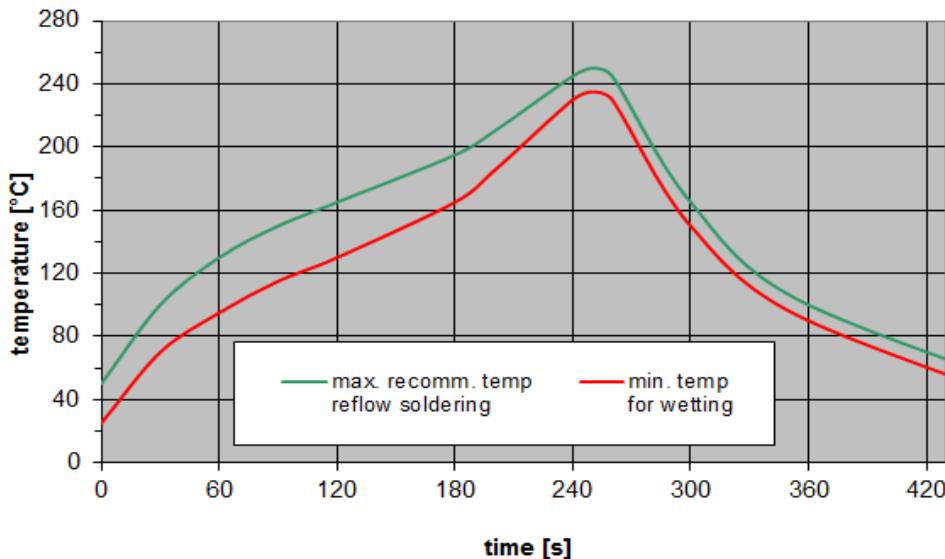


Figure 16: Recommended reflow profile for convection and infrared soldering – lead-free solder.

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14 Annotations

14.1 Matching coils

See TDK inductor pdf-catalog <http://www.tdk.co.jp/tefe02/coil.htm#aname1> and Data Library for circuit simulation <http://www.tdk.co.jp/etvcl/index.htm>.

14.2 RoHS compatibility

ROHS-compatible means that products are compatible with the requirements according to Art. 4 (substance restrictions) of Directive 2011/65/EU of the European Parliament and of the Council of June 8th, 2011, on the restriction of the use of certain hazardous substances in electrical and electronic equipment ("Directive") with due regard to the application of exemptions as per Annex III of the Directive in certain cases.

14.3 Scattering parameters (S-parameters)

The pin/port assignment is available in the headers of the S-parameter files. Please contact your local RF360 sales office.

14.4 Ordering codes and packing units

Ordering code	Packing unit
B39771B8035P810	5000 pcs

Table 4: Ordering codes and packing units.

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15 Cautions and warnings

15.1 Display of ordering codes for RF360 products

The ordering code for one and the same product can be represented differently in data sheets, data books, other publications and the website of RF360, or in order-related documents such as shipping notes, order confirmations and product labels. The varying representations of the ordering codes are due to different processes employed and do not affect the specifications of the respective products. Detailed information can be found on the Internet under www.rf360jv.com/orderingcodes.

15.2 Material information

Due to technical requirements components may contain dangerous substances. For information on the type in question please also contact one of our sales offices.

For information on recycling of tapes and reels please contact one of our sales offices.

15.3 Moldability

Before using in overmolding environment, please contact your local RF360 sales office.

15.4 Package information

Landing area

The printed circuit board (PCB) land pattern (landing area) shown is based on RF360 internal development and empirical data and illustrated for example purposes, only. As customers' SMD assembly processes may have a plenty of variants and influence factors which are not under control or knowledge of RF360, additional careful process development on customer side is necessary and strongly recommended in order to achieve best soldering results tailored to the particular customer needs.

Dimensions

Unless otherwise specified all dimensions are understood using unit millimeter (mm).

Dimensions do not include burrs.

Projection method

Unless otherwise specified first-angle projection is applied.

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