

Vishay Siliconix

Automotive Dual N-Channel 20 V (D-S) 175 °C MOSFET

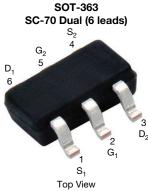
PRODUCT SUMMARY				
V _{DS} (V)	20			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 \text{ V}$	0.280			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 2.5 \text{ V}$	0.360			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 1.8 \text{ V}$	0.450			
I _D (A)	0.8			
Configuration	Dual			

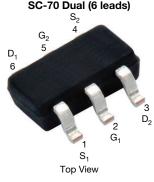
FEATURES

- TrenchFET® power MOSFET
- AEC-Q101 qualified
- 100 % R_q tested
- Typical ESD protection: 800 V
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912









Marking Code: 8R

G_1 G_2 G_2	3 kΩ D ₂ S ₂
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ORDERING INFORMATION	
Package	SC-70
Lead (Pb)-free and Halogen-free	SQ1912AEEH-T1-GE3

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)						
PARAMETER		SYMBOL	LIMIT	UNIT		
Drain-Source Voltage		V _{DS}	20	V		
Gate-Source Voltage		V _{GS}	± 12	V		
Continuous Drain Current ^a	T _C = 25 °C	I _D	0.8	Δ.		
Continuous Drain Current "	T _C = 125 °C		0.8			
Continuous Source Current (Diode Conduction) a		I _S	0.8	A		
Pulsed Drain Current ^b		I _{DM}	3			
Maximum Power Dissipation ^b	T _C = 25 °C	P _D	1.5	W		
waximum rower bissipation "	T _C = 125 °C	T _D	0.5	VV		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to +175	°C		

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	LIMIT	UNIT		
Junction-to-Ambient	PCB Mount ^c	R _{thJA}	220	°C/W		
Junction-to-Foot (Drain)		R_{thJF}	100	C/VV		

Notes

- a. Package limited.
- b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.
- c. When mounted on 1" square PCB (FR4 material).



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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static	•	^			•		
Drain-Source Breakdown Voltage	V _{DS}	V _{GS}	= 0, I _D = 250 μA	20	-	-	
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	· V _{GS} , I _D = 250 μA	0.45	0.6	1.5	V
Gate-Source Leakage		V _{DS} =	$0 \text{ V}, \text{ V}_{GS} = \pm 4.5 \text{ V}$	-	-	± 1	μΑ
Gate-Source Leakage	I _{GSS}	V _{DS} =	$0 \text{ V}, \text{ V}_{GS} = \pm 12 \text{ V}$	-	-	± 10	mA
		$V_{GS} = 0 V$	V _{DS} = 20 V	1	-	1	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V$	$V_{DS} = 20 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$	1	-	50	μΑ
		$V_{GS} = 0 V$	V _{DS} = 20 V, T _J = 175 °C	-	-	150	
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 4.5 V	$V_{DS} \ge 5 V$	1.5	-	1	Α
		$V_{GS} = 4.5 \text{ V}$	I _D = 1.2 A	1	0.200	0.280	
		$V_{GS} = 4.5 \text{ V}$	I _D = 1.2 A, T _J = 125 °C	-	-	0.423	
Drain-Source On-State Resistance a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}$	$I_D = 1.2 \text{ A}, T_J = 175^{\circ}\text{C}$	-	-	0.510	Ω
		$V_{GS} = 2.5 \text{ V}$	I _D = 1 A	1	0.261	0.360	
		$V_{GS} = 1.8 \text{ V}$	$I_D = 0.2 A$	i	0.320	0.450	
Forward Transconductance b	9 _{fs}	V _{DS} = 10 V, I _D = 1.2 A		1	2.6	1	S
Dynamic ^b							
Input Capacitance	C _{iss}			-	27	-	
Output Capacitance	C _{oss}	$V_{GS} = 0 V$	$V_{DS} = 10 \text{ V}, f = 1 \text{ MHz}$	ı	19	ı	pF
Reverse Transfer Capacitance	C _{rss}			ı	7	1	
Total Gate Charge ^c	Q_{g}			-	1	1.25	
Gate-Source Charge ^c	Q_{gs}	$V_{GS} = 4.5 \text{ V}$	$V_{DS} = 10 \text{ V}, I_{D} = 1.2 \text{ A}$	1	0.14	1	nC
Gate-Drain Charge ^c	Q_{gd}			-	0.27	-	
Gate Resistance d	R_{g}	f = 1 MHz		1.5	3	4.5	kΩ
Turn-On Delay Time ^c	t _{d(on)}			ı	66	82	
Rise Time ^c	t _r	$V_{DD} = 10 \text{ V, } R_L = 20 \Omega$ $I_D \cong 0.5 \text{ A, } V_{GEN} = 4.5 \text{ V, } R_g = 1 \Omega$		1	108	135	ns
Turn-Off Delay Time ^c	t _{d(off)}			ı	715	893	113
Fall Time ^c	t _f			-	390	487	
Source-Drain Diode Ratings and Chara	acteristics ^b						
Pulsed Current a	I _{SM}			_	_	3	Α
T dioca Garront	·SIVI						

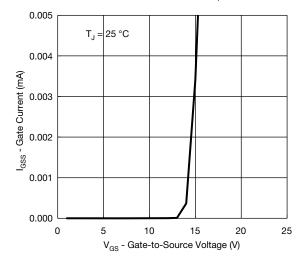
Notes

- a. Pulse test; pulse width $\leq 300 \,\mu\text{s}$, duty cycle $\leq 2 \,\%$.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.
- d. Gate is obscured by ESD network series resistance and cannot be tested directly.

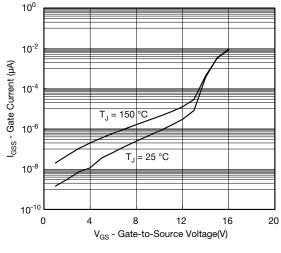
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



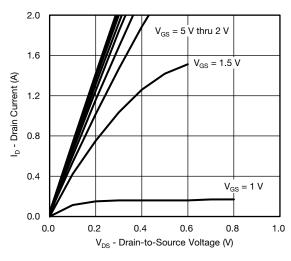
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



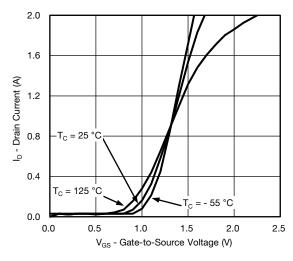
Gate Current vs. Gate-Source Voltage



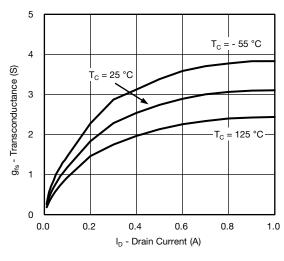
Gate Current vs. Gate-Source Voltage



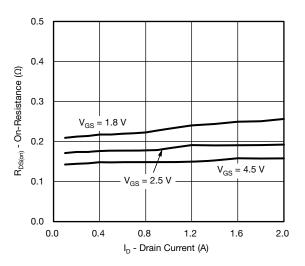
Output Characteristics



Transfer Characteristics



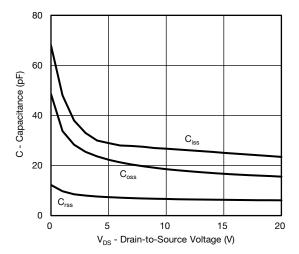




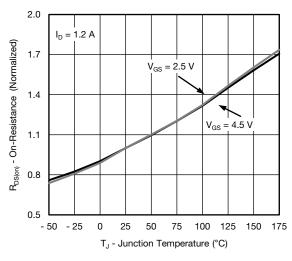
On-Resistance vs. Drain Current



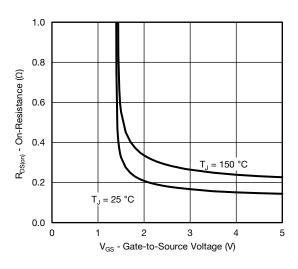
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



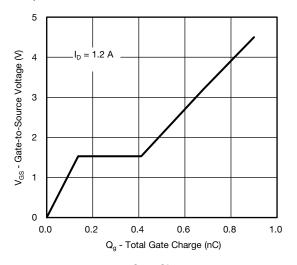
Capacitance



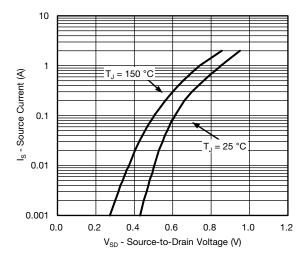
On-Resistance vs. Junction Temperature



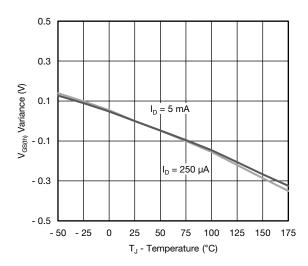
On-Resistance vs. Gate-to-Source Voltage



Gate Charge



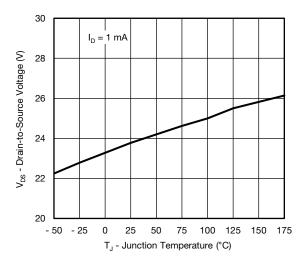
Source Drain Diode Forward Voltage



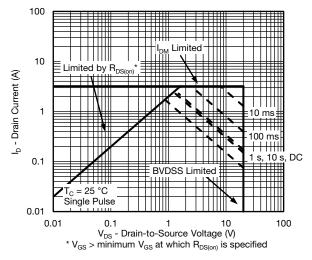
Threshold Voltage



TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



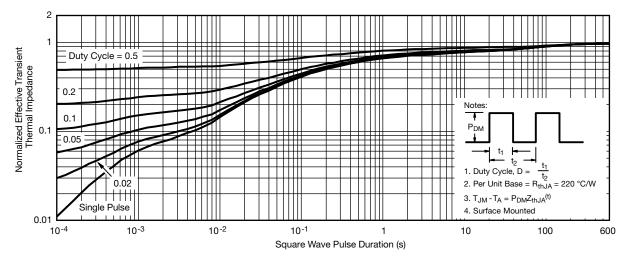
Drain Source Breakdown vs. Junction Temperature



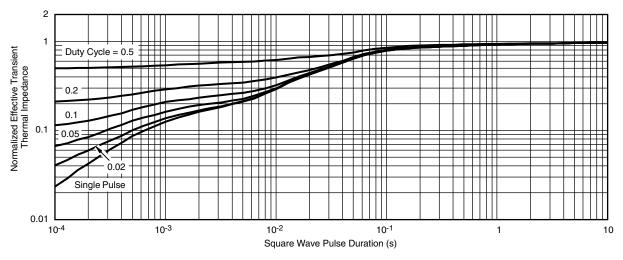
Safe Operating Area



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

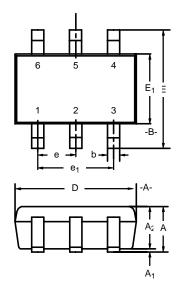
- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

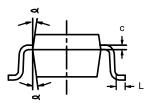
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SC-70: 6-LEADS





	MILLIMETERS			I	NCHE	S
Dim	Min	Nom	Max	Min	Nom	Max
Α	0.90	_	1.10	0.035	_	0.043
A ₁	-	_	0.10	-	_	0.004
A ₂	0.80	_	1.00	0.031	_	0.039
b	0.15	_	0.30	0.006	_	0.012
С	0.10	-	0.25	0.004	-	0.010
D	1.80	2.00	2.20	0.071	0.079	0.087
Ε	1.80	2.10	2.40	0.071	0.083	0.094
E ₁	1.15	1.25	1.35	0.045	0.049	0.053
е		0.65BSC			0.026BSC	;
e ₁	1.20	1.30	1.40	0.047	0.051	0.055
L	0.10	0.20	0.30	0.004	0.008	0.012
کے	7°Nom 7°Nom					

DWG: 5550



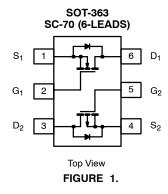
Dual-Channel LITTLE FOOT® 6-Pin SC-70 MOSFET Copper Leadframe Version Recommended Pad Pattern and Thermal Performance

INTRODUCTION

The new dual 6-pin SC-70 package with a copper leadframe enables improved on-resistance values and enhanced thermal performance as compared to the existing 3-pin and 6-pin packages with Alloy 42 leadframes. These devices are intended for small to medium load applications where a miniaturized package is required. Devices in this package come in a range of on-resistance values, in n-channel and p-channel versions. This technical note discusses pin-outs, package outlines, pad patterns, evaluation board layout, and thermal performance for the dual-channel version.

PIN-OUT

Figure 1 shows the pin-out description and Pin 1 identification for the dual-channel SC-70 device in the 6-pin configuration. Both n-and p-channel devices are available in this package — the drawing example below illustrates the p-channel device.



For package dimensions see outline drawing SC-70 (6-Leads) (http://www.vishay.com/doc?71154)

BASIC PAD PATTERNS

See Application Note 826, Recommended Minimum Pad Patterns With Outline Drawing Access for Vishay Siliconix MOSFETs, (http://www.vishay.com/doc?72286) for the SC-70 6-pin basic pad layout and dimensions. This pad pattern is sufficient for the low-power applications for which this package is intended. Increasing the drain pad pattern (Figure 2) yields a reduction in thermal resistance and is a preferred footprint.

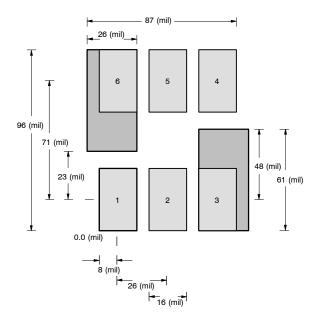


FIGURE 2. SC-70 (6 leads) Dual

EVALUATION BOARD FOR THE DUAL-CHANNEL SC70-6

The 6-pin SC-70 evaluation board (EVB) shown in Figure 3 measures 0.6 in. by 0.5 in. The copper pad traces are the same as described in the previous section, *Basic Pad Patterns*. The board allows for examination from the outer pins to the 6-pin DIP connections, permitting test sockets to be used in evaluation testing.

The thermal performance of the dual 6-pin SC-70 has been measured on the EVB, comparing both the copper and Alloy 42 leadframes. This test was then repeated using the 1-inch² PCB with dual-side copper coating.

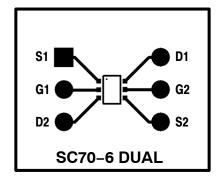
A helpful way of displaying the thermal performance of the 6-pin SC-70 dual copper leadframe is to compare it to the traditional Alloy 42 version.

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Front of Board SC70-6



Back of Board SC70-6

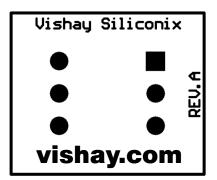


FIGURE 3.

THERMAL PERFORMANCE

Junction-to-Foot Thermal Resistance (the Package Performance)

Thermal performance for the dual SC-70 6-pin package is measured as junction-to-foot thermal resistance, in which the "foot" is the drain lead of the device as it connects with the body. The junction-to-foot thermal resistance for this device is typically 80°C/W, with a maximum thermal resistance of approximately 100°C/W. This data compares favorably with another compact, dual-channel package - the dual TSOP-6 which features a typical thermal resistance of 75°C/W and a maximum of 90°C/W.

Power Dissipation

The typical $R\theta_{JA}$ for the dual-channel 6-pin SC-70 with a copper leadframe is 224°C/W steady-state, compared to 413°C/W for the Alloy 42 version. All figures are based on the 1-inch² FR4 test board. The following example shows how the thermal resistance impacts power dissipation for the dual 6-pin SC-70 package at varying ambient temperatures.

Alloy 42 Leadframe

ALLOY 42 LEADFRAME				
Room Ambient 25 °C	Elevated Ambient 60 °C			
$P_D = \frac{T_{J(max)} - T_A}{R\theta_{JA}}$	$P_D = \frac{T_{J(max)} - T_A}{R\theta_{JA}}$			
$P_{D} = \frac{150^{\circ}C - 25^{\circ}C}{413^{\circ}C/W}$	$P_{D} = \frac{150^{\circ}C - 60^{\circ}C}{413^{\circ}C/W}$			
$P_D = 303 \text{ mW}$	$P_D = 218 \text{ mW}$			

COOPER LEADFRAME					
Room Ambient 25 °C	Elevated Ambient 60 °C				
$P_D = \frac{T_{J(max)} - T_A}{R\theta_{JA}}$	$P_D = \frac{T_{J(max)} - T_A}{R\theta_{JA}}$				
$P_{D} = \frac{150^{\circ}C - 25^{\circ}C}{224^{\circ}C/W}$	$P_{D} = \frac{150^{\circ}C - 60^{\circ}C}{224^{\circ}C/W}$				
$P_D = 558 \text{ mW}$	$P_D = 402 \text{mW}$				

Although they are intended for low-power applications, devices in the 6-pin SC-70 dual-channel configuration will handle power dissipation in excess of 0.5 W.

TESTING

To further aid the comparison of copper and Alloy 42 leadframes, Figures 4 and 5 illustrate the dual-channel 6-pin SC-70 thermal performance on two different board sizes and pad patterns. The measured steady-state values of $R\theta_{JA}$ for the dual 6-pin SC-70 with varying leadframes are as follows:

LITTLE FOOT 6-PIN SC-70						
Alloy 42 Copper						
1) Minimum recommended pad pattern on the EVB board (see Figure 3).	518°C/W	344°C/W				
2) Industry standard 1-inch ² PCB with maximum copper both sides.	413°C/W	224°C/W				

The results indicate that designers can reduce thermal resistance (θJA) by 34% simply by using the copper leadframe device as opposed to the Alloy 42 version. In this example, a 174°C/W reduction was achieved without an increase in board area. If an increase in board size is feasible, a further 120°C/W reduction can be obtained by utilizing a 1-inch². PCB area.

The Dual copper leadframe versions have the following suffix:

Dual: Si19xxEDH Si15xxEDH Compl.:

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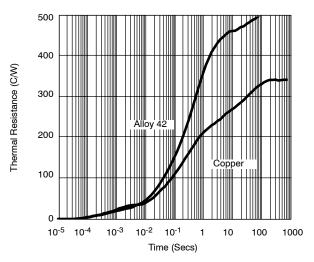


FIGURE 4. Dual SC70-6 Thermal Performance on EVB

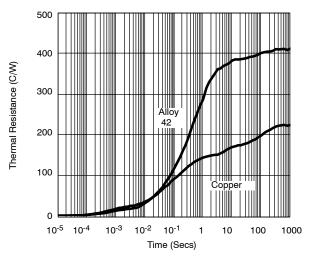
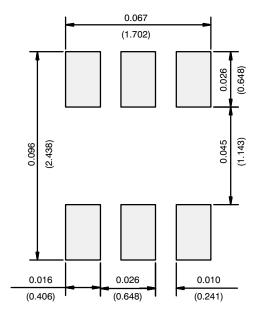


FIGURE 5. Dual SC70-6 Comparison on 1-inch² PCB



RECOMMENDED MINIMUM PADS FOR SC-70: 6-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

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Офис по работе с юридическими лицами:

105318, г. Москва, ул. Щербаковская д. 3, офис 1107, 1118, ДЦ «Щербаковский»

Телефон: +7 495 668-12-70 (многоканальный)

Факс: +7 495 668-12-70 (доб.304)

E-mail: info@moschip.ru

Skype отдела продаж:

moschip.ru moschip.ru_6 moschip.ru_4 moschip.ru_9