

CMOS Digital Integrated Circuits Silicon Monolithic

TC7SB3157DL6X

1. Functional Description

• Single 1-of-2 Multiplexer/Demultiplexer

2. General

The TC7SB3157DL6X is a high-speed CMOS single 1-of-2 multiplexer/demultiplexer. The low ON resistance of the switch allows connections to be made with minimal propagation delay time.

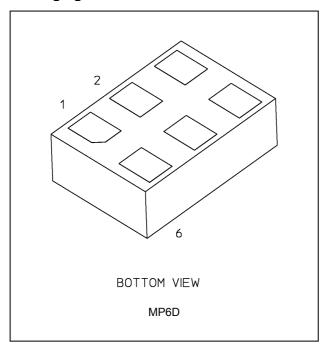
This device is 1 to 2 multiplexer/demultiplexer controlled by the select input (S). The A input is connected to B1 or B2 output based on the selection of Control input (S).

All inputs are equipped with protection circuits against static discharge.

3. Features

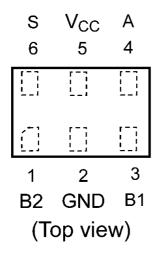
- (1) Operating voltage: $V_{CC} = 1.65 \text{ to } 5.5 \text{ V}$
- (2) ON capacitance: $C_{I/O} = 15 \text{ pF Switch On (typ.)} @V_{CC} = 5.0 \text{ V}$
- (3) ON resistance: $R_{ON} = 4 \Omega$ (typ.) @ $V_{CC} = 4.5 \text{ V}$, $V_{IS} = 0 \text{ V}$
- (4) ESD performance: Machine model $\geq \pm 200$ V, Human body model $\geq \pm 2000$ V
- (5) Package: MP6D

4. Packaging

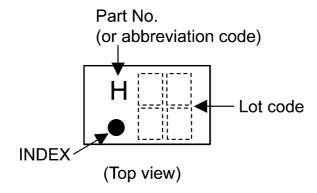




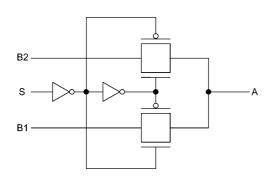
5. Pin Assignment



6. Marking



7. Block Diagram



Rev.2.0



8. Principle of Operation

8.1. Truth Table

Inputs S	Function
L	A port = B1 port
Н	A port = B2 port

9. Absolute Maximum Ratings (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V _{CC}		-0.5 to 6.5	V
Input voltage (S)	V _{IN}		-0.5 to 6.5	
Switch I/O voltage	Vs		-0.5 to V _{CC}	
Clamp diode current	I _{IK}		-50	mA
Switch I/O current	I _S		50	
Power dissipation	P _D	(Note 1)	250	mW
V _{CC} /ground current	I _{CC} /I _{GND}		±100	mA
Storage temperature	T _{stg}		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Mounted on an FR4 board

10. Operating Ranges (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V _{CC}		1.65 to 5.5	V
Input voltage(S)	V _{IN}		0 to 5.5	
Switch I/O voltage	Vs		0 to V _{CC}	
Operating temperature	T _{opr}		-40 to 85	°C
Input rise time	dt/dv		0 to 10	ns/V
Input fall time	dt/dv		0 to 10	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused control inputs must be tied to either V_{CC} or GND.



11. Electrical Characteristics

11.1. DC Characteristics (Unless otherwise specified, Ta = -40 to 85 °C)

Characteristics	Symbol	Note	Test Condition	V _{CC} (V)	Min	Тур.	Max	Unit
High-level input voltage (S)	V_{IH}		_	1.65 to 1.95	$0.8 \times V_{CC}$	_	_	V
				2.3 to 5.5	$0.7 \times V_{CC}$	_	_	
Low-level input voltage (S)	V_{IL}		_	1.65 to 1.95	_		$0.2 \times V_{CC}$	
				2.3 to 5.5	_		$0.3 \times V_{CC}$	
Input leakage current	I _{IN}		V _{IN} = 0 to 5.5 V	1.65 to 5.5		-	±1.0	μА
Switch OFF-state leakage current	I _{SZ}		B1, B2 = 0 to V _{CC}	1.65 to 5.5	_		±10	
ON-resistance	R _{ON}	(Note 1),	V _{IS} = 0 V, I _{IS} = 30 mA	4.5	_	4	7	Ω
		(Note 2)	V _{IS} = 2.4 V, I _{IS} = 30 mA	4.5	_	5	12	
			V _{IS} = 4.5 V, I _{IS} = 30 mA	4.5	_	6	10	
			V _{IS} = 0 V, I _{IS} = 24 mA	3.0	_	5	9	
			V _{IS} = 3.0 V, I _{IS} = 24 mA	3.0		7	14	
			$V_{IS} = 0 \text{ V}, I_{IS} = 8 \text{ mA}$	2.3		6	12	
			$V_{IS} = 2.3 \text{ V}, I_{IS} = 8 \text{ mA}$	2.3	_	9	18	
			$V_{IS} = 0 \text{ V}, I_{IS} = 4 \text{ mA}$	1.65		8	20	
			V _{IS} = 1.65 V, I _{IS} = 4 mA	1.65	_	15	30	
Quiescent supply current	Icc		$V_{IN} = V_{CC}$ or GND, $I_{OUT} = 0$ A	5.5			10	μА
	ΔI_{CC}		$V_{IN} = V_{CC} - 0.6 V$	5.5	_	_	50	μА

Note 1: All typical values are at $T_a = 25$ °C.

Note 2: Measured by the voltage drop between A and B pins at the indicated current through the switch. On-resistance is determined by the lower of the voltages on the two (A or B) pins.

11.2. AC Characteristics (Unless otherwise specified, T_a = -40 to 85 °C)

Characteristics	Symbol	Note	Test Condition	V _{CC} (V)	Min	Max	Unit													
3-state output enable time	t _{PZL} /t _{PZH}		See Fig. 11.2.1, 11.2.2,	5.0 ± 0.5	1	4	ns													
					Table 11.2.1	3.3 ± 0.3		6												
				2.5 ± 0.2		8														
																	1.8 ± 0.15		16	
3-state output disable time	t _{PLZ} /t _{PHZ}		See Fig. 11.2.1, 11.2.2,	5.0 ± 0.5		4.5														
			Ta		Table 11.2.1	3.3 ± 0.3		7												
						2.5 ± 0.2		9												
				1.8 ± 0.15	_	16														

11.3. Capacitive Characteristics (Note) (Unless otherwise specified, T_a = 25 °C)

Characteristics	Symbol	Note	Test Condition	V _{CC} (V)	Тур.	Unit
Input capacitance(S)	C _{IN}		V _{IN} = 0 V	5.0	4	pF
Switch terminal OFF-capacitance	C _{I/O}		B Port,V _{I/O} = 0 V	5.0	5	
Switch terminal ON-capacitance	C _{I/O}		A Port,V _{I/O} = 0 V	5.0	15	
			B Port,V _{I/O} = 0 V	5.0	15	

Note: Parameter guaranteed by design.

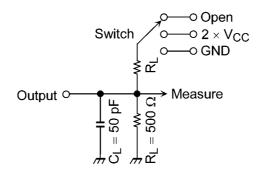


Fig. 11.2.1 AC Test Circuit

Table 11.2.1 Parameter for AC Test Circuit

Parameter	Switch
t_{PLZ} , t_{PZL}	2 × V _{CC}
t _{PHZ} , t _{PZH}	GND

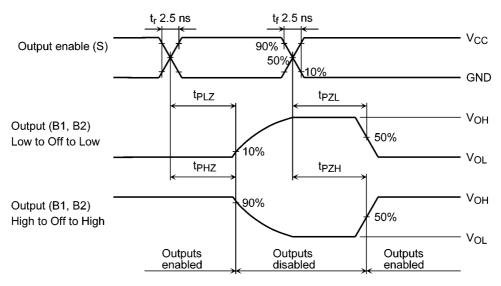


Fig. 11.2.2 AC Waveform t_{PLZ}, t_{PHZ}, t_{PZL}, t_{PZH}

12. Rise and Fall Time (t_r/t_f)

The $t_{r(out)}$ and $t_{f(out)}$ values of the output signals are affected by the CR time constant of the input, which consists of the switch terminal capacitance ($C_{I/O}$) and the on-resistance (R_{ON}) of the input.

In practice, the $t_{r(out)}$ and $t_{f(out)}$ values are also affected by the circuit's capacitance and resistance components other than the capacitance of TC7SB3157DL6X

The $t_r/t_{f(out)}$ values can be approximated as follows.

(Figure 12.1, Table 12.1 shows the test circuit.)

$$t_{r}/t_{f(out)} \; (approx) = -\left(C_{I/O} + C_{L}\right) \; \cdot \; \left(R_{DRIVE} + R_{ON}\right) \; \cdot \; ln \; \left(\left(\left(V_{OH} - V_{OL}\right) - V_{M}\right) / \left(V_{OH} - V_{OL}\right)\right) \; .$$

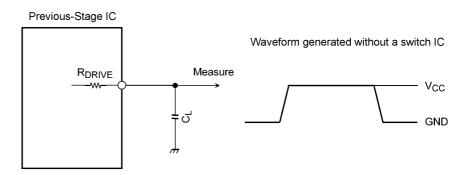
Where, $R_{\mbox{\scriptsize DRIVE}}$ is the output impedance of the previous-stage circuit.

Calculation example:

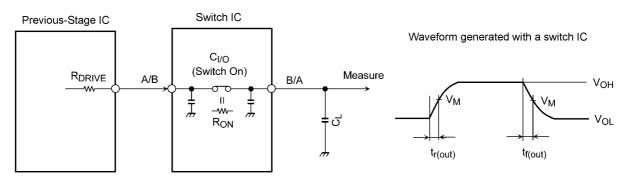
$$t_{r(out)}$$
 (approx) = \cdot (15 + 15) E \cdot 12 \cdot (120 + 4) \cdot ln (((4.5 \cdot 0) \cdot 2.25) / (4.5 \cdot 0)) = \approx 2.6 ns

Calculation conditions:

 V_{CC} = 4.5 V, C_L = 15 pF, R_{DRIVE} = 120 Ω (output impedance of the previous IC), V_M = 2.25 V (V_{CC} /2) Output of the previous IC = digital (i.e., high-level voltage = V_{CC} , low-level voltage = GND)



R_{DRIVE} = output impedance of the previous-stage IC



RDRIVE = output impedance of the previous-stage IC

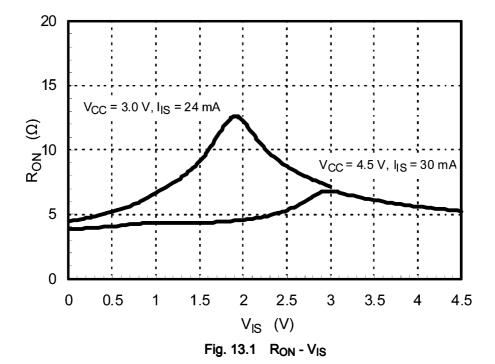
Fig. 12.1 Calculation Circuit

Table 12.1 Calculation Circuit

Characteristics	$V_{CC} = 5.0 \pm 0.5 \text{ V}$	$V_{CC} = 3.3 \pm 0.3 \text{ V}$	V_{CC} = 2.5 ± 0.2 V	V_{CC} = 1.8 \pm 0.15 V
V_{M}	V _{CC} /2	V _{CC} /2	V _{CC} /2	V _{CC} /2



13. Characteristics Curves (Note)

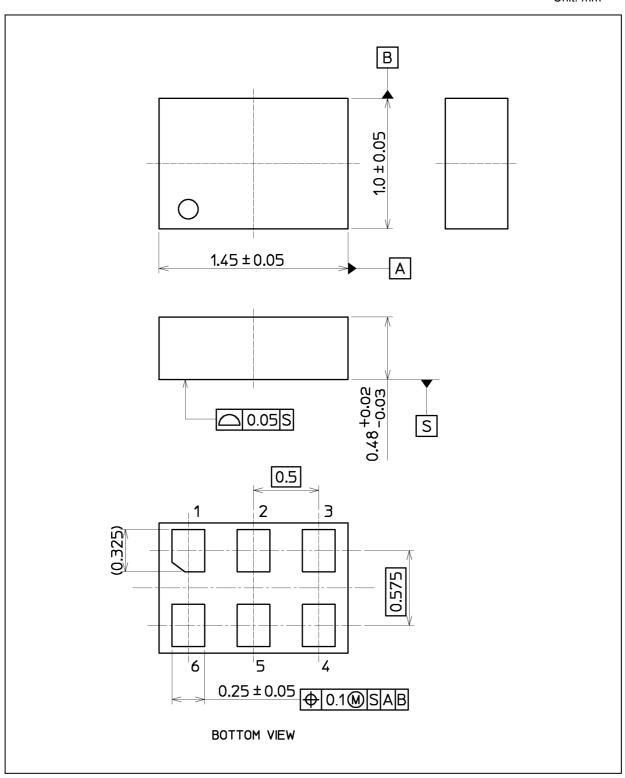


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Package Dimensions

Unit: mm



Weight: 0.002 g (typ.)

	Package Name(s)
Nickname: MP6D	



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