

1. General description

Enhanced ultrafast power diode in a SOT404 (D2PAK) surface-mountable plastic package.

2. Features and benefits

- High thermal cycling performance
- Low on-state losses
- Low thermal resistance
- Soft recovery characteristic
- Surface-mountable package

3. Applications

- Dual Mode (DCM and CCM) PFC
- Power Factor Correction (PFC) for Interleaved Topology

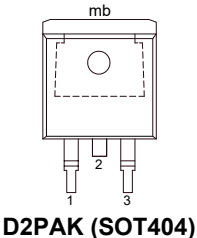
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V _R	reverse voltage	DC		-	-	600	V
I _{F(AV)}	average forward current	δ = 0.5 ; T _{mb} ≤ 115 °C; SQW; Fig. 1 ; Fig. 2		-	-	9	A
I _{FRM}	repetitive peak forward current	δ = 0.5 ; t _p = 25 μs; T _{mb} ≤ 115 °C; SQW		-	-	18	A
I _{FSM}	non-repetitive peak forward current	t _p = 10 ms; T _{j(init)} = 25 °C; SIN; Fig. 3		-	-	91	A
		t _p = 8.3 ms; T _{j(init)} = 25 °C; SIN; Fig. 3		-	-	100	A
Static characteristics							
V _F	forward voltage	I _F = 8 A; T _J = 25 °C; Fig. 5		-	1.45	1.9	V
		I _F = 8 A; T _J = 150 °C; Fig. 5		-	1.25	1.7	V
Dynamic characteristics							
t _{rr}	reverse recovery time	I _F = 1 A; V _R = 30 V; dI _F /dt = 100 A/μs; T _J = 25 °C; Fig. 6		-	17.5	35	ns

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	n.c.	no connection	 D2PAK (SOT404)	
2	K	cathode ^[1]		
3	A	anode		
mb	K	mounting base; cathode		

[1] It is not possible to connect to pin 2 of the SOT404 package.

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BYV29FB-600	D2PAK	plastic single-ended surface-mounted package (D2PAK); 3 leads (one lead cropped)	SOT404

7. Limiting values

Table 4. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{RRM}	repetitive peak reverse voltage		-	600	V
V _{RWM}	crest working reverse voltage		-	600	V
V _R	reverse voltage	DC	-	600	V
I _{F(AV)}	average forward current	δ = 0.5 ; T _{mb} ≤ 115 °C; SQW; Fig. 1; Fig. 2	-	9	A
I _{FRM}	repetitive peak forward current	δ = 0.5 ; t _p = 25 μs; T _{mb} ≤ 115 °C; SQW	-	18	A
I _{FSM}	non-repetitive peak forward current	t _p = 10 ms; T _{j(init)} = 25 °C; SIN; Fig. 3	-	91	A
		t _p = 8.3 ms; T _{j(init)} = 25 °C; SIN; Fig. 3	-	100	A
T _{stg}	storage temperature		-40	150	°C
T _j	junction temperature		-	150	°C

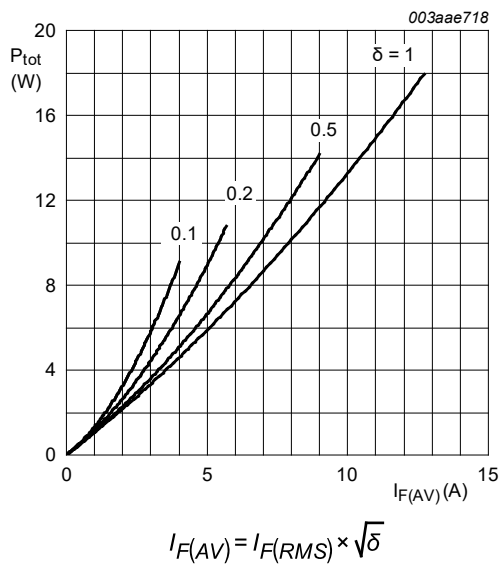


Fig. 1. Forward power dissipation as a function of average forward current; square waveform; maximum values

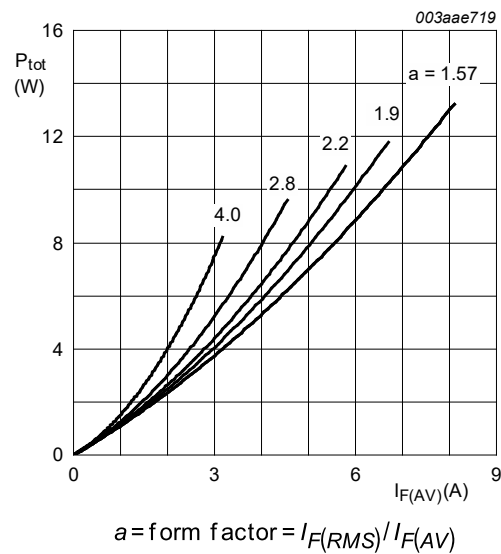
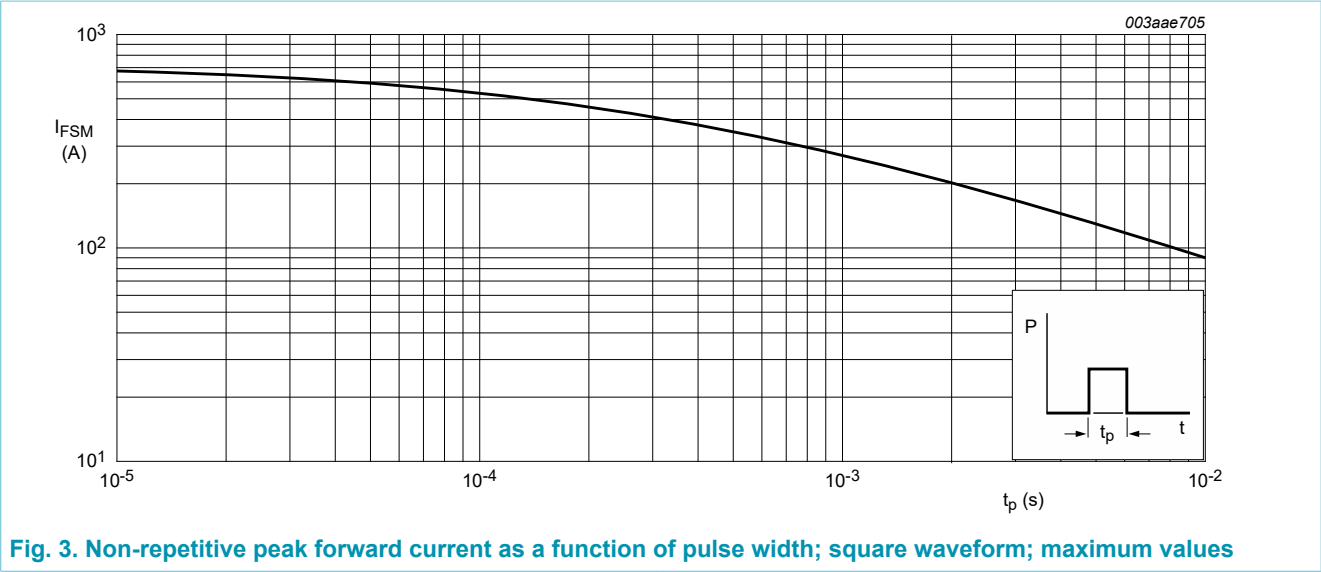


Fig. 2. Forward power dissipation as a function of average forward current; sinusoidal waveform; maximum values

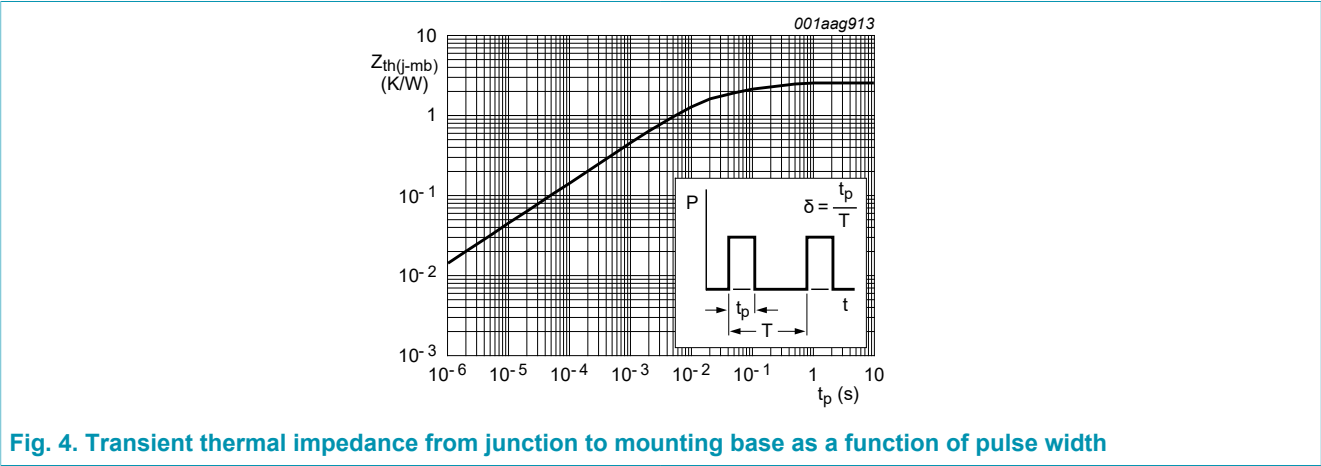


8. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	Fig. 4		-	-	2.5	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient free air	in free air	[1]	-	50	-	K/W

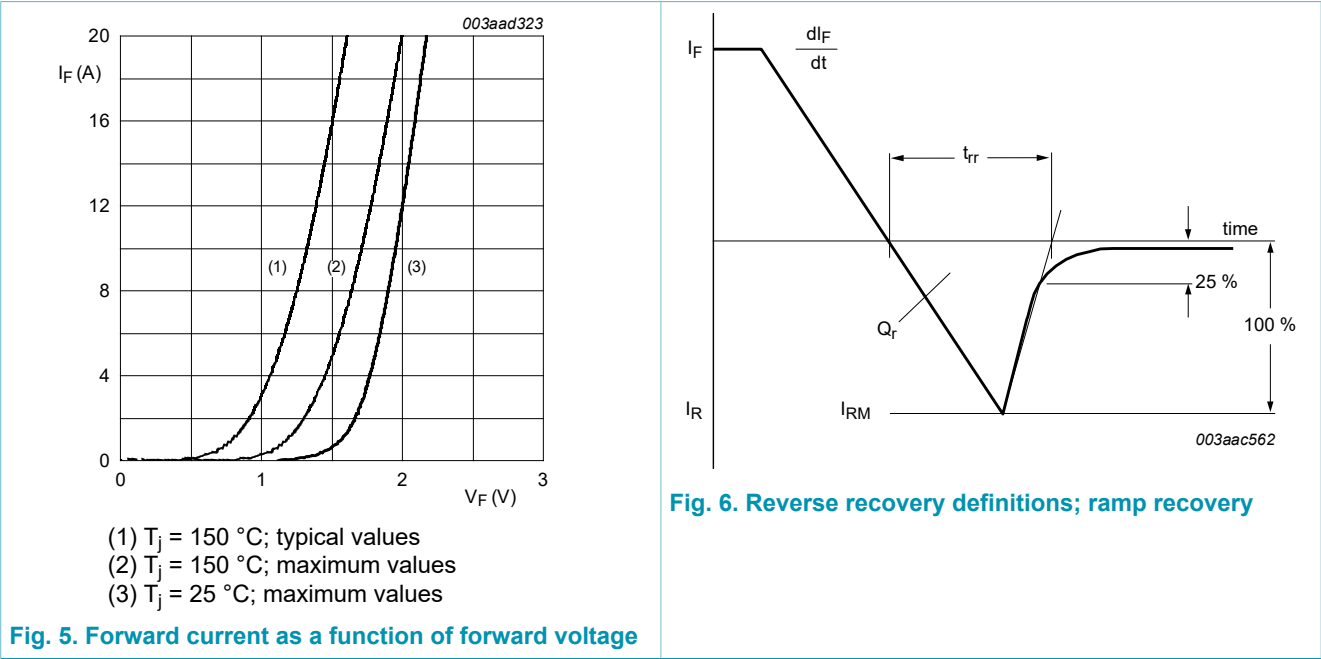
[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Static characteristics							
V _F	forward voltage	I _F = 8 A; T _j = 25 °C; Fig. 5		-	1.45	1.9	V
		I _F = 8 A; T _j = 150 °C; Fig. 5		-	1.25	1.7	V
I _R	reverse current	V _R = 600 V; T _j = 100 °C		-	-	1.5	mA
		V _R = 600 V; T _j = 25 °C		-	-	50	μA
Dynamic characteristics							
t _{rr}	reverse recovery time	I _F = 1 A; V _R = 30 V; dI _F /dt = 100 A/μs; T _j = 25 °C; Fig. 6		-	17.5	35	ns
I _{RM}	peak reverse recovery current	I _F = 1 A; V _R = 30 V; dI _F /dt = 100 A/μs		-	1.5	-	A
Q _r	recovered charge			-	13	-	nC
V _{FR}	forward recovery voltage	I _F = 1 A; dI _F /dt = 100 A/μs; Fig. 7		-	3.2	-	V



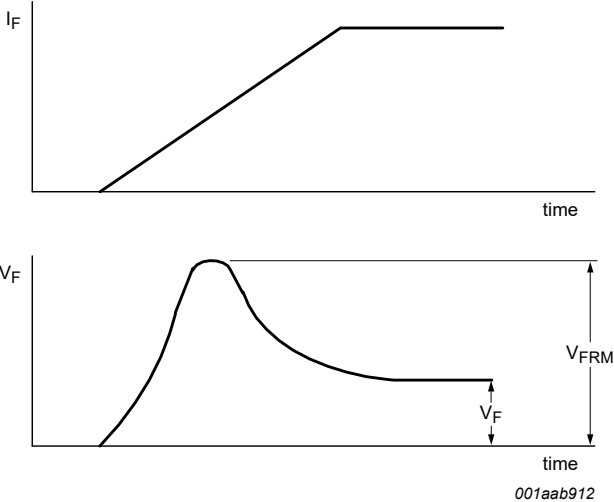


Fig. 7. Forward recovery definitions

10. Package outline

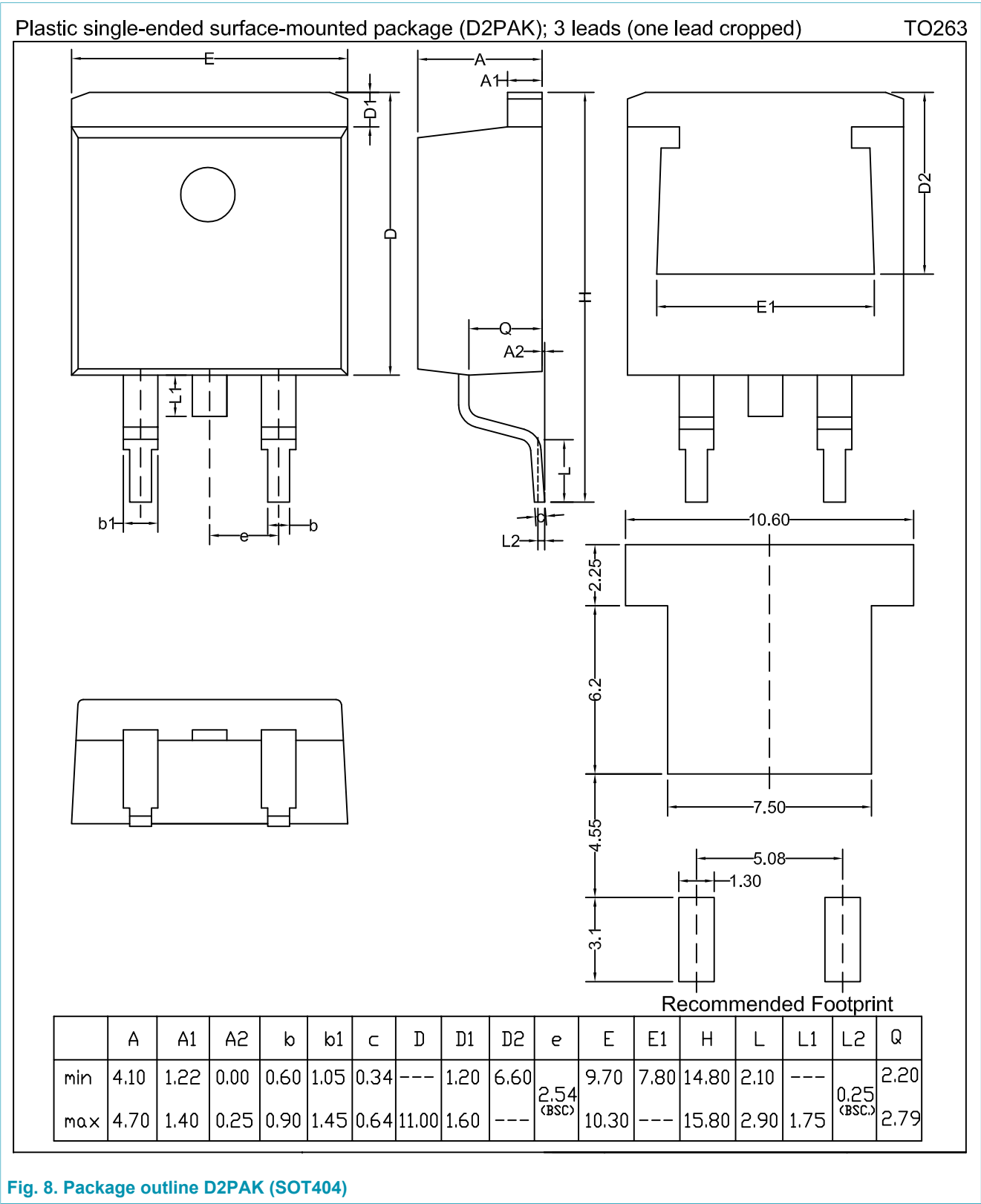


Fig. 8. Package outline D2PAK (SOT404)

11. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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