

Parameter	Rating	Units
Blocking Voltage	60	$V_{P}$
Load Current	750	mA
Load Current, Peak AC	1	А
On-Resistance (max)	0.6	Ω
LED Current to operate	2	mA

#### **Features**

Low On-Resistance: 0.6Ω
Low Drive Current: 2mA
High Load Current: 750mA
1500V<sub>rms</sub> Input/Output Isolation

100% Solid State

Compact 4-Pin SOP Package

Arc-Free With No Snubbing Circuits

No EMI/RFI Generation

Immune to Radiated EM Fields

• Tape & Reel Version Available

Flammability Rating UL 94 V-0

# **Applications**

Security

Passive Infrared Detectors (PIR)

Data Signalling

Sensor Circuitry

Instrumentation

Multiplexers

Data Acquisition

Electronic Switching

I/O Subsystems

Utility Meters (gas, oil, electric and water)

Medical Equipment—Patient/Equipment Isolation

Aerospace

Industrial Controls

ATE

# **Description**

CPC1019N is a miniature, low-voltage, low on-resistance, single-pole, normally-open (1-Form-A) solid state relay in a small 4-pin SOP package.

Embodying IXYS Integrated Circuits' patented OptoMOS technology, the CPC1019N comprises a highly efficient infrared LED that is optically coupled to efficient MOSFET output switches to provide 1500V<sub>rms</sub> of input-to-output isolation.

IXYS Integrated Circuits' state of the art double-molded vertical construction packaging produces a very compact solid state relay that is ideal for replacing larger, less-reliable reed and electromechanical relays.

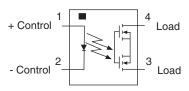
# **Approvals**

- UL Recognized Component: File E76270
- CSA Certified Component: Certificate 1172007
- EN/IEC 60950-1 Certified Component: Certificate available on our website

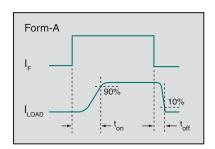
# Ordering Information

Part #	Description
CPC1019N	4-Pin SOP (100/tube)
CPC1019NTR	4-Pin SOP (2000/reel)

# **Pin Configuration**



# **Switching Characteristics** of Normally-Open Devices











# Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage	60	$V_{p}$
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	А
Input Power Dissipation 1	70	mW
Total Power Dissipation <sup>2</sup>	400	mW
Isolation Voltage, Input to Output	1500	$V_{\rm rms}$
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

Derate linearly 1.33 mW / °C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Typical values are characteristic of the device at +25°C, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

## Electrical Characteristics @ 25°C

Parameter	Conditions	Symbol	Min	Тур	Max	Units
Output Characteristics						
Load Current						
Continuous	1 Om A	1	-	-	750	mA <sub>DC</sub>
Continuous, AC Peak	I <sub>F</sub> =2mA	ı <sub>L</sub>	-	-	1	Α
Peak	t <u>&lt;</u> 10ms	I <sub>LPK</sub>	-	-	3	A <sub>P</sub>
On-Resistance <sup>1</sup>	I <sub>L</sub> =750mA	R <sub>ON</sub>	-	0.35	0.6	Ω
Off-State Leakage Current	$V_L = 60V_P$	I <sub>LEAK</sub>	-	-	1	μΑ
Switching Speeds						
Turn-On	I <sub>F</sub> =5mA, V <sub>L</sub> =10V	t <sub>on</sub>	-	0.4	3	ms
Turn-Off		t <sub>off</sub>	-	0.4	3	ms
Output Capacitance	I <sub>F</sub> =0mA, V <sub>L</sub> =50V, f=1MHz	C <sub>OUT</sub>	-	60	-	pF
Input Characteristics						
Input Control Current to Activate <sup>2</sup>	I <sub>L</sub> =750mA	I <sub>F</sub>	-	0.15	2	mA
Input Control Current to Deactivate	-	I <sub>F</sub>	0.1	0.14	-	mA
Input Voltage Drop	I <sub>F</sub> =5mA	$V_{F}$	0.9	1.2	1.5	V
Reverse Input Current	V <sub>R</sub> =5V	I <sub>R</sub>	-	-	10	μА
Common Characteristics				·		
Capacitance, Input to Output	V <sub>IO</sub> =0V, f=1MHz	$C_IO$	-	1	-	pF

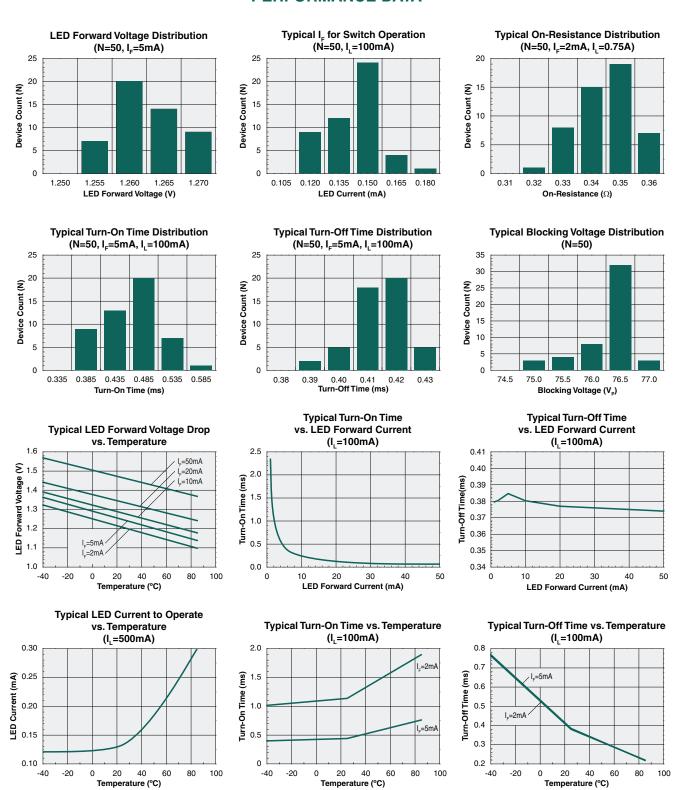
<sup>&</sup>lt;sup>1</sup> Measurement taken within 1 second of on-time.

<sup>&</sup>lt;sup>2</sup> Derate linearly 3.33 mW / °C

For applications requiring high temperature operation (greater than 60°C) a minimum LED drive current of 4mA is recommended.



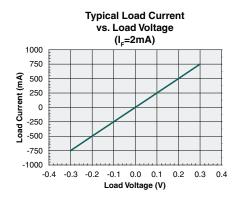
#### **PERFORMANCE DATA\***

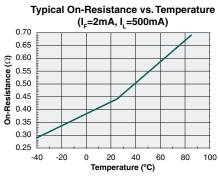


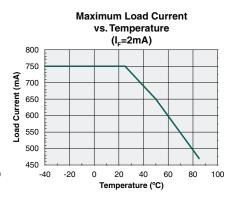
\*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C. For guaranteed parameters not indicated in the written specifications, please contact our application department.

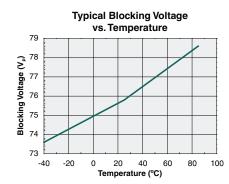


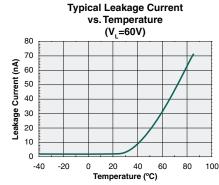
#### **PERFORMANCE DATA\***

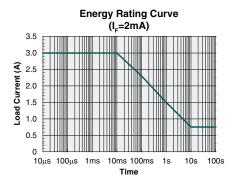














#### **Manufacturing Information**

#### **Moisture Sensitivity**

All plastic encapsulated semiconductor packages are susceptible to moisture ingression. IXYS Integrated Circuits classifies its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, IPC/JEDEC J-STD-020, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a Moisture Sensitivity Level (MSL) classification as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Classification
CPC1019N	MSL 3

#### **ESD Sensitivity**



This product is ESD Sensitive, and should be handled according to the industry standard JESD-625.

#### **Soldering Profile**

Provided in the table below is the Classification Temperature ( $T_C$ ) of this product and the maximum dwell time the body temperature of this device may be ( $T_C$  - 5)°C or greater. The classification temperature sets the Maximum Body Temperature allowed for this device during lead-free reflow processes. For through-hole devices, and any other processes, the guidelines of **J-STD-020** must be observed.

Device	Classification Temperature (T <sub>c</sub> )	Dwell Time (t <sub>p</sub> )	Max Reflow Cycles
CPC1019N	260°C	30 seconds	3

#### **Board Wash**

IXYS Integrated Circuits recommends the use of no-clean flux formulations. Board washing to reduce or remove flux residue following the solder reflow process is acceptable provided proper precautions are taken to prevent damage to the device. These precautions include, but are not limited to: using a low pressure wash and providing a follow up bake cycle sufficient to remove any moisture trapped within the device due to the washing process. Due to the variability of the wash parameters used to clean the board, determination of the bake temperature and duration necessary to remove the moisture trapped within the package is the responsibility of the user (assembler). Cleaning or drying methods that employ ultrasonic energy may damage the device and should not be used. Additionally, the device must not be exposed to flux or solvents that are Chlorine- or Fluorine-based.



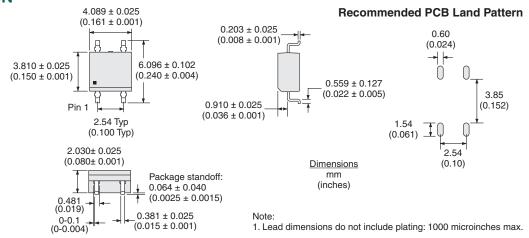




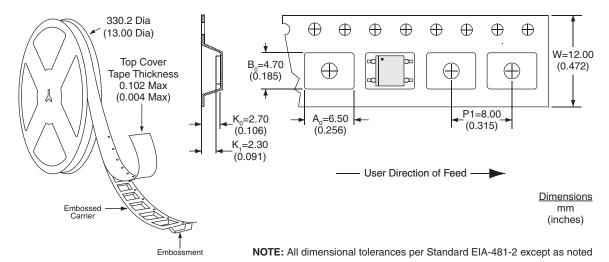


#### **MECHANICAL DIMENSIONS**

#### **CPC1019N**



# **CPC1019NTR Tape & Reel**



#### For additional information please visit our website at: www.ixysic.com

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### Офис по работе с юридическими лицами:

105318, г. Москва, ул. Щербаковская д. 3, офис 1107, 1118, ДЦ «Щербаковский»

Телефон: +7 495 668-12-70 (многоканальный)

Факс: +7 495 668-12-70 (доб.304)

E-mail: info@moschip.ru

Skype отдела продаж:

moschip.ru moschip.ru\_6 moschip.ru\_4 moschip.ru\_9