

# **BGU6009/N2**

# Low-noise amplifier MMIC for GPS, GLONASS, Galileo and COMPASS

Rev. 4 — 18 January 2017

**Product data sheet** 

## 1. Product profile

## 1.1 General description

The BGU6009/N2 is, also known as the GPS1002M, a Low-Noise Amplifier (LNA) for GNSS receiver applications in a plastic leadless 6-pin, very thin small outline SOT1230 package. The BGU6009/N2 requires only one external matching inductor and one external decoupling capacitor.

## 1.2 Features and benefits

- Covers full GNSS L1 band, from 1559 MHz to 1610 MHz
- Noise figure = 0.9 dB
- Gain 17 dB
- High input 1 dB compression point P<sub>i(1dB)</sub> of -4 dBm
- High out of band IP3<sub>i</sub> of 7 dBm
- Supply voltage 1.5 V to 3.1 V
- Power-down mode current consumption < 2 μA
- Optimized performance at low supply current of 5.1 mA
- Integrated matching for the output
- Requires only one input matching inductor and one supply decoupling capacitor
- Input and output DC decoupled
- ESD protection on all pins (HBM > 2 kV)
- Integrated temperature stabilized bias for easy design
- Small 6-pin leadless package 1.1 mm × 0.9 mm × 0.47 mm; 0.4 mm pitch: SOT1230

## 1.3 Applications

LNA for GPS, GLONASS, Gallileo and COMPASS (Beidou).

- Smart phones and feature phones
- Tablet PCs
- Personal Navigation Devices
- Digital Still Cameras
- Digital Video Cameras
- RF front-end modules
- Complete GPS chip set modules and theft protection (laptop, ATM)



## 1.4 Quick reference data

Table 1. Quick reference data

f = 1559 MHz to 1610 MHz;  $V_{CC}$  = 1.8 V;  $V_{I(ENABLE)} \ge 0.9$  V;  $P_i$  < -40 dBm;  $T_{amb}$  = 25 °C; input matched to 50  $\Omega$  using a 6.8 nH inductor; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CC</sub>	supply voltage	RF input AC coupled	1.5	1.8	3.1	V
I <sub>CC</sub>	supply current		-	5.1	-	mΑ
Gp	power gain	no jammer	-	17	-	dB
NF	noise figure	no jammer [1]	-	0.9	-	dB
P <sub>i(1dB)</sub>	input power at 1 dB	f = 1575 MHz				
	gain compression	V <sub>CC</sub> = 1.8 V	-	-7	-	dBm
		V <sub>CC</sub> = 2.85 V	-	-4	-	dBm
IP3 <sub>i</sub>	input third-order intercept point	f = 1575 MHz				
		V <sub>CC</sub> = 1.8 V [2]	-	5	-	dBm
		V <sub>CC</sub> = 2.85 V [2]	-	7	-	dBm

<sup>[1]</sup> PCB losses are subtracted.

## 2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Graphic symbol
1	GND		0.0
2	V <sub>CC</sub>	4 3	6 2
3	RF_OUT		5—3
4	GND_RF	5 2	Ţ ļ
5	RF_IN		1 4 aaa-006408
6	ENABLE	6 1	
		transparent top view	

## 3. Ordering information

Table 3. Ordering information

Type number	Package	Package			
	Name	Description	Version		
BGU6009/N2	XSON6	plastic very thin small outline package; no leads; 6 terminals; body 1.1 $\times$ 0.9 $\times$ 0.47 mm	SOT1230		

## 4. Marking

Table 4. Marking codes

Type number	Marking code
BGU6009/N2	W

<sup>[2]</sup>  $f_1 = 1713 \text{ MHz}$ ;  $f_2 = 1851 \text{ MHz}$ ;  $P_i = -20 \text{ dBm at } f_1$ ;  $P_i = -65 \text{ dBm at } f_2$ .

#### **Limiting values** 5.

Table 5. **Limiting values** 

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CC</sub>	supply voltage	RF input AC coupled		-0.5	+5.0	V
V <sub>I(ENABLE)</sub>	input voltage on pin ENABLE	V <sub>I(ENABLE)</sub> < V <sub>CC</sub> + 0.6 V	<u>[1]</u>	-0.5	+5.0	V
$V_{I(RF\_IN)}$	input voltage on pin RF_IN	DC; V <sub>I(RF_IN)</sub> < V <sub>CC</sub> + 0.6 V	[1][2]	-0.5	+5.0	V
V <sub>I(RF_OUT)</sub>	input voltage on pin RF_OUT	DC; V <sub>I(RF_OUT)</sub> < V <sub>CC</sub> + 0.6 V	[1][2]	-0.5	+5.0	V
Pi	input power			-	10	dBm
P <sub>tot</sub>	total power dissipation	T <sub>sp</sub> ≤ 130 °C	[3]	-	55	mW
T <sub>stg</sub>	storage temperature			-65	+150	°C
T <sub>j</sub>	junction temperature			-	150	°C
V <sub>ESD</sub>	electrostatic discharge voltage	Human Body Model (HBM) according to ANSI/ESDA/JEDEC standard JS-001		-	±2.5	kV
		Charged Device Model (CDM) according to JEDEC standard JESD22-C101		-	±1	kV

<sup>[1]</sup> Warning: due to internal ESD diode protection, the applied DC voltage should not exceed V<sub>CC</sub> + 0.6 V and shall not exceed 5.0 V to avoid excess current.

## Thermal characteristics

#### Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Тур	Unit
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		225	K/W

## **Characteristics**

#### Table 7. Characteristics at V<sub>CC</sub> = 1.8 V

f = 1559 MHz to 1610 MHz;  $V_{CC}$  = 1.8 V;  $V_{I(ENABLE)} \ge 0.9$  V;  $P_i$  < -40 dBm;  $T_{amb}$  = 25 °C; input matched to 50  $\Omega$  using a 6.8 nH inductor; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CC</sub>	supply voltage	RF input AC coupled	-	1.8	-	V
I <sub>CC</sub>	supply current	$V_{I(ENABLE)} \ge 0.9 \text{ V}$	-	5.1	-	mA
		$V_{I(ENABLE)} \le 0.3 \text{ V}$	-	-	2	μΑ
Gp	power gain	no jammer	-	17	-	dB
RLin	input return loss		-	12	-	dB
RL <sub>out</sub>	output return loss		-	11	-	dB
ISL	isolation		-	25	-	dB
NF	noise figure	no jammer [1]	-	0.9	-	dB
P <sub>i(1dB)</sub>	input power at 1 dB gain compression	f = 1575 MHz	-	-7	-	dBm
IP3 <sub>i</sub>	input third-order intercept point	f = 1575 MHz [2]	-	5	-	dBm

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The RF input and RF output are AC coupled through internal DC blocking capacitor.

T<sub>sp</sub> is the temperature at the soldering point of GND\_RF (pin 4).

## Table 7. Characteristics at V<sub>CC</sub> = 1.8 V ...continued

f = 1559 MHz to 1610 MHz;  $V_{CC}$  = 1.8 V;  $V_{I(ENABLE)} \ge 0.9$  V;  $P_i$  < -40 dBm;  $T_{amb}$  = 25 °C; input matched to 50  $\Omega$  using a 6.8 nH inductor; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
t <sub>on</sub>	turn-on time	[3]	-	-	2	μS
t <sub>off</sub>	turn-off time	[3]	-	-	1	μS
K	Rollett stability factor		1	-	-	-

- [1] PCB losses are subtracted.
- [2]  $f_1 = 1713 \text{ MHz}$ ;  $f_2 = 1851 \text{ MHz}$ ;  $P_i = -20 \text{ dBm at } f_1$ ;  $P_i = -65 \text{ dBm at } f_2$ .
- [3] Within 10 % of the final gain.

## Table 8. Characteristics at V<sub>CC</sub> = 2.85 V

f = 1559 MHz to 1610 MHz;  $V_{CC}$  = 2.85 V;  $V_{I(ENABLE)} \ge 0.9$  V;  $P_i$  < -40 dBm;  $T_{amb}$  = 25 °C; input matched to 50  $\Omega$  using a 6.8 nH inductor; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CC</sub>	supply voltage	RF input AC coupled	-	2.85	-	V
I <sub>CC</sub>	supply current	V <sub>I(ENABLE)</sub> ≥ 0.9 V	-	5.5	-	mA
		$V_{I(ENABLE)} \le 0.3 \text{ V}$	-	-	2	μΑ
G <sub>p</sub>	power gain	no jammer	-	17.5	-	dB
RLin	input return loss		-	13	-	dB
RLout	output return loss		-	11	-	dB
ISL	isolation		-	25	-	dB
NF	noise figure	no jammer [1]	-	0.95	-	dB
P <sub>i(1dB)</sub>	input power at 1 dB gain compression	f = 1575 MHz	-	-4	-	dBm
IP3 <sub>i</sub>	input third-order intercept point	f = 1575 MHz [2]	-	7	-	dBm
t <sub>on</sub>	turn-on time	[3]	-	-	2	μS
t <sub>off</sub>	turn-off time	[3]	-	-	1	μS
K	Rollett stability factor		1	-	-	-

- [1] PCB losses are subtracted.
- [2]  $f_1 = 1713 \text{ MHz}$ ;  $f_2 = 1851 \text{ MHz}$ ;  $P_i = -20 \text{ dBm at } f_1$ ;  $P_i = -65 \text{ dBm at } f_2$ .
- [3] Within 10 % of the final gain.

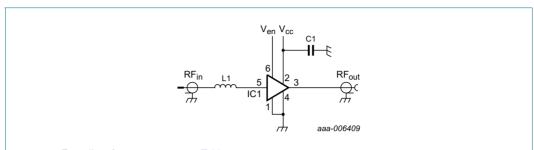
## Table 9. ENABLE (pin 6)

 $-40 \, ^{\circ}\text{C} \le T_{amb} \le +85 \, ^{\circ}\text{C}; \ 1.5 \, \text{V} \le \text{V}_{CC} \le 3.1 \, \text{V}.$ 

V <sub>I(ENABLE)</sub> (V)	State
≤ 0.3	OFF
≥ 0.9	ON

## 8. Application information

## 8.1 GNSS LNA



For a list of components, see <u>Table 10</u>.

Fig 1. Schematics GNSS LNA evaluation board

Table 10. List of components

For schematics, see Figure 1.

Component	Description	Value	Remarks
C1	decoupling capacitor	1 nF	
IC1	BGU6009/N2	-	NXP Semiconductors
L1	high-quality matching inductor	6.8 nH	Murata LQW15A

## 9. Package outline

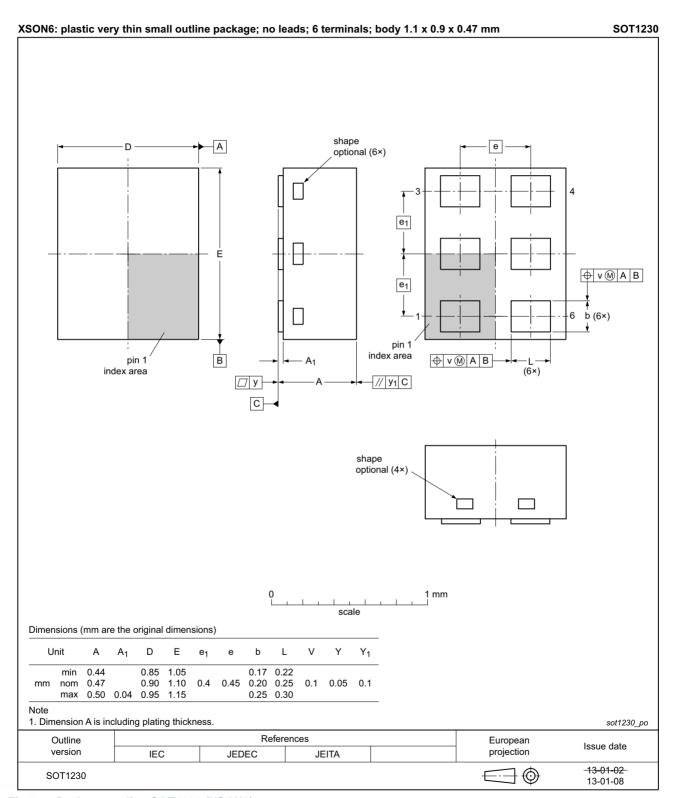


Fig 2. Package outline SOT1230 (XSON6)

## 10. Handling information

## **CAUTION**



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

## 11. Abbreviations

## Table 11. Abbreviations

Acronym	Description
ATM	Automated Teller Machine (cash dispenser)
ESD	ElectroStatic Discharge
GLONASS	GLObal NAvigation Satellite System
GNSS	Global Navigation Satellite System
GPS	Global Positioning System
HBM	Human Body Model
LNA	Low-Noise Amplifier
MMIC	Monolithic Microwave Integrated Circuit
PCB	Printed-Circuit Board

## 12. Revision history

#### Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BGU6009_N2 v.4	20170118	Product data sheet	-	BGU6009_N2 v.3
Modifications:	• Section 1: adde	d GPS1002M according to our ne	w naming convention	
BGU6009_N2 v.3	20160315	Product data sheet	-	BGU6009_N2 v.2
Modifications:	Marking code F changed into W			
BGU6009_N2 v.2	20141106	Product data sheet	-	BGU6009_N2 v.1
Modifications:	Table 1 on page	2: The value of the inductor has	been corrected.	
BGU6009_N2 v.1	20141001	Product data sheet	-	-

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## 13.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
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## Low-noise amplifier MMIC for GPS, GLONASS, Galileo and COMPASS

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